1200 V SiC MPS™ Diode

Silicon Carbide Schottky Diode



V _{RRM}	=	1200 V
I _{F (Tc = 135°C)}	=	44 A
Qc	=	79 nC

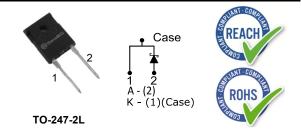
Features

- High Avalanche (UIS) Capability
- Enhanced Surge Current Capability
- Superior Figure of Merit Q_C/I_F
- Low Thermal Resistance
- 175 °C Maximum Operating Temperature
- Temperature Independent Switching Behavior
- Positive Temperature Coefficient of V_F
- Extremely Fast Switching Speeds

Advantages

- Low Standby Power Losses
- Improved Circuit Efficiency (Lower Overall Cost)
- Low Switching Losses
- Ease of Paralleling without Thermal Runaway
- Smaller Heat Sink Requirements
- Low Reverse Recovery Current
- Low Device Capacitance
- Low Reverse Leakage Current

Package



Applications

- Boost Diode in Power Factor Correction (PFC)
- Switched Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Motor Drives
- Freewheeling / Anti-parallel Diode in Inverters
- Solar Inverters
- Electric Vehicles (EV) & Charging Stations
- Induction Heating & Welding

Absolute Maximum Ratings (At T_c = 25 °C Unless Otherwise Stated)

Parameter	Symbol	Conditions Values		Unit	
Repetitive Peak Reverse Voltage	V _{RRM}		1200	V	
		T _C = 25 °C, D = 1	90		
Continuous Forward Current	I _F	T _C = 135 °C, D = 1	44	А	
		T _C = 164 °C, D = 1	20		
Non-Repetitive Peak Forward Surge Current, Half Sine Wave	1	T _C = 25 °C, t _P = 10 ms	120	٨	
	I _{F,SM}	T _C = 150 °C, t _P = 10 ms	96	A	
Repetitive Peak Forward Surge Current, Half Sine Wave	I _{F,RM}	T _C = 25 °C, t _P = 10 ms	82	А	
		T _C = 150 °C, t _P = 10 ms	55	A	
Non-Repetitive Peak Forward Surge Current	$I_{F,max}$	T _C = 25 °C, t _P = 10 μs	1100	А	
i ² t Value	∫i ² dt	T _C = 25 °C, t _P = 10 ms	72	A ² s	
Non-Repetitive Avalanche Energy	E _{AS}	L = 1.1 mH, I _{AS} = 20 A	220	mJ	
Diode Ruggedness	dV/dt	V _R = 0 ~ 960 V	100	V/ns	
Power Dissipation	P _{tot}	T _C = 25 °C	549	W	
Operating and Storage Temperature	T _j , T _{stg}		-55 to 175	°C	

1200 V SiC MPS™ Diode



Electrical Characteristics

Parameter	Symbol	Conditions		Values		llmit	
Farameter	Symbol			Min.	Тур.	Max.	Unit
Diode Forward Voltage	V _F	I _F = 20 A, T _j =	I _F = 20 A, T _j = 25 °C		1.5	1.8	V
	VF	I _F = 20 A, T _j = 175 °C			2	2.4	
Reverse Current	I_	V _R = 1200 V, T _j = 25 °C			1.8	18	μA
	I _R	V _R = 1200 V, T _j = 175 °C			5.4	64.8	
Total Capacitive Charge	0		V _R = 400 V		54	nC	
	Q _C	l _F ≤ I _{F,MAX} − dI _F /dt = 200 A/μs	V _R = 800 V		79		
Switching Time	+	$T_j = 175 \text{°C}$	V _R = 400 V		< 10		ns
	t _s		V _R = 800 V		< 10		
Total Capacitance	С	V_R = 1 V, f = 1 MHz, T _j = 25 °C			1298		pF
	U	V_R = 800 V, f = 1 MHz, T _j = 25 °C			99		

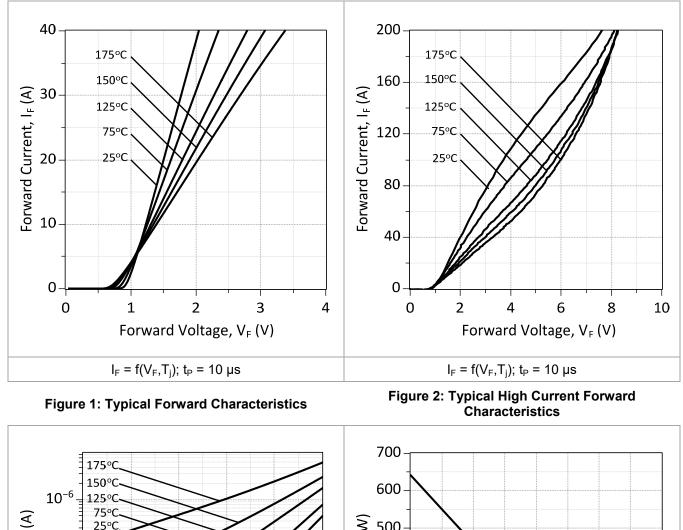
Thermal / Mechanical Characteristics

Thermal Resistance, Junction - Case	R _{thJC}	0.23	°C/W
Weight	W _T	6	g
Mounting Torque	T _M	1.1	Nm









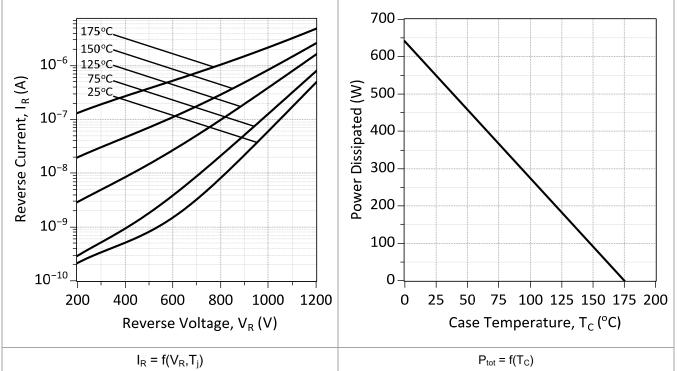
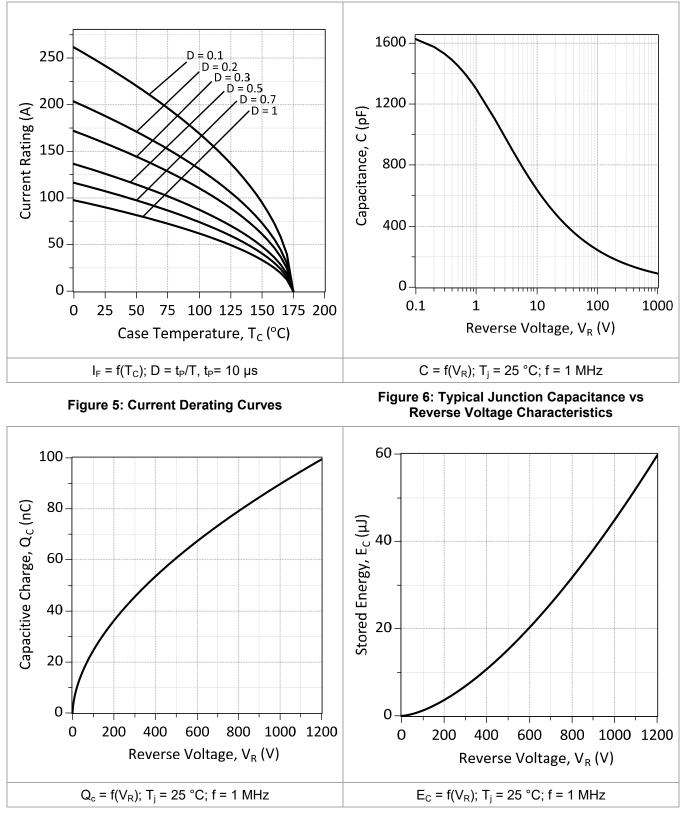


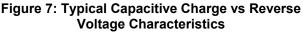


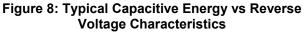
Figure 4: Power Derating Curve

1200 V SiC MPS[™] Diode









1200 V SiC MPS[™] Diode



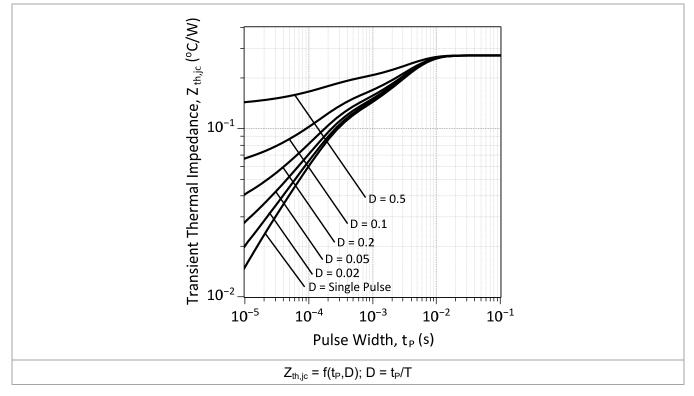
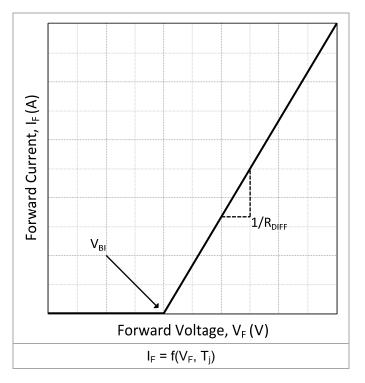
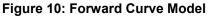


Figure 9: Transient Thermal Impedance





 $I_{F} = (V_{F} - V_{BI})/R_{DIFF}(A)$

Built-In Voltage (V_{BI}):

$$V_{Bl}(T_j) = m^*T_j + n (V)$$

m = -1.60e-03, n = 1.01

Differential Resistance (R_{DIFF}):

$$R_{DIFF}(T_j) = a^*T_j^2 + b^*T_j + c(\Omega);$$

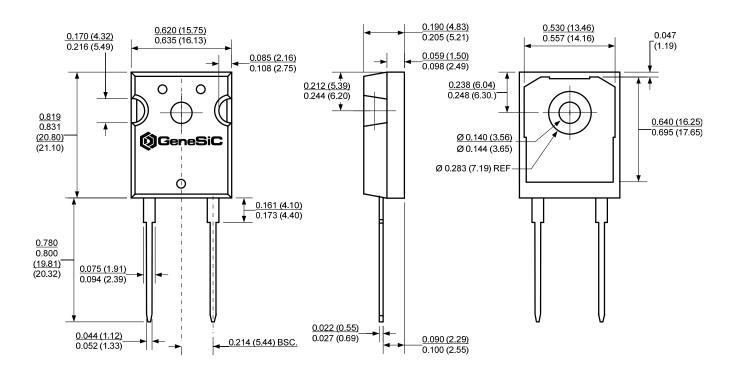
1200 V SiC MPS™ Diode



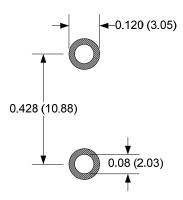
Package Dimensions

TO-247-2L

Package Outline



Recommended Solder Pad Layout



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS

1200 V SiC MPS[™] Diode



RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS), as implemented November 15, 2017. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.

GeneSiC disclaims all and any warranty and liability arising out of use or application of any product. No license, express or implied to any intellectual property rights is granted by this document.

Related Links

- Soldering Document: http://www.genesicsemi.com/quality/quality-manual/
- Tin-whisker Report: http://www.genesicsemi.com/quality/compliance/
- Reliability Report: http://www.genesicsemi.com/quality/reliability/



Copyright © 2018 GeneSiC Semiconductor Inc. All Rights Reserved The information in this document is subject to change without notice

Published by GeneSiC Semiconductor, Inc. 43670 Trade Center Place Suite 155 Dulles, VA 20166