



## Final Product Change Notification

202007035F01

**Issue Date:** 05-Aug-2020

**Effective Date:** 02-Nov-2020

Dear *Gordon Love*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)

**This notice is NXP Company Proprietary.**



### Change Category

- |  |   |   |   |   |
|--|---|---|---|---|
| <input type="checkbox"/> Wafer Fab Process   | <input type="checkbox"/> Assembly Process   | <input checked="" type="checkbox"/> Product Marking | <input type="checkbox"/> Test Location  | <input checked="" type="checkbox"/> Design              |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification   | <input type="checkbox"/> Test Process   | <input type="checkbox"/> Errata                         |
| <input type="checkbox"/> Wafer Fab Location  | <input type="checkbox"/> Assembly Location  | <input type="checkbox"/> Packing/Shipping/Labeling  | <input type="checkbox"/> Test Equipment | <input type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware            | <input type="checkbox"/> Other              |   |   |   |

i.MX RT1060 New Silicon Revision With New Part Number



### Description of Change

NXP Semiconductors is announcing i.MX RT1060 maskset revision 1N00X which has been qualified with new part number MIMXRT106xxxxxB.

The new mask revision has the below improvement:

Fixed ERR050101 USB: Endpoint conflict issue in device mode. An endpoint conflict occurs when the USB is working in device mode during an isochronous communication.

Please contact your NXP sales or marketing representative for additional information.

### Reason for Change

The release of i.MX RT1060 new maskset 1N00X fixed ERR050101 USB: Endpoint conflict issue in device mode.

### Identification of Affected Products

Top side marking

Please refer to enclosed attachment

## Product Availability

### Sample Information

Samples are available from 31-Aug-2020

Below are the sample part numbers that will be made available.

MIMXRT1061DVL6B  
MIMXRT1062DVL6B  
MIMXRT1061CVJ5B  
MIMXRT1062DVJ6B  
MIMXRT1061CVL5B  
MIMXRT1062CVL5B

### Production

Planned first shipment 31-Dec-2020

### Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

### Disposition of Old Products

Existing maskset revision 0N00X inventory will be shipped until depleted.

### Additional information

Affected products and sales history information: see attached file

Self qualification: [view online](#)

Additional documents: [view online](#)

### Timing and Logistics

In compliance with JEDEC J-STD-046, your acknowledgement of this change is expected by 04-Sep-2020.

### Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

**Name** Melisa Shen  
**Position** Product Marketing Manager  
**e-mail address** [melisa.shen@nxp.com](mailto:melisa.shen@nxp.com)

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

NXP Quality Management Team.

### About NXP Semiconductors

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