Customer Information Notification

202304028I : S32G3 Errata Update from Rev2 to Rev2.1

Note: This notice is NXP Company Proprietary.

Issue Date: May 09, 2023 **Effective date:** May 10, 2023

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[ ]Wafer Fab Process	[ ]Assembly Process	[ ]Product Marking	[ ]Test Process	[ ]Design
[ ]Wafer Fab Materials	[ ]Assembly Materials	[ ]Mechanical Specification	[ ]Test Equipment	[X]Errata
[ ]Wafer Fab Location	[ ]Assembly Location	[ ]Packing/Shipping/Labeling	[ ]Test Location	[ ]Electrical spec./Test coverage
[ ]Firmware	[ ]Other			

PCN Overview
Description

NXP Semiconductors announces errata update for the S32G3 mask set 1P72B from revision 2.0 to revision 2.1. The errata document provides a detailed description of the changes in history revision.

The S32G3 mask set 1P72B errata revision 2.1 is attached to this notice and can be found at:
https://www.nxp.com/products/processors-and-microcontrollers/s32-automotive-platform/s32g-vehicle-network-processors/s32g3-processors-for-vehicle-networking:S32G3#documentation

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02
Reason
The Errata have been updated to provide additional technical clarification on some device features.
Identification of Affected Products
Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality
Additional information

Additional documents: view online

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name	NXP Tech Support
Position	NXP Technical Support
e-mail address	tech.support@nxp.com

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NXP Quality Management Team.
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