



Line driver and receivers

Order code	Manufacturer code	Description
82-0132	n/a	MAX220CSE +5V PWRD M CHNL RS232 DRV/RCV
82-0134	n/a	n/a
82-0138	n/a	n/a
82-0142	n/a	MAX232ACPE+ +5V PWRD M CHNL RS232 (RC)
82-0144	n/a	MAX232ACSE +5V M CHNL RS232 DRV/RCV (SMD
82-0146	n/a	MAX232ACWE +5V PWRD M CHNL RS232 DRV/RCV
82-0148	n/a	MAX232CPE+ CMOS DUAL RS232TRANS/RCV RC
82-0150	n/a	MAX232CPP +5V PWRD M CHNL RS232 DRV/RCV
82-0152	n/a	MAX232CSE+ DRIVER/RECEIVER (RC)
82-0154	n/a	MAX232CWE CMOS DUAL RS232 TRANS/REC(SMD
82-0156	n/a	MAX236CNG +5V PWRD M CHNL RS232 DRV/RCV
82-0158	n/a	MAX236CWG +5V PWRD M CHNL RS232 DRV/RCV
82-0160	n/a	MAX238CWG +5V PWRD M CHNL RS232 RCV/DRV

 Line driver and receivers
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 The enclosed information is believed to be correct, Information may change 'without notice' due to product improvement. Users should ensure that the product is suitable for their use. E. & O. E.
 Revision A

Technical: 01206 835555 Tech@rapidelec.co.uk Fax: 01206 7551188 www.rapidelectronics.co.uk

General Description

The MAX220-MAX249 family of line drivers/receivers is intended for all EIA/TIA-232E and V.28/V.24 communications interfaces, and in particular, for those applications where $\pm 12V$ is not available.

These parts are particularly useful in battery-powered systems, since their low-power shutdown mode reduces power dissipation to less than 5 μ W. The MAX225, MAX233, MAX235, and MAX245-MAX247 use no external components and are recommended for applications where printed circuit board space is critical.

Applications

Portable Computers Low-Power Modems

Interface Translation

Battery-Powered RS-232 Systems

Multi-Drop RS-232 Networks

Superior to Bipolar

- Operate from Single +5V Power Supply (+5V and +12V—MAX231/MAX239)
- Low-Power Receive Mode in Shutdown (MAX223/MAX242)
- Meet All EIA/TIA-232E and V.28 Specifications
- Multiple Drivers and Receivers
- ♦ 3-State Driver and Receiver Outputs
- Open-Line Detection (MAX243)

__Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX220CPE	0°C to +70°C	16 Plastic DIP
MAX220CSE	0°C to +70°C	16 Narrow SO
MAX220CWE	0°C to +70°C	16 Wide SO
MAX220C/D	0°C to +70°C	Dice*
MAX220EPE	-40°C to +85°C	16 Plastic DIP
MAX220ESE	-40°C to +85°C	16 Narrow SO
MAX220EWE	-40°C to +85°C	16 Wide SO
MAX220EJE	-40°C to +85°C	16 CERDIP
MAX220MJE	-55°C to +125°C	16 CERDIP

Ordering Information continued at end of data sheet. *Contact factory for dice specifications.

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Part	Power Supply	No. of RS-232	No. of	Nominal Cap. Value	SHDN & Three-	Rx Active in	Data Rate	
Number	(V)	Drivers/Rx	Ext. Caps	(µF)	State	SHDN	(kbps)	Features
MAX220	+5	2/2	4	4.7/10	No		120	Ultra-low-power, industry-standard pinout
MAX222	+5	2/2	4	0.1	Yes		200	Low-power shutdown
MAX223 (MAX213)	+5	4/5	4	1.0 (0.1)	Yes	~	120	MAX241 + receivers active in shutdown
MAX225	+5	5/5	0	-	Yes	~	120	Available in SO
MAX230 (MAX200)	+5	5/0	4	1.0 (0.1)	Yes		120	5 drivers with shutdown
MAX231 (MAX201)	+5 and +7.5 to +13.2	2/2	2	1.0 (0.1)	No		120	Standard +5/+12V or battery supplies;
		0.0		10(04)				same functions as MAX232
MAX232 (MAX202) MAX232A	+5	2/2	4	1.0 (0.1)	No		120 (64)	Industry standard
	+5	2/2	4	0.1	No		200	Higher slew rate, small caps
MAX233 (MAX203)	+5	2/2	0	-	No		120	No external caps
MAX233A	+5	2/2	0		No		200	No external caps, high slew rate
MAX234 (MAX204)		4/0	4	1.0 (0.1)	No		120	Replaces 1488
MAX235 (MAX205)	+5	5/5	0	-	Yes		120	No external caps
MAX236 (MAX206)		4/3	4	1.0 (0.1)	Yes		120	Shutdown, three state
		5/3	4	1.0 (0.1)	No		120	Complements IBM PC serial port
MAX238 (MAX208)		4/4	4	1.0 (0.1)	No		120	Replaces 1488 and 1489
MAX239 (MAX209)	+5 and	3/5	2	1.0 (0.1)	No		120	Standard +5/+12V or battery supplies;
	+7.5 to +13.2							single-package solution for IBM PC serial port
MAX240	+5	5/5	4	1.0	Yes		120	DIP or flatpack package
MAX241 (MAX211)	+5	4/5	4	1.0 (0.1)	Yes		120	Complete IBM PC serial port
MAX242	+5	2/2	4	0.1	Yes	~	200	Separate shutdown and enable
MAX243	+5	2/2	4	0.1	No		200	Open-line detection simplifies cabling
MAX244	+5	8/10	4	1.0	No		120	High slew rate
MAX245	+5	8/10	0	-	Yes	~	120	High slew rate, int. caps, two shutdown modes
MAX246	+5	8/10	0	-	Yes	~	120	High slew rate, int. caps, three shutdown modes
MAX247	+5	8/9	0	-	Yes	~	120	High slew rate, int. caps, nine operating modes
MAX248	+5	8/8	4	1.0	Yes	~	120	High slew rate, selective half-chip enables
MAX249	+5	6/10	4	1.0	Yes	~	120	Available in quad flatpack package

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Features

ABSOLUTE MAXIMUM RATINGS—MAX220/222/232A/233A/242/243

Supply Voltage (V _{CC})0.3V to +6V
Input Voltages
T _{IN} 0.3V to (V _{CC} - 0.3V)
R _{IN} ±30V
TOUT (Note 1)±15V
Output Voltages
Tout±15V
Rout0.3V to (V _{CC} + 0.3V)
Driver/Receiver Output Short Circuited to GNDContinuous
Continuous Power Dissipation ($T_A = +70^{\circ}C$)
16-Pin Plastic DIP (derate 10.53mW/°C above +70°C)842mW
18-Pin Plastic DIP (derate 11.11mW/°C above +70°C)889mW
20-Pin Plastic DIP (derate 8.00mW/°C above +70°C)440mW

16-Pin Narrow SO (derate 8.70mW/°C above +70°C)696mW
16-Pin Wide SO (derate 9.52mW/°C above +70°C)762mW
18-Pin Wide SO (derate 9.52mW/°C above +70°C)762mW
20-Pin Wide SO (derate 10.00mW/°C above +70°C)800mW
20-Pin SSOP (derate 8.00mW/°C above +70°C)640mW
16-Pin CERDIP (derate 10.00mW/°C above +70°C)800mW
18-Pin CERDIP (derate 10.53mW/°C above +70°C)842mW
Operating Temperature Ranges
MAX2AC, MAX2C0°C to +70°C
MAX2AE, MAX2E40°C to +85°C
MAX2_AM, MAX2_M55°C to +125°C
Storage Temperature Range65°C to +160°C

Lead Temperature (soldering, 10sec)+300°C

Note 1: Input voltage measured with T_{OUT} in high-impedance state, \overline{SHDN} or $V_{CC} = 0V$.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—MAX220/222/232A/233A/242/243

(V_{CC} = +5V ±10%, C1-C4 = 0.1 μ F, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER		CONDITIONS	MIN	ТҮР	MAX	UNITS
RS-232 TRANSMITTERS	4					
Output Voltage Swing	All transmitter o	utputs loaded with $3k\Omega$ to GND	±5	±8		V
Input Logic Threshold Low				1.4	0.8	V
Input Logic Threshold High			2	1.4		V
	Normal operation	n		5	40	
Logic Pull-Up/Input Current	SHDN = 0V, MA	X222/242, shutdown		±0.01	±1	μA
	V _{CC} = 5.5V, SH	DN = 0V, V _{OUT} = ±15V, MAX222/242		±0.01	±10	
Output Leakage Current	V _{CC} = SHDN =	$0V, V_{OUT} = \pm 15V$		±0.01	±10	μA
Data Rate	Except MAX220, normal operation			200	116	kbits/
Data Rate	MAX220			22	20	sec
Transmitter Output Resistance	$V_{CC} = V_{+} = V_{-} = 0V, V_{OUT} = \pm 2V$			10M		Ω
Output Short-Circuit Current	V _{OUT} = 0V			±22		mA
RS-232 RECEIVERS						
RS-232 Input Voltage Operating Range					±30	V
RS-232 Input Voltage Operating Hange	$V_{CC} = 5V$	Except MAX243 R2IN	0.8	1.3		v
RS-232 Input Theshold Low	VCC = 5V	MAX243 R2IN (Note 2)	-3			1 1
		Except MAX243 R2IN		1.8	2.4	v
RS-232 Input Threshold High	$V_{CC} = 5V$	MAX243 R2IN (Note 2)		-0.5	-0.1	1 ^v
	Except MAX243	3, V _{CC} = 5V, no hyst. in shdn.	0.2	0.5	1	v
RS-232 Input Hysteresis	MAX243			1		
RS-232 Input Resistance			3	5	7	kΩ
TTL/CMOS Output Voltage Low	I _{OUT} = 3.2mA			0.2	0.4	V
TTL/CMOS Output Voltage High	IOUT = -1.0mA		3.5	Vcc - 0.2		V
TH /CMOS Output Shart Circuit Current	Sourcing Vout = GND			-10		
TTL/CMOS Output Short-Circuit Current	Shrinking Vout	= V _{CC}	10	30		- mA
TTL/CMOS Output Leakage Current	$\overline{SHDN} = V_{CC} \text{ or}$ $0V \le V_{OUT} \le V_{C}$	$\overline{EN} = V_{CC} (\overline{SHDN} = 0V \text{ for MAX222}),$		±0.05	±10	μA

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ELECTRICAL CHARACTERISTICS—MAX220/222/232A/233A/242/243 (continued) (V_{CC} = +5V \pm 10%, C1-C4 = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.) CONDITIONS MAX UNITS PARAMETER MIN TYP EN Input Threshold Low MAX242 1.4 0.8 ٧ MAX242 2.0 1.4 ۷ EN Input Threshold High POWER SUPPLY 4.5 V 5.5 **Operating Supply Voltage** MAX220 0.5 2 No load MAX222/232A/233A/242/243 10 V_{CC} Supply Current (SHDN = V_{CC}), 4 mΑ Figures 5, 6, 9, 19 MAX220 12 $3k\Omega$ load both inputs MAX222/232A/233A/242/243 15 $T_A = +25^{\circ}C$ 0.1 10 $T_A = 0^\circ$ to +70°C 2 50 Shutdown Supply Current MAX222/242 μA $T_A = -40^\circ$ to $+85^\circ$ C 2 50 TA = -55° to +125°C 35 100 SHDN Input Leakage Current MAX222/242 ±1 μA SHDN Threshold Low MAX222/242 1.4 0.8 ٧ SHDN Threshold High MAX222/242 2.0 1.4 V AC CHARACTERISTICS C₁ = 50pF to 2500pF MAX222/232A/233A/242/243 6 12 30 $R_L = 3k\Omega$ to $7k\Omega$, $V_{CC} = 5V, T_A = +25^{\circ}C$ V/us **Transition Slew Rate** measured from +3V MAX220 1.5 3 30 to -3V or -3V to +3V MAX222/232A/233A/242/243 1.3 3.5 **TPHLT** Transmitter Propagation Delay **MAX220** 4 10 TLL to RS-232 (normal operation), μs MAX222/232A/233A/242/243 1.5 3.5 Figure 1 **TPLHT** MAX220 5 10 MAX222/232A/233A/242/243 0.5 1 **TPHLR** Receiver Propagation Delay **MAX220** 0.6 3 RS-232 to TLL (normal operation), μs MAX222/232A/233A/242/243 0.6 1 Figure 2 **TPLHR MAX220** 0.8 3 MAX242 0.5 10 **Receiver Propagation Delay tPHLS** μs RS-232 to TLL (shutdown), Figure 2 MAX242 10 2.5 **tPLHS** MAX242 125 500 Receiver-Output Enable Time, Figure 3 ter ns MAX242 160 500 Receiver-Output Disable Time, Figure 3 ns t_{DR} Transmitter-Output Enable Time MAX222/242, 0.1µF caps 250 μs ter (includes charge-pump start-up) (SHDN goes high), Figure 4 Transmitter-Output Disable Time MAX222/242, 0.1µF caps 600 ns tor (SHDN goes low), Figure 4 MAX222/232A/233A/242/243 300 Transmitter + to - Propagation **THULT - THULHT** ns Delay Difference (normal operation) MAX220 2000 Receiver + to - Propagation MAX222/232A/233A/242/243 100 ns tPHLR - TPLHR Delay Difference (normal operation) MAX220 225

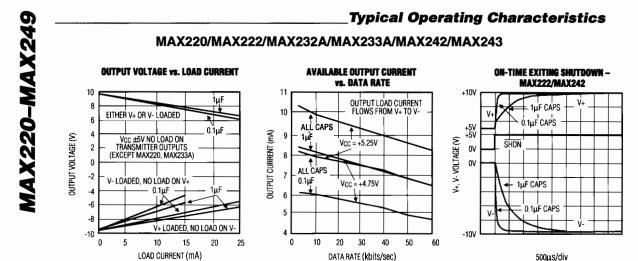
Note 2: MAX243 R2_{OUT} is guaranteed to be low when R2_{IN} is ≥ 0V or is floating.

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MAX220-MAX249

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500µs/div

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ABSOLUTE MAXIMUM RATINGS—MAX223/MAX230-MAX241

V _{CC} 0.3V to +6V V+(V _{CC} - 0.3V) to +14V V+0.3V to -14V
Input Voltages
TIN0.3V to (V _{CC} + 0.3V) RIN
Output Voltages
Tout(V+ + 0.3V) to (V 0.3V)
ROUT0.3V to (V _{CC} + 0.3V)
Short-Circuit Duration, TOUTContinuous
Continuous Power Dissipation ($T_A = +70^{\circ}C$)
14-Pin Plastic DIP (derate 10.00mW/°C above +70°C)800mW
16-Pin Plastic DIP (derate 10.53mW/°C above +70°C)842mW
20-Pin Plastic DIP (derate 11.11 mW/°C above +70°C)889mW
24-Pin Narrow Plastic DIP
24-Pin Narrow Plastic DIP (derate 13.33mW/°C above +70°C)

20-Pin Wide SO (derate 10 00mW/°C above +70°C)......800mW 24-Pin Wide SO (derate 11.76mW/°C above +70°C)......941mW 28-Pin Wide SO (derate 12.50mW/°C above +70°C)......941mW 44-Pin Plastic FP (derate 11.11 mW/°C above +70°C)......889mW 14-Pin CERDIP (derate 0.09mW/°C above +70°C)......800mW 16-Pin CERDIP (derate 10.00mW/°C above +70°C)......800mW 20-Pin CERDIP (derate 11.11mW/°C above +70°C)......889mW 24-Pin Narrow CERDIP

 (derate 12.50mW/°C above +70°C)
 1W

 24-Pin Sidebraze (derate 20.0mW/°C above +70°C)
 1.6W

 28-Pin SSOP (derate 9.52mW/°C above +70°C)
 762mW

 Operating Temperature Ranges
 MAX2 _ _ C _ ______0°C to +70°C

 MAX2 _ _ E _ ______40°C to +85°C
 40°C to +85°C

 MAX2 _ _ M _ _
 -55°C to +125°C

 Storage Temperature Range
 -65°C to +160°C

Lead Temperature (soldering, 10sec)+300°C

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ELECTRICAL CHARACTERISTICS-MAX223/MAX230-MAX241

 $(MAX223/230/232/234/236/237/238/240/241 V_{CC} = +5V \pm 10\%, MAX233/MAX235 V_{CC} = 5V \pm 5\%, C1-C4 = 1.0 \mu F MAX231/MAX239 V_{CC} = 5V \pm 10\%, V+ = 7.5V to 13.2V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)$

PARAMETER		CONDITIONS			MAX	UNITS
Output Voltage Swing	All transmitter of	butputs loaded with $3k\Omega$ to ground	±5.0	±7.3		V
		MAX232/233		5	10	
VCC Power-Supply Current	No load, T _A = +25°C	MAX223/230/234-238/240/241		7	15	mA
	14 - 120 0	MAX231 /239		.4	1	1
		MAX231		1.8	5	-
V+ Power-Supply Current		MAX239		5	15	mA
	T. 05%0	MAX223		15	50	
Shutdown Supply Current	T _A = +25°C	MAX230/235/236/240/241		1	10	μA
Input Logic Threshold Low	TIN; EN, SHDN	(MAX223), EN, SHDN (MAX230/235-241)			0.8	V
	TIN		2.0			
Input Logic Threshold High	EN, <u>SHDN</u> (MA EN, SHDN (MA	,X223), ,X230/235/236/240/241)	2.4			V
Logic Pull-Up Current	T _{IN} = 0V			1.5	200	μA
Receiver Input Voltage Operating Range			-30		30	v

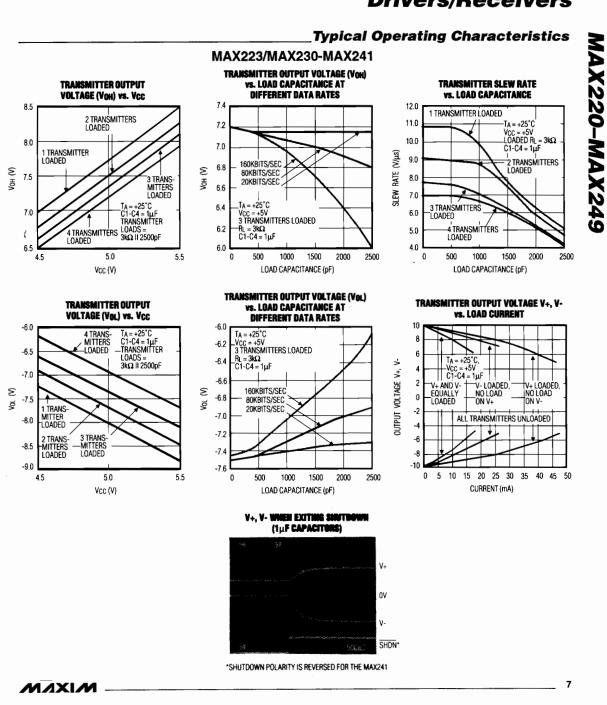
MAX220-MAX249

ELECTRICAL CHARACTERISTICS—MAX223/MAX230-MAX241 (continued)

 $(MAX223/230/232/234/236/237/238/240/241 V_{CC} = +5V \pm 10\%, MAX233/MAX235 V_{CC} = 5V \pm 5\%, C1-C4 = 1.0 \mu F MAX231/MAX239 V_{CC} = 5V \pm 10\%, V+ = 7.5V to 13.2V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)$

PARAMETER	CONDITIONS		MIN	ТҮР	MAX	UNITS	
RS-232 Input Threshold Low	T _A = +25°C,	T _A = +25°C, Normal operation SHDN = 5V (MAX223) SHDN = 0V (MAX235)		0.8	1.2		v
HS-232 input miesnola Low	V _{CC} = 5V	Shutdown (MAX22 SHDN = 0V, EN = 5V (R4 _{IN} , F		0.6	1.5		
RS-232 Input Threshold High	T _A = +25°C,	Normal operation SHDN = 5V (MA SHDN = 0V (MA	X223) X235/236/240/241)		1.7	2.4	
N3-232 Input miesiloid nigh	V _{CC} = 5V	Shutdown (MAX22 SHDN = 0V, EN = 5V (R4 _{IN} , F	,		1.5	2.4	
RS-232 Input Hysteresis	V _{CC} = 5V; no hys	steresis in shutdown	1	· 0.2	0.5	1.0	V
RS-232 Input Resistance	T _A = +25°C, V _{CC}	: = 5V		3	5	7	kΩ
TTL/CMOS Output Voltage Low	IOUT = 1.6mA (M	AX231-233 I _{OUT} = 3	3.2mA)			0.4	V
TTL/CMOS Output Voltage High	lout = -1mA	DUT = -1mA					V
TTL/CMOS Output Leakage Current	$0V \le R_{OUT} \le V_{CC}$ $\overline{EN} = V_{CC}$ (MAX2	nA r ≤ V _{CC} ; EN = 0V (MAX223);			0.05	±10	μA
Receiver Output Enchle Time	Normal	MAX223			600		
Receiver Output Enable Time	operation	MAX235/236/239/2	240/241		400		กร
Receiver Output Disable Time	Normal	MAX223			900		ns
Receiver Output Disable Time	operation	MAX235/236/239/2	240/241		250		115
	RS-232 IN to	Normal operation			0.5	10	
Propagation Delay	TTL/CMOS OUT,	SHDN = 0V	t PHLS		4	40	μs
	C _L = 150pF	(MAX223)	tplhs		6	40	
				3	5.1	30	
Transition Region Slew Rate	$T_A = +25^{\circ}C, V_{CC}$ $R_L = 3k\Omega$ to $7k\Omega$,	$\begin{array}{l} \mbox{MAX231/MAX232/MAX233} \\ \mbox{T}_A = +25^\circ C, \ \mbox{V}_C = 5 V, \\ \mbox{R}_L = 3 k \Omega \ \mbox{to} \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$			4	30	- V/µs
Transmitter Output Resistance	$V_{CC} = V_{+} = V_{-} =$	$0V, V_{OUT} = \pm 2V$		300			Ω
Transmitter Out Short-Circuit Current					±10		mA

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ABSOLUTE MAXIMUM RATINGS—MAX225/MAX244-MAX249

 Supply Voltage (V_{CC})
 -0.3V to +6V

 Input Voltages
 TIN, ENA, ENB, ENR, ENT, ENRA,

 ENRB, ENTA, ENTB
 -0.3V to (V_{CC} + 0.3V)

 RIN
 ±25V

 TOUT (Note 3)
 ±15V

 ROUT
 -0.3V to (V_{CC} + 0.3V)

 Short Circuit (one output at a time)
 TOUT to GND

 TOUT to GND
 Continuous

 ROUT to GND
 Continuous

Continuous Power Dissipation ($T_A = +70^{\circ}$ C) 28-Pin Wide SO (derate 12.50mW/°C above +70°C)1W 40-Pin Plastic DIP (derate 11.11mW/°C above +70°C)10W 44-Pin PLCC (derate 13.33mW/°C above +70°C)1.07W Operating Temperature Ranges MAX225C__, MAX24 C__0°C to +70°C MAX225E__, MAX24 C__40°C to +85°C

Storage Temperature Range-65°C to +160°C Lead Temperature (soldering, 10sec)+300°C

Note 3: Input voltage measured with transmitter output in a high-impedance state, shutdown, or V_{CC} = 0V.

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ELECTRICAL CHARACTERISTICS—MAX225/MAX244-MAX249

(MAX225 V_{CC} = 5.0V \pm 5%; MAX244-MAX249 V_{CC} = +5.0V \pm 10%, external capacitors CI-C4 = 1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER		· MIN	TYP	MAX	UNITS	
RS-232 TRANSMITTER						
Input Logic Threshold Low				1.4	0.8	V
Input Logic Threshold High		· ·	2	1.4		V
Logic Pull-Up/Input Current	Tables 1a-1d	Normal operation		10	50	
Logic Pail-Ophiput Current	Tables 1a-10	Shutdown		±0.01	±1	μΑ
Data Rate	Tables 1a-1d, n	ormal operation		120	64	kbits/sec
Output Voltage Swing	All transmitter o	utputs loaded with $3\Omega k$ to GND	±5	±7.5		V
	Tables 1a-1d	ENA, ENB, ENT, ENTA, ENTB = VCC, VOUT = ±15V		±0.01	±25	
Output Leakage Current (shutdown)	Tables Ta-Tu	$V_{CC} = 0V,$ $V_{OUT} = \pm 15V$		±0.01	±25	μA
Transmitter Output Resistance	$V_{CC} = V_{+} = V_{-}$	= 0V, V_{OUT} = ±2V (Note 4)	300	10M		Ω
Output Short-Circuit Current	V _{OUT} = 0V			±30		mA
RS-232 RECEIVERS						
RS-232 Input Voltage Operating Range					±25	V
RS-232 Input Threshold Low	$V_{CC} = 5V$		0.8	1.3		V
RS-232 Input Threshold High	$V_{CC} = 5V$			1.8	2.4	V
RS-232 Input Hysteresis	$V_{CC} = 5V$		0.2	0.5	1.0	V
RS-232 Input Resistance			3	5	7	kΩ
TTL/CMOS Output Voltage Low	$I_{OUT} = 3.2 \text{mA}$			0.2	0.4	V
TTL/CMOS Output Voltage High	IOUT = -1.0mA	IOUT = -1.0mA				V
TTL/CMOS Output Short-Circuit Current	Sourcing VOUT	= GND	-2	-10		mA
The one of the state of the sta	Shrinking Vout	r = V _{CC}	10	30		mA
TTL/CMOS Output Leakage Current		on, outputs disabled, $OV \leq V_{OUT} \leq V_{CC}$, $\overline{ENR}_{-} = V_{CC}$		±0.05	±0.10	μA

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ELECTRICAL CHARACTERISTICS—MAX225/MAX244-MAX249 (continued)

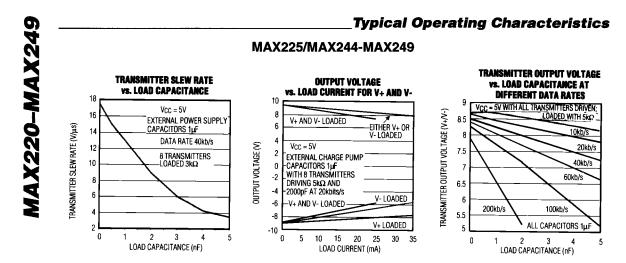
(MAX225 V_{CC} = 5.0V \pm 5%; MAX244-MAX249 V_{CC} = +5.0V \pm 10%, external capacitors CI-C4 = 1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	CONDITIONS			TYP	MAX	UNITS
POWER SUPPLY AND CONTROL LO	GIC					
		MAX225	4.75		5.25	
Operating Supply Voltage		MAX244-MAX249	4.5		5.5	V
		MAX225		10	20	
Vcc Supply Current	No load	MAX244-MAX249		11	30	mA
(normal operation)	3kΩ loads on	MAX225		40		
	all outputs	MAX244-MAX249		57]
Shutdown Supply Current	T _A = +25°C			8	25	цА
Shuldown Supply Current	$T_A = T_{MIN}$ to T_N	IAX			50	μΑ
	Leakage currer	ht			±1	μA
Control Input	Threshold low			1.4	0.8	v
	Threshold high			1.4		v
AC CHARACTERISTICS						
Transition Slew Rate	CL = 50pF to 25 TA = +25°C, me	5	10	30	V/µs	
Transmitter Propagation Delay	tрнLт			1.3	3.5	
TLL to RS-232 (normal operation), Figure 1	t PLHT		1.5	3.5	μs	
Receiver Propagation Delay TLL to RS-232 (normal operation),	TPHLR		0.6	1.5		
Figure 2	tPLHR			0.6	1.5	μs
Receiver Propagation Delay	t PHLS		0.6	10		
TLL to RS-232 (low-power mode), Figure 2	tPLHS			3.0	10	μs
Transmitter + to - Propagation Delay Difference (normal operation)	tphlt - tplht			350		ns
Receiver + to - Propagation Delay Difference (normal operation)	tPHLR - tPLHR			350		ns
Receiver-Output Enable Time, Figure 3	tER			100	500	ns
Receiver-Output Disable Time, Figure 3	tDR .			100	500	ns
Transmitter Enable Time	ter	MAX246-MAX249 (excludes charge-pump start-up)		5		µs
Transmiller Enable Time	tετ	MAX225/MAX245-MAX249 (includes charge-pump start-up)		10		ms
Transmitter Disable Time, Figure 4	tDT.			100		ns

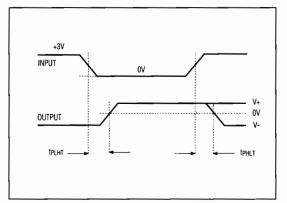
Note 4: The 300Ω minimum specification complies with EIA/TIA-232E, but the actual resistance when in shutdown mode or V_{CC} = 0 is $10M\Omega$ as is implied by the leakage specification.

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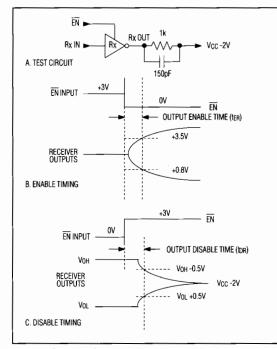
MAX220-MAX249

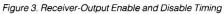


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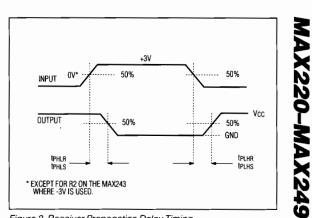
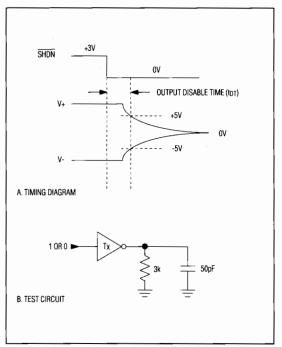
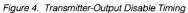


Figure 2. Receiver Propagation Delay Timing





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Table 1a. MAX225 Control Pin Configurations

ENT	ENR	OPERATION STATUS	TRANSMITTERS	RECEIVERS
0	0	Normal Operation	All Active	All Active
0	1	Normal Operation	All Active	All 3-State
1	0	Shutdown	All 3-State	All Low-Power Receive Mode
1	1	Shutdown	All 3-State	All 3-State

Table 1b. MAX245 Control Pin Configurations

ENT	ENR	OPERATION	TRAN	SMITTERS	RECEIVERS		
		STATUS	TA1-TA4	TB1-TB4	RA1-RA5	RB1-RB5	
0	0	Normal Operation	All Active	All Active	All Active	All Active	
0	1	Normal Operation	All Active	All Active	RA1-RA4 3-State RA5 Active	RB1-RB4 3-State RB5 Active	
1	0	Shutdown	All 3-State	All 3-State	All Low Power Receiver Mode	All Low Power Receiver Mode	
1	1	Shutdown	All 3-State	All 3-State	RA1-RA4 3-State RA5 Low-Power Receiver Mode	RB1-RB4 3-State RA5 Low-Power Receiver Mode	

Table 1c. MAX246 Control Pin Configurations

ENA	ENB	OPERATION	TRAN	ISMITTERS	RECEIVERS		
		STATUS	TA1-TA4	TB1-TB4	RA1-RA5	RB1-RB5	
0	0	Normal Operation	All Active	All Active	All Active	All Active	
0	1	Normal Operation	All Active	All 3-State	All Active	RB1-RB4 3-State RB5 Active	
1	0	Shutdown	All 3-State	All Active	RA1-RA4 3-State RA5 Active	All Active	
1	1	Shutdown	All 3-State	All 3-State	RA1-RA4 3-State RA5 Low-Power Receiver Mode	RB1-RB4 3-State RA5 Low-Power Receiver Mode	

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MAX220-MAX249

						TRANSMITTERS		RE	CEIVERS
ENTA	ENTO	ENRA	ENDD	OPERATION	MAX247 MAX248	TA1-TA4	TB1-TB4	RA1-RA4	RB1–RB5
ENTA	CNID	ENRA	ENRD	STATUS		TA1-TA4	TB1-TB4	RA1-RA4	RB1–RB4
					MAX249	TA1-TA3	TB1-TB3	RA1-RA5	RB1RB5
0	0	0	0	Normal Operation		All Active	All Active	All Active	All Active
0	0	0	1	Normal Operation		All Active	All Active	All Active	All 3-State, except RB5 stays active or MAX247
0	0	1	0	Normal Operation		All Active	All Active	All 3-State	All Active
0	0	1	1	Normal Operation		All Active	All Active	All 3-State	All 3-State, except RB5 stays active on MAX247
0	1	0	0	Normal Operation		All Active	All 3-State	All Active	All Active
0	1	0	1	Normal Operation	-	All Active	All 3-State	All Active	All 3-State, except RB5 stays active on MAX247
0	1	1	0	Normal Operation		All Active	All 3-State	All 3-State	All Active
0	1	1	1	Normal Operation	-	All Active	All 3-State	All 3-State	All 3-State, except RB5 stays active on MAX247
1	0	0	0	Normal Operation		All 3-State	All Active	All Active	All Active
1	0	0	1	Normal Operation		All 3-State	All Active	All Active	All 3-State, except RB5 stays active on MAX247
1	0	1	0	Normal Operation		All 3-State	All Active	All 3-State	All Active
1	0	1	1	Normal Operation		All 3-State	All Active	All 3-State	All 3-State, except RB5 stays active on MAX247
1	1	0	0	Shutdown		All 3-State	All 3-State	Low-Power Receive Mode	Low-Power Receive Mode
1	1	0	1	Shutdown		All 3-State	All 3-State	Low-Power Receive Mode	All 3-State, except RB5 stays active on MAX247
1	1	1	0	Shutdown	-	All 3-State	All 3-State	All 3-State	Low-Power Receive Mode
1	1	1	1	Shutdown		All 3-State	All 3-State	All 3-State	All 3-State, except RB5 stays active on MAX247

Table 1d. MAX247/248/249 Control Pin Configurations

_Detailed Description

The MAX220-MAX249 contain four sections: dual chargepump DC-DC voltage converters, RS-232 drivers, RS-232 receivers, and receiver and transmitter enable control inputs.

Dual Charge-Pump Voltage Converter

The MAX220-MAX249 have two internal charge-pumps that convert +5V to $\pm 10V$ (unloaded) for RS-232 driver operation. The first converter uses capacitor C1 to double the +5V input to +10V on C3 at the V+ output. The second converter uses capacitor C2 to invert +10V to -10V on C4 at the V- output.

A small amount of power may be drawn from the +10V (V+) and -10V (V-) outputs to power external circuitry (see Typical Operating Characteristics), except on the MAX225 and MAX245-MAX247, where these pins are not available. V+ and V- are not regulated, so the output voltage drops with increasing load current. Do not load V+ and V- to a point that violates the minimum \pm 5V EIA/TIA-232E driver output voltage when sourcing current from V+ and V- to external circuitry.

When using the shutdown feature in the MAX222, MAX225, MAX230, MAX235, MAX236, MAX240, MAX241, and MAX245-MAX249 avoid using V+ and V- to power external circuitry. When these parts are shut down, V-falls to 0V, and V+ falls to +5V. For applications where a +10V external supply is applied to the V+ pin (instead of using the internal charge pump to generate +10V), the C1 capacitor must not be installed and the \overline{SHDN} pin must be tied to V_{CC}. This is because V+ is internally connected to V_{CC} in shutdown mode.

RS-232 Drivers

The typical driver output voltage swing is ±8V when loaded with a nominal 5k Ω RS-232 receiver and V_{CC} = +5V. Output swing is guaranteed to meet the EIA/TIA-232E and V.28 specification, which calls for ±5V minimum driver output levels under worst-case conditions. These include a minimum 3k Ω load, V_{CC} = +4.5V, and maximum operating temperature. Unloaded driver output voltage ranges from (V+ -1.3V) to (V- +0.5V).

Input thresholds are both TTL and CMOS compatible. The inputs of unused drivers can be left unconnected since 400k Ω input pull-up resistors to V_{CC} are built-in. The pull-up resistors force the outputs of unused drivers low because all drivers invert. The internal input pull-up resistors typically source 12µA, except in shutdown mode where the pull-ups are disabled. Driver outputs turn off and enter a high-impedance state—where leakage current is typically microamperes (maximum 25µA)—when in shutdown mode, in three-state mode, or when device power is removed. Outputs can be driven to ±15V. The power-supply current typically drops to 8µA in shutdown mode.

The MAX239 has a receiver 3-state control line, and the MAX233, MAX225, MAX235, MAX236, MAX240, and MAX241 have both a receiver 3-state control line and a low-power shutdown control. The receiver TTL/CMOS outputs are in a high-impedance, 3-state mode whenever the 3-state ENable line is high, and are also high-impedance whenever the shutdown control line is high.

When in low-power shutdown mode, the driver outputs are turned off and their leakage current is less than 1µA with the driver output pulled to ground. The driver output leakage remains less than 1µA, even if the transmitter output is backdriven between 0V and (V_{CC} + 6V). Below -0.5V, the transmitter is diode clamped to ground with 1kΩ series impedance. The transmitter is also zener clamped to approximately V_{CC} + 6V, with a series impedance of 1kΩ.

The driver output slew rate is limited to less than 30V/ μ s as required by the EIA/TIA-232E and V.28 specifications. Typical slew rates are 24V/ μ s unloaded and 10V/ μ s loaded with 3 Ω and 2500pF.

RS-232 Receivers

EIA/TIA-232E and V.28 specifications define a voltage level greater than 3V as a logic 0, so all receivers invert. Input thresholds are set at 0.8V and 2.4V, so receivers respond to TTL level inputs as well as EIA/TIA-232E and V.28 levels.

The receiver inputs withstand an input overvoltage up to $\pm 25V$ and provide input terminating resistors with nominal 5k Ω values. The receivers implement Type 1 interpretation of the fault conditions of V.28 and EIA/TIA-232E.

The receiver input hysteresis is typically 0.5V with a guaranteed minimum of 0.2V. This produces clear output transitions with slow-moving input signals, even with moderate amounts of noise and ringing. The receiver propagation delay is typically 600ns and is independent of input swing direction.

Low-Power Receive Mode

The low-power receive-mode feature of the MAX223, MAX242, and MAX245-MAX249 puts the IC into shutdown mode, but still allows it to receive information. This is important for applications where systems are periodically awakened to look for activity. Using low-power receive mode, the system can still receive a signal that will activate it on command and prepare it for communication at faster data rates. This operation conserves system power.

Negative Threshold—MAX243

The MAX243 is pin compatible with the MAX232A, differing only in that RS-232 cable fault protection is removed on one of the two receiver inputs. This means that control lines such as CTS and RTS can either be driven or left floating without interrupting communication. Different cables are not needed to interface with different pieces of equipment.



The input threshold of the receiver without cable fault protection is -0.8V rather than +1.4V. Its output goes positive only if the input is connected to a control line that is actively driven negative. If not driven, it defaults to the 0 or "OK to send" state. Normally, the MAX243's other receiver (+1.4V threshold) is used for the data line (TD or RD), while the negative threshold receiver is connected to the control line (DTR, DTS, CTS, RTS, etc.).

Other members of the RS-232 family implement the optional cable fault protection as specified by EIA/TIA-232E specifications. This means a receiver output goes high whenever its input is driven negative, left floating, or shorted to ground. The high output tells the serial communications IC to stop sending data. To avoid this, the control lines must either be driven or connected with jumpers to an appropriate positive voltage level.

Shutdown—MAX222-MAX242

On the MAX222, MAX235, MAX236, MAX240, and MAX241, all receivers are disabled during shutdown. On the MAX223 and MAX242, two receivers continue to operate in a reduced power mode when the chip is in shutdown. Under these conditions, the propagation delay increases to about 2.5µs for a high-to-low input transition. When in shutdown the receiver acts as a CMOS inverter with no hysteresis. The MAX223 and MAX242 also have a receiver output enable input (EN) that allows receiver output. SHDN also disables the receiver outputs.

The MAX225 provides five transmitters and five receivers, while the MAX245 provides ten receivers and eight transmitters. Both devices have separate receiver and transmitter-enable controls. The charge pumps turn off and the devices shut down when a logic high is applied to the ENT input. In this state, the supply current drops to less than 25µA and the receivers continue to operate in a low-power receive mode. Driver outputs enter a high-impedance state (three-state mode). On the MAX225, all five receivers are controlled by the ENR input. On the MAX245, eight of the receiver outputs are controlled by the ENR input, while the remaining two receivers (RA5 and RB5) are always active. RA1-RA4 and RB1-RB4 are put in a three-state mode when ENR is a logic high.

Receiver and Transmitter Enable Control Inputs

The MAX225 and MAX245-MAX249 feature transmitter and receiver enable controls.

The receivers have three modes of operation: full-speed receive (normal active), three-state (disabled), and low-power receive (enabled receivers continue to function at lower data rates). The receiver enable inputs control the

full-speed receive and three-state modes. The transmitters have two modes of operation: full-speed transmit (normal active) and three-state (disabled). The transmitter enable inputs also control the shutdown mode. The device enters shutdown mode when all transmitters are disabled. Enabled receivers function in the low-power receive mode when in shutdown.

Tables 1a-1d define the control states. The MAX244 has no control pins and is not included in these tables.

The MAX246 has ten receivers and eight drivers with two control pins, each controlling one side of the device. A logic high at the A-side control input (ENA) causes the four A-side receivers and drivers to go into a three-state mode. Similarly, the B-side control input (ENB) causes the four B-side drivers and receivers to go into a three-state mode. As in the MAX245, one A-side and one B-side receiver (RA5 and RB5) remain active at all times. The entire device is put into shutdown mode when both the A and B sides are disabled (ENA = ENB = +5V).

The MAX247 provides nine receivers and eight drivers with four control pins. The ENRA and ENRB receiver enable inputs each control four receiver outputs. The ENTA and ENTB transmitter enable inputs each control four drivers. The ninth receiver (RB5) is always active. The device enters shutdown mode with a logic high on both ENTA and ENTB.

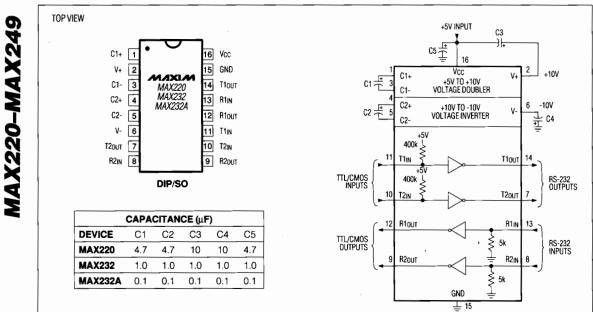
The MAX248 provides eight receivers and eight drivers with four control pins. The $\overline{E}NRA$ and $\overline{E}NRB$ receiver enable inputs each control four receiver outputs. The $\overline{E}NTA$ and $\overline{E}NTB$ transmitter enable inputs control four drivers each. This part does not have an always-active receiver. The device enters shutdown mode and transmitters go into a three-state mode with a logic high on both $\overline{E}NTA$ and \overline{ENTB} .

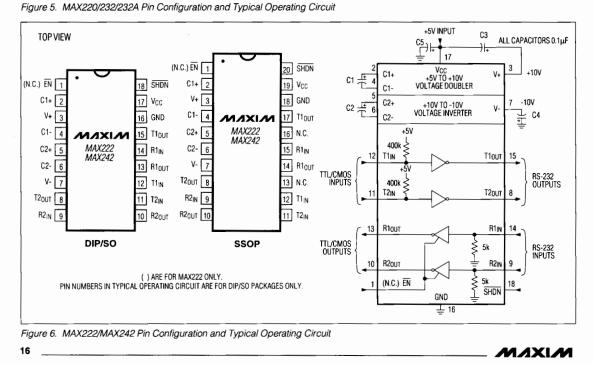
The MAX249 provides ten receivers and six drivers with four control pins. The ENRA and ENRB receiver enable inputs each control five receiver outputs. The ENTA and ENTB transmitter enable inputs control three drivers each. There is no always-active receiver. The device enters shutdown mode and transmitters go into a three-state mode with a logic high on both ENTA and ENTB. In shutdown mode, active receivers operate in a low-power receiver mode at data rates up to 20kbits/s.

Applications Information

Figures 5 through 25 show pin configurations and typical operating circuits. In applications that are sensitive to power-supply noise, V_{CC} should be decoupled to ground with a capacitor of the same value as C1 and C2 connected as close as possible to the device.

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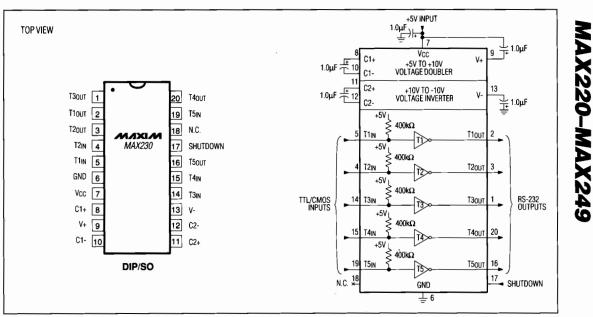
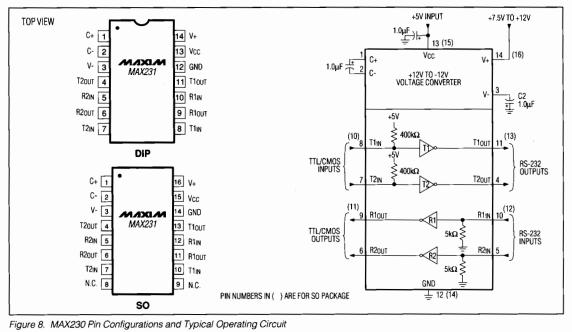


Figure 7. MAX230 Pin Configuration and Typical Operating Circuit



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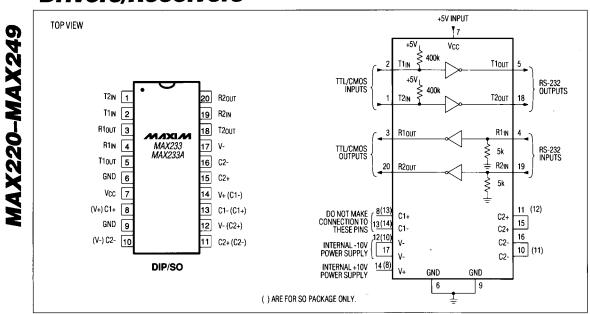


Figure 9. MAX233/MAX233A Pin Configuration and Typical Operating Circuit

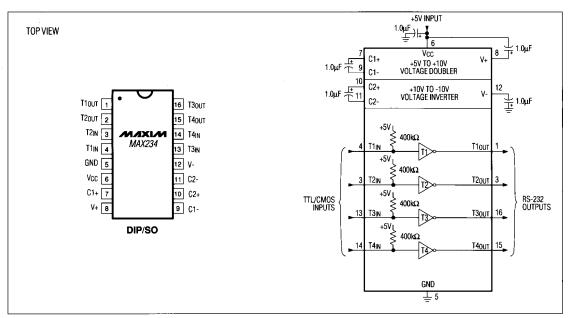


Figure 10. MAX234 Pin Configuration and Typical Operating Circuit

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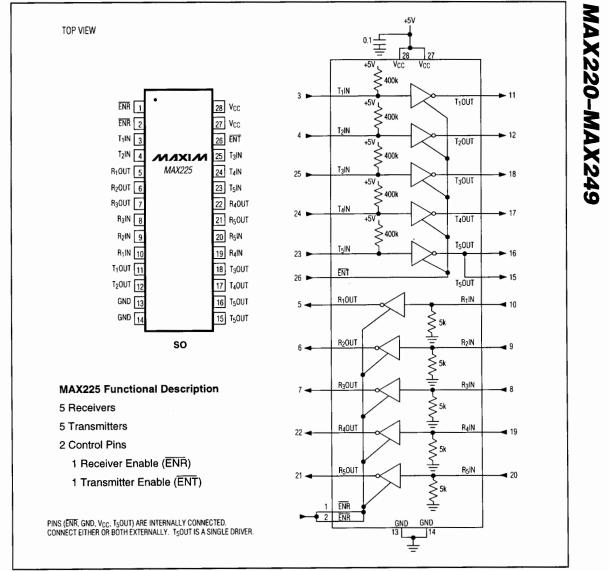


Figure 11. MAX225 Pin Configuration and Typical Operating Circuit

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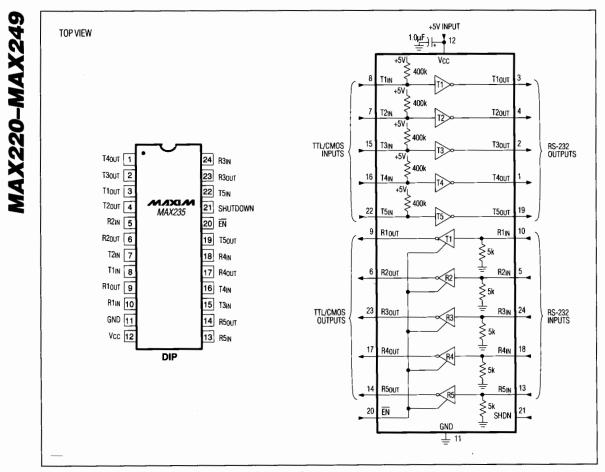


Figure 12. MAX235 Pin Configuration and Typical Operating Circuit

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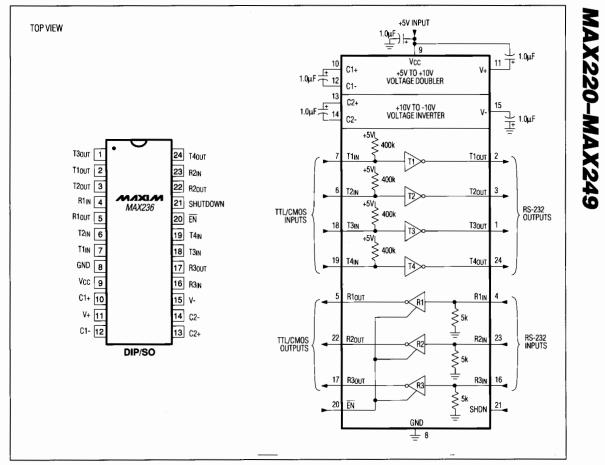


Figure 13. MAX236 Pin Configuration and Typical Operating Circuit

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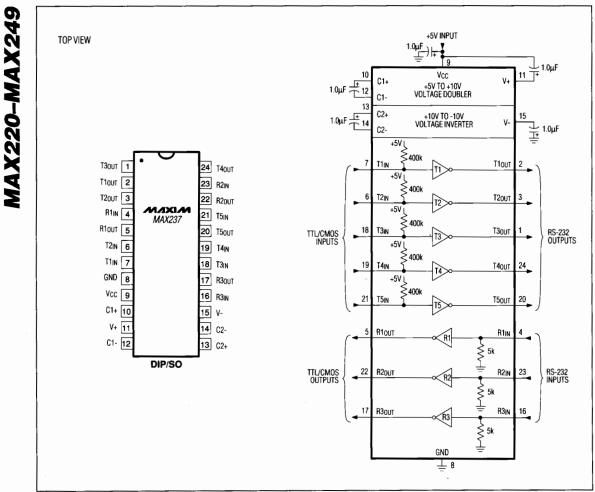


Figure 14. MAX237 Pin Configuration and Typical Operating Circuit

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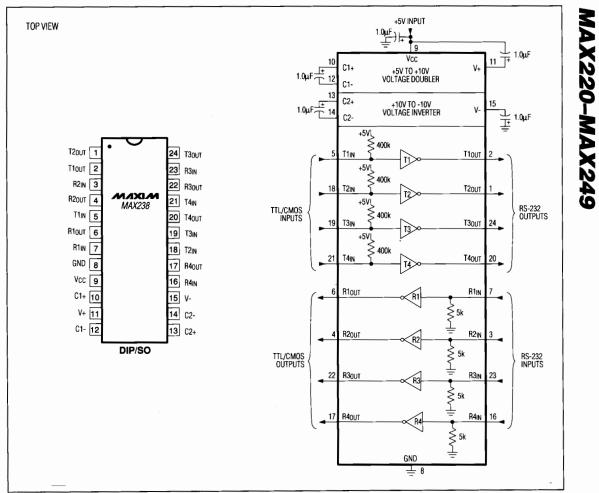


Figure 15. MAX238 Pin Configuration and Typical Operating Circuit

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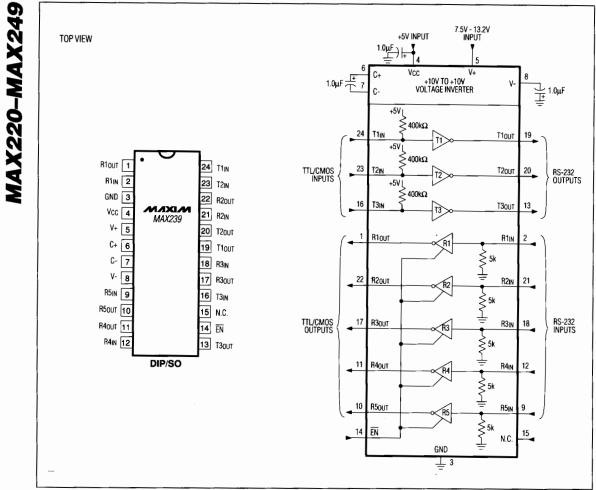


Figure 16. MAX239 Pin Configuration and Typical Operating Circuit

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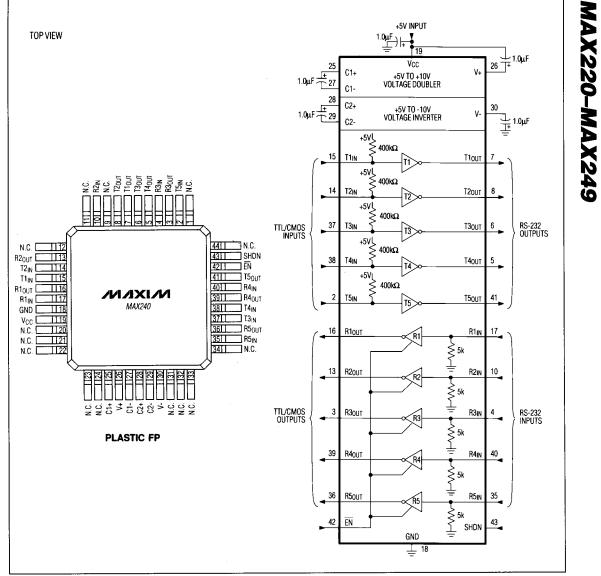


Figure 17. MAX240 Pin Configuration and Typical Operating Circuit

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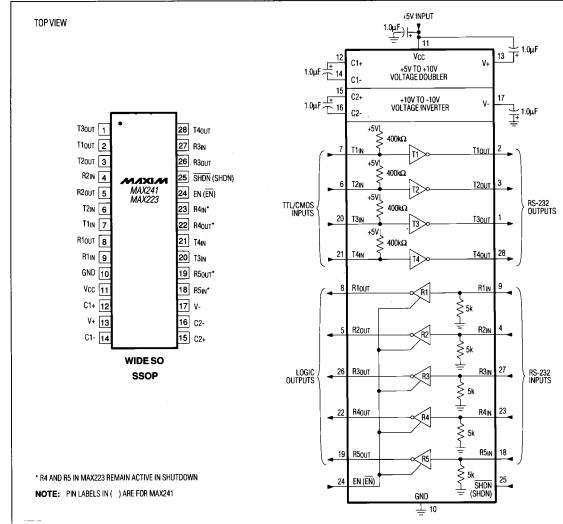


Figure 18. MAX241, MAX223 Pin Configuration and Typical Operating Circuit

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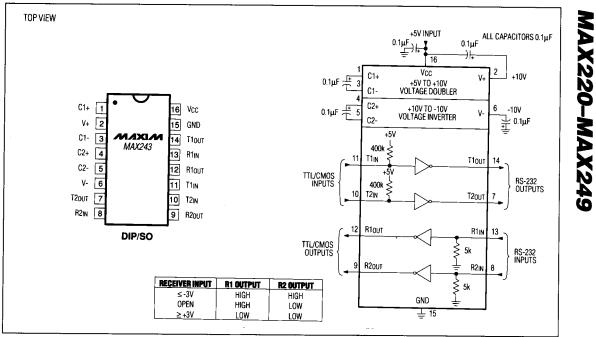


Figure 19. MAX243 Pin Configuration and Typical Operating Circuit

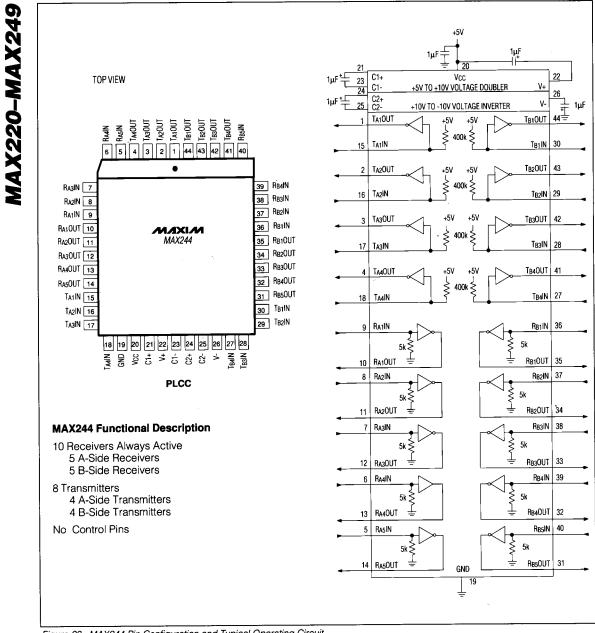


Figure 20. MAX244 Pin Configuration and Typical Operating Circuit

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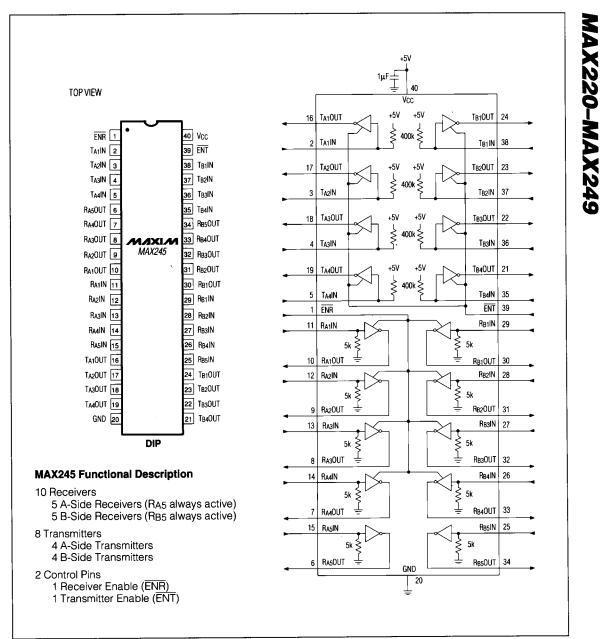
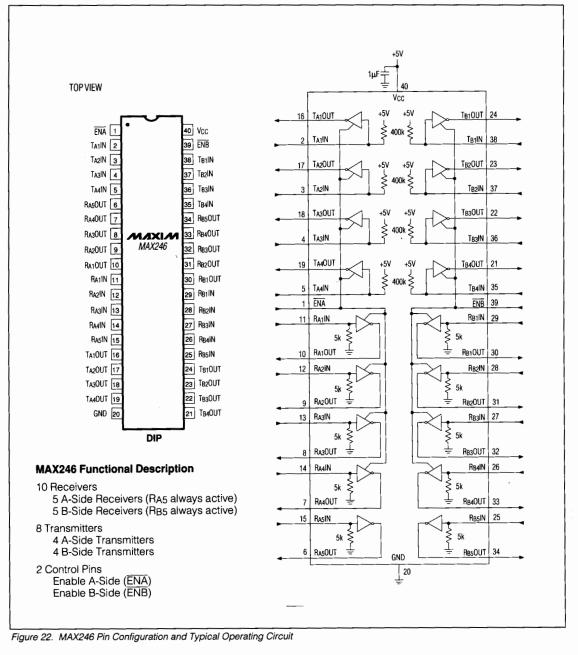


Figure 21. MAX245 Pin Configuration and Typical Operating Circuit

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MAX220-MAX249





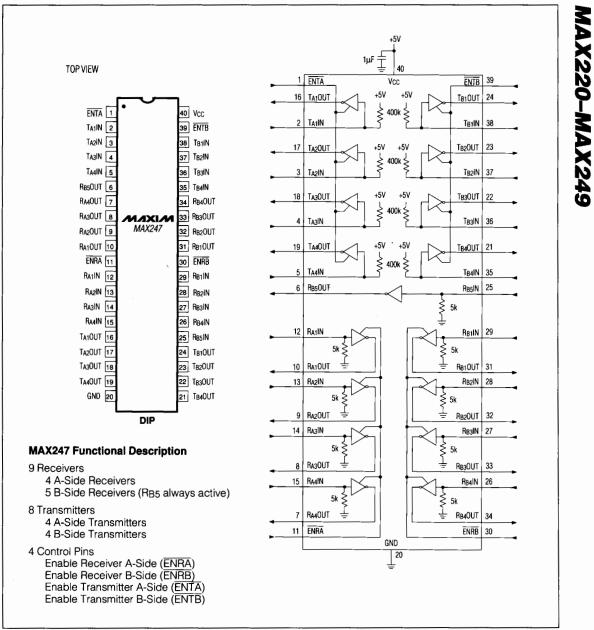


Figure 23. MAX247 Pin Configuration and Typical Operating Circuit

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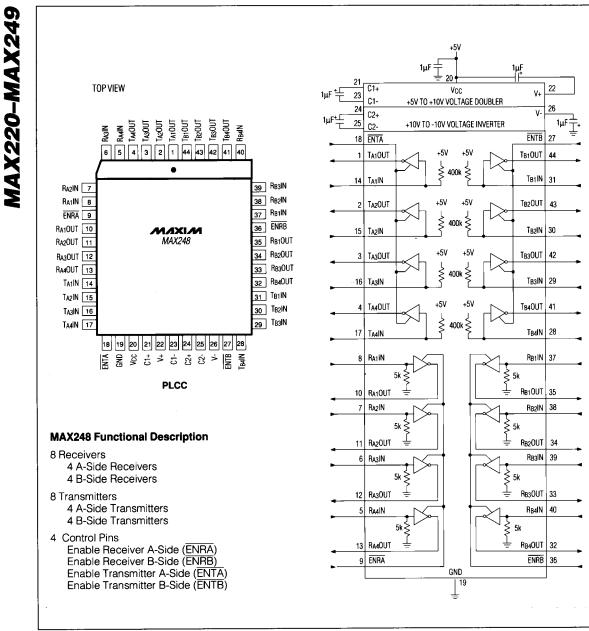
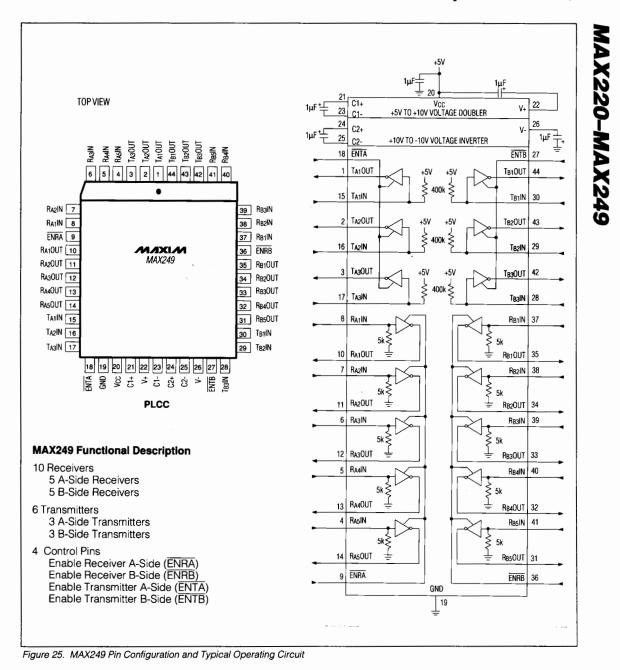


Figure 24. MAX248 Pin Configuration and Typical Operating Circuit

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PART	TEMP. RANGE	PIN-PACKAGE	
MAX222CPN	0°C to +70°C	18 Plastic DIP	
MAX222CWN	0°C to +70°C	18 Wide SO	
MAX222C/D	0°C to +70°C	Dice*	
MAX222EPN	-40°C to +85°C	18 Plastic DIP	
MAX222EWN	-40°C to +85°C	18 Wide SO	
MAX222EJN	-40°C to +85°C	18 CERDIP	
MAX222MJN	-55°C to +125°C	18 CERDIP	
MAX223CAI	0°C to +70°C	28 SSOP	
MAX223CWI	0°C to +70°C	28 Wide SO	
MAX223C/D	0°C to +70°C	Dice*	
MAX223EAI	-40°C to +85°C	28 SSOP	
MAX223EWI	-40°C to +85°C	28 Wide SO	
MAX225CWI	0°C to +70°C	28 Wide SO	
MAX225EWI	-40°C to +85°C	28 Wide SO	
MAX230CPP	0°C to +70°C	20 Plastic DIP	
MAX230CWP	0°C to +70°C	20 Wide SO	
MAX230C/D	0°C to +70°C	Dice*	
MAX230EPP	-40°C to +85°C	20 Plastic DIP	
MAX230EWP	-40°C to +85°C	20 Wide SO	
MAX230EJP	-40°C to +85°C	20 CERDIP	
MAX230MJP	-55°C to +125°C	20 CERDIP	
MAX231CPD	0°C to +70°C	14 Plastic DIP	
MAX231CWE	0°C to +70°C	16 Wide SO	
MAX231CJD	0°C to +70°C	14 CERDIP	
MAX231C/D	0°C to +70°C	Dice*	
MAX231EPD	-40°C to +85°C	14 Plastic DIP	
MAX231EWE	-40°C to +85°C	16 Wide SO	
MAX231EJD	-40°C to +85°C	14 CERDIP	
MAX231MJD	-55°C to +125°C	14 CERDIP	
MAX232CPE	0°C to +70°C	16 Plastic DIP	
MAX232CSE	0°C to +70°C	16 Narrow SO	
MAX232CWE	0°C to +70°C	16 Wide SO	
MAX232C/D	0°C to +70°C	Dice*	
MAX232EPE	-40°C to +85°C	16 Plastic DIP	
MAX232ESE	-40°C to +85°C	16 Narrow SO	
MAX232EWE	-40°C to +85°C	16 Wide SO	
MAX232EJE	-40°C to +85°C	16 CERDIP	
MAX232MJE	-55°C to +125°C	16 CERDIP	
MAX232MLP	-55°C to +125°C	20 LCC	
MAX232ACPE	0°C to +70°C	16 Plastic DIP	
MAX232ACSE	0°C to +70°C	16 Narrow SO	
MAX232ACWE	0°C to +70°C	16 Wide SO	

MAX232AC/D 0°C to +70°C Dice* MAX232AEPE -40°C to +85°C 16 Plastic DIP MAX232AESE -40°C to +85°C 16 Narrow SO MAX232AEWE -40°C to +85°C 16 Wide SO MAX232AEJE -40°C to +85°C 16 CERDIP MAX232AMJE -55°C to +125°C 16 CERDIP MAX232AMLP -55°C to +125°C 20 LCC MAX233CPP 0°C to +70°C 20 Plastic DIP MAX233EPP -40°C to +85°C 20 Plastic DIP MAX233ACPP 0°C to +70°C 20 Plastic DIP MAX233ACWP 0°C to +70°C 20 Wide SO MAX233AEPP -40°C to +85°C 20 Plastic DIP MAX233AEWP -40°C to +85°C 20 Wide SO MAX234CPE 0°C to +70°C 16 Plastic DIP MAX234CWE 0°C to +70°C 16 Wide SO 0°C to +70°C MAX234C/D Dice* MAX234EPE -40°C to +85°C 16 Plastic DIP MAX234EWE -40°C to +85°C 16 Wide SO MAX234EJE -40°C to +85°C 16 CERDIP -55°C to +125°C 16 CERDIP MAX234MJE MAX235CPG 0°C to +70°C 24 Wide Plastic DIP MAX235EPG -40°C to +85°C 24 Wide Plastic DIP MAX235EDG -40°C to +85°C 24 Ceramic SB MAX235MDG -55°C to +125°C 24 Ceramic SB MAX236CNG 0°C to +70°C 24 Narrow Plastic DIP 24 Wide SO MAX236CWG 0°C to +70°C MAX236C/D 0°C to +70°C Dice* MAX236ENG -40°C to +85°C 24 Narrow Plastic DIP MAX236EWG -40°C to +85°C 24 Wide SO MAX236ERG -40°C to +85°C 24 Narrow CERDIP MAX236MRG -55°C to +125°C 24 Narrow CERDIP 0°C to +70°C MAX237CNG 24 Narrow Plastic DIP MAX237CWG 0°C to +70°C 24 Wide SO MAX237C/D 0°C to +70°C Dice* MAX237ENG -40°C to +85°C 24 Narrow Plastic DIP MAX237EWG -40°C to +85°C 24 Wide SO MAX237ERG -40°C to +85°C 24 Narrow CERDIP MAX237MRG -55°C to +125°C 24 Narrow CERDIP MAX238CNG 0°C to +70°C 24 Narrow Plastic DIP MAX238CWG 0°C to +70°C 24 Wide SO MAX238C/D 0°C to +70°C Dice*

Ordering Information (continued)

* Contact factory for dice specifications.

-40°C to +85°C

MAX238ENG

ΜΛΧΙΜ

24 Narrow Plastic DIP

PART	TEMP. RANGE	PIN-PACKAGE
MAX238EWG	-40°C to +85°C	24 Wide SO
MAX238ERG	-40°C to +85°C	24 Narrow CERDIP
MAX238MRG	-55°C to +125°C	24 Narrow CERDIP
MAX239CNG	0°C to +70°C	24 Narrow Plastic DIP
MAX239CWG	0°C to +70°C	24 Wide SO
MAX239C/D	0°C to +70°C	Dice*
MAX239ENG	-40°C to +85°C	24 Narrow Plastic DIP
MAX239EWG	-40°C to +85°C	24 Wide SO
MAX239ERG	-40°C to +85°C	24 Narrow CERDIP
MAX239MRG	-55°C to +125°C	24 Narrow CERDIP
MAX240CMH	0°C to +70°C	44 Plastic FP
MAX240C/D	0°C to +70°C	Dice*
MAX241CAI	0°C to +70°C	28 SSOP
MAX241CW1	0°C to +70°C	28 Wide SO
MAX241C/D	0°C to +70°C	Dice*
MAX241EAI	-40°C to +85°C	28 SSOP
MAX241EWI	-40°C to +85°C	28 Wide SO
MAX242CAP	0°C to +70°C	20 SSOP
MAX242CPN	0°C to +70°C	18 Plastic DIP
MAX242CWN	0°C to +70°C	18 Wide SO
MAX242C/D	0°C to +70°C	Dice*
MAX242EPN	-40°C to +85°C	18 Plastic DIP
MAX242EWN	-40°C to +85°C	18 Wide SO
MAX242EJN	-40°C to +85°C	18 CERDIP
MAX242MJN	-55°C to +125°C	18 CERDIP

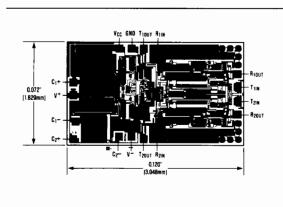
	mormation	i (continuea
MAX243CPE	0°C to +70°C	16 Plastic DIP
MAX243CSE	0°C to +70°C	16 Narrow SO
MAX243CWE	0°C to +70°C	16 Wide SO
MAX243C/D	0°C to +70°C	Dice*
MAX243EPE	-40°C to +85°C	16 Plastic DIP
MAX243ESE	-40°C to +85°C	16 Narrow SO
MAX243EWE	-40°C to +85°C	16 Wide SO
MAX243EJE	-40°C to +85°C	16 CERDIP
MAX243MJE	-55°C to +125°C	16 CERDIP
MAX244CQH	0°C to +70°C	44 PLCC
MAX244C/D	0°C to +70°C	Dice*
MAX244EQH	-40°C to +85°C	44 PLCC
MAX245CPL	0°C to +70°C	40 Plastic DIP
MAX245C/D	0°C to +70°C	Dice*
MAX245EPL	-40°C to +85°C	40 Plastic DIP
MAX246CPL	0°C to +70°C	40 Plastic DIP
MAX246C/D	0°C to +70°C	Dice*
MAX246EPL	-40°C to +85°C	40 Plastic DIP
MAX247CPL	0°C to +70°C	40 Plastic DIP
MAX247C/D	0°C to +70°C	Dice*
MAX247EPL	-40°C to +85°C	40 Plastic DIP
MAX248CQH	0°C to +70°C	44 PLCC
MAX248C/D	0°C to +70°C	Dice*
MAX248EQH	-40°C to +85°C	44 PLCC
MAX249CQH	0°C to +70°C	44 PLCC
MAX249EQH	-40°C to +85°C	44 PLCC

MAX220-MAX249

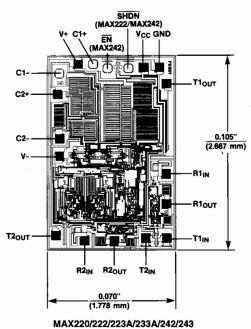
* Contact factory for dice specifications.

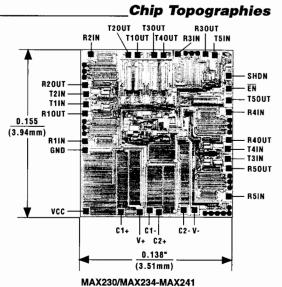
ΜΙΧΙΜ



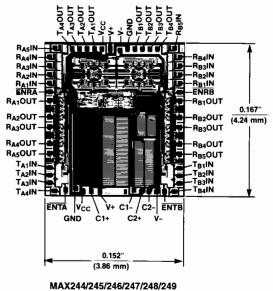


MAX231, MAX232 and MAX233





SUTDOWN PIN OF MAX234, MAX237, MAX238, MAX239, MAX240 AND MAX241 ARE INTERNALLY CONNECTED TO GROUND.



CONNECT SUBSTRATE TO V+

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