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# FCMT099N65S3

## N-Channel SuperFET® III MOSFET

### 650 V, 30 A, 99 mΩ

### Features

- 700 V @  $T_J = 150\text{ }^\circ\text{C}$
- Typ.  $R_{DS(on)} = 87\text{ m}\Omega$
- Ultra Low Gate Charge (Typ.  $Q_g = 56\text{ nC}$ )
- Low Effective Output Capacitance (Typ.  $C_{oss(eff.)} = \text{TBD pF}$ )
- 100% Avalanche Tested
- RoHS Compliant

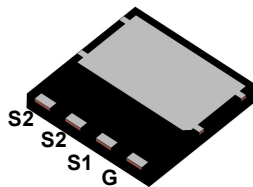
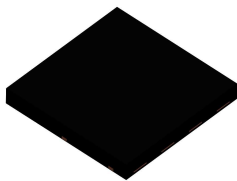
### Applications

- Server and Telecom Power Supplies
- Solar Inverters
- Adaptors

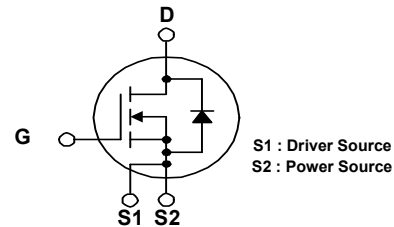
### Description

SuperFET® III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SuperFET III MOSFET is very suitable for the switching power applications such as server/telecom power, adaptor and solar inverter applications.

The Power88 package is an ultra-slim surface-mount package (1 mm high) with a low profile and small footprint (8x8 mm<sup>2</sup>). SuperFET III MOSFET in a Power88 package offers excellent switching performance due to lower parasitic source inductance and separated power and drive sources. Power88 offers Moisture Sensitivity Level 1 (MSL 1).



Power88



### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	FCMT099N65S3	Unit
$V_{DSS}$	Drain to Source Voltage	650	V
$V_{GSS}$	Gate to Source Voltage	- DC	$\pm 30$
		- AC ( $f > 1\text{ Hz}$ )	$\pm 30$
$I_D$	Drain Current	- Continuous ( $T_C = 25^\circ\text{C}$ )	30
		- Continuous ( $T_C = 100^\circ\text{C}$ )	19
$I_{DM}$	Drain Current	- Pulsed (Note 1)	75
$E_{AS}$	Single Pulsed Avalanche Energy	(Note 2)	145
$I_{AS}$	Avalanche Current	(Note 1)	4.4
$E_{AR}$	Repetitive Avalanche Energy	(Note 1)	2.27
dv/dt	MOSFET dv/dt		100
	Peak Diode Recovery dv/dt	(Note 3)	20
$P_D$	Power Dissipation	( $T_C = 25^\circ\text{C}$ )	227
		- Derate Above $25^\circ\text{C}$	1.82
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$
$T_L$	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds	300	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	FCMT099N65S3	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.55	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	45	

## Package Marking and Ordering Information

Part Number	Top Mark	Package	Reel Size	Tape Width	Quantity
FCMT099N65S3	FCMT099N65S3	Power88	13"	13.3 mm	3000

## Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
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### Off Characteristics

$BV_{DSS}$	Drain to Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}, T_J = 25^\circ\text{C}$	650	-	-	V
		$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}, T_J = 150^\circ\text{C}$	700	-	-	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 1\text{ mA}$ , Referenced to $25^\circ\text{C}$	-	0.68	-	$V/^\circ\text{C}$
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$	-	-	10	$\mu\text{A}$
		$V_{DS} = 520\text{ V}, T_C = 125^\circ\text{C}$	-	2.77	-	
$I_{GSS}$	Gate to Body Leakage Current	$V_{GS} = \pm 30\text{ V}, V_{DS} = 0\text{ V}$	-	-	$\pm 100$	nA

### On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 3\text{ mA}$	2.5	-	4.5	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10\text{ V}, I_D = 15\text{ A}$	-	87	99	$\text{m}\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = 20\text{ V}, I_D = 15\text{ A}$	-	17	-	S

### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{DS} = 400\text{ V}, V_{GS} = 0\text{ V},$ $f = 1\text{ MHz}$	-	2270	-	pF
$C_{oss}$	Output Capacitance		-	50	-	pF
$C_{oss(eff.)}$	Effective Output Capacitance	$V_{DS} = 0\text{ V to } 400\text{ V}, V_{GS} = 0\text{ V}$	-	500	-	pF
$C_{oss(er.)}$	Energy Related Output Capacitance	$V_{DS} = 0\text{ V to } 400\text{ V}, V_{GS} = 0\text{ V}$	-	74	-	pF
$Q_{g(tot)}$	Total Gate Charge at 10V	$V_{DS} = 400\text{ V}, I_D = 15\text{ A},$ $V_{GS} = 10\text{ V}$	-	56	-	nC
$Q_{gs}$	Gate to Source Gate Charge		-	13	-	nC
$Q_{gd}$	Gate to Drain "Miller" Charge		(Note 4)	-	23	-
ESR	Equivalent Series Resistance	$f = 1\text{ MHz}$	-	0.5	-	$\Omega$

### Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 400\text{ V}, I_D = 15\text{ A},$ $V_{GS} = 10\text{ V}, R_g = 4.7\ \Omega$	-	22	-	ns
$t_r$	Turn-On Rise Time		-	20	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	58	-	ns
$t_f$	Turn-Off Fall Time		(Note 4)	-	5	-

### Source-Drain Diode Characteristics

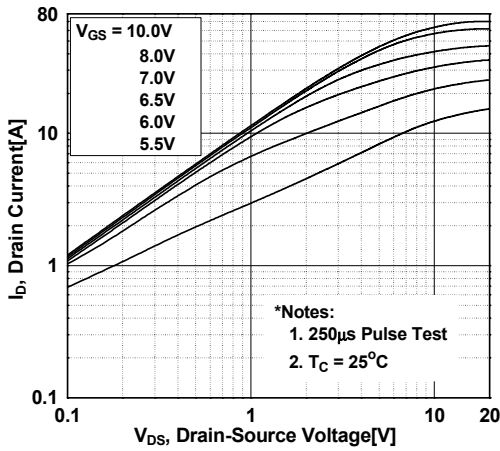
$I_S$	Maximum Continuous Source to Drain Diode Forward Current	-	-	30	A	
$I_{SM}$	Maximum Pulsed Source to Drain Diode Forward Current	-	-	75	A	
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_{SD} = 15\text{ A}$	-	-	1.2	V
$t_{rr}$	Reverse Recovery Time	$V_{GS} = 0\text{ V}, I_{SD} = 15\text{ A},$ $di_F/dt = 100\text{ A}/\mu\text{s}$	-	352	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	6.5	-	$\mu\text{C}$

#### Notes:

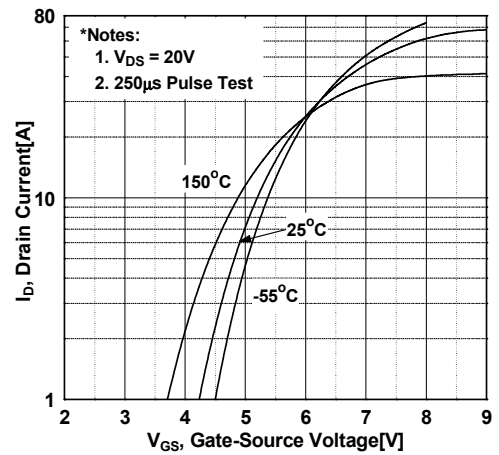
1. Repetitive rating: pulse-width limited by maximum junction temperature.
2.  $I_{AS} = 4.4\text{ A}, R_G = 25\ \Omega$ , starting  $T_J = 25^\circ\text{C}$ .
3.  $I_{SD} \leq 15\text{ A}, di/dt \leq 200\text{ A}/\mu\text{s}, V_{DD} \leq 400\text{ V}$ , starting  $T_J = 25^\circ\text{C}$ .
4. Essentially independent of operating temperature typical characteristics.

## Typical Performance Characteristics

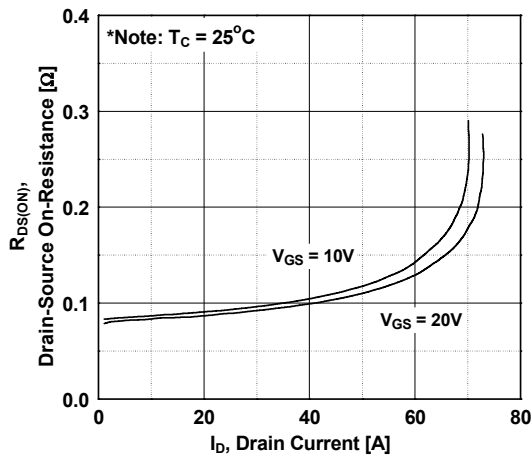
**Figure 1. On-Region Characteristics**



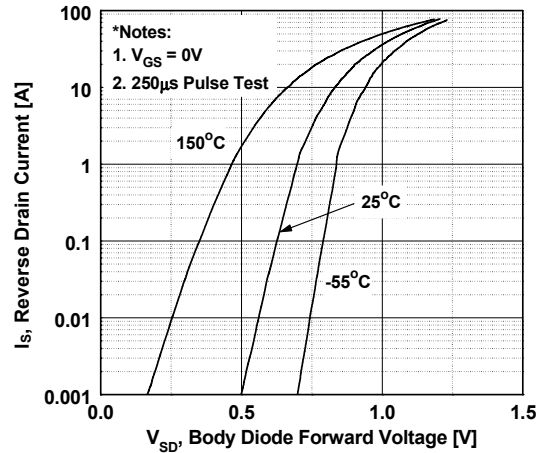
**Figure 2. Transfer Characteristics**



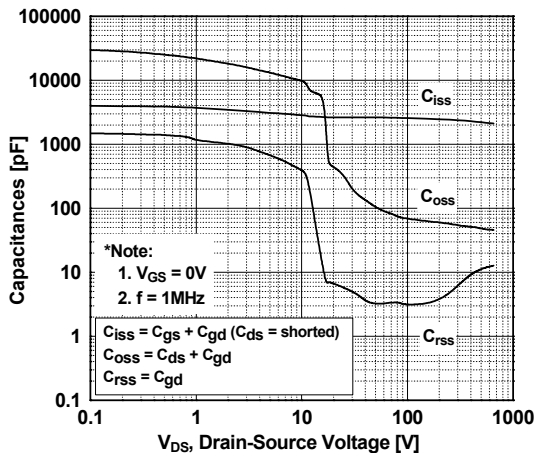
**Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage**



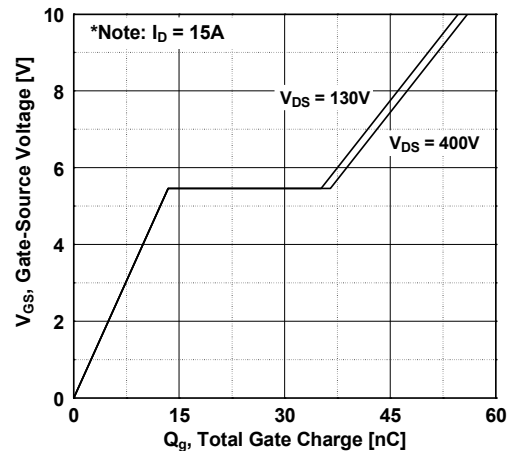
**Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature**



**Figure 5. Capacitance Characteristics**

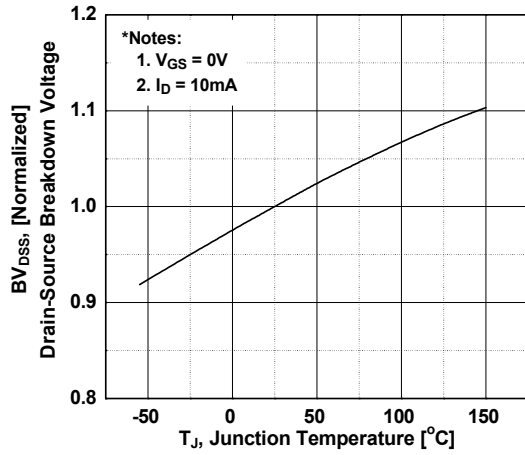


**Figure 6. Gate Charge Characteristics**

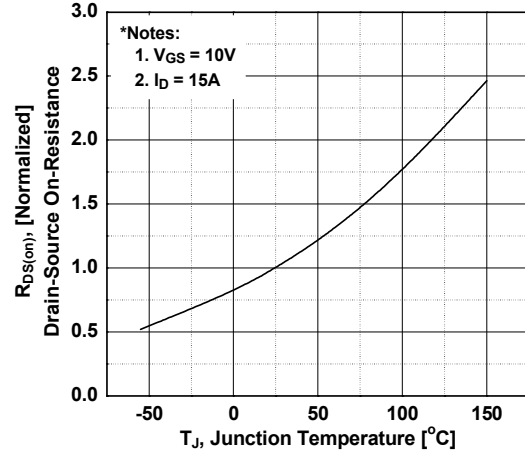


## Typical Performance Characteristics (Continued)

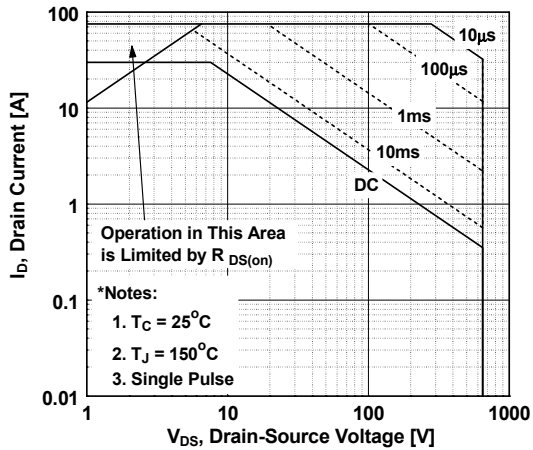
**Figure 7. Breakdown Voltage Variation vs. Temperature**



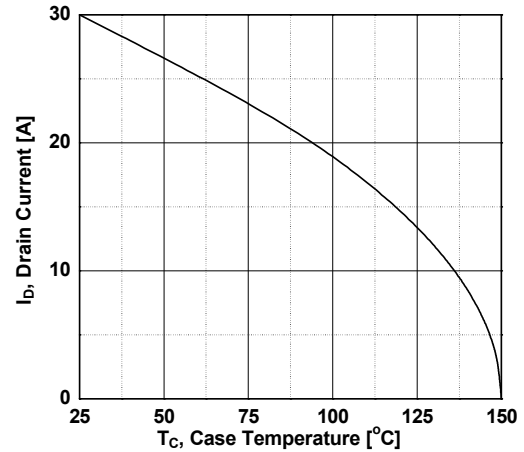
**Figure 8. On-Resistance Variation vs. Temperature**



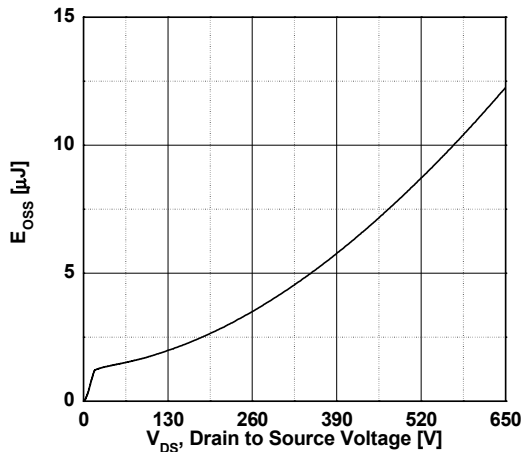
**Figure 9. Maximum Safe Operating Area**



**Figure 10. Maximum Drain Current vs. Case Temperature**

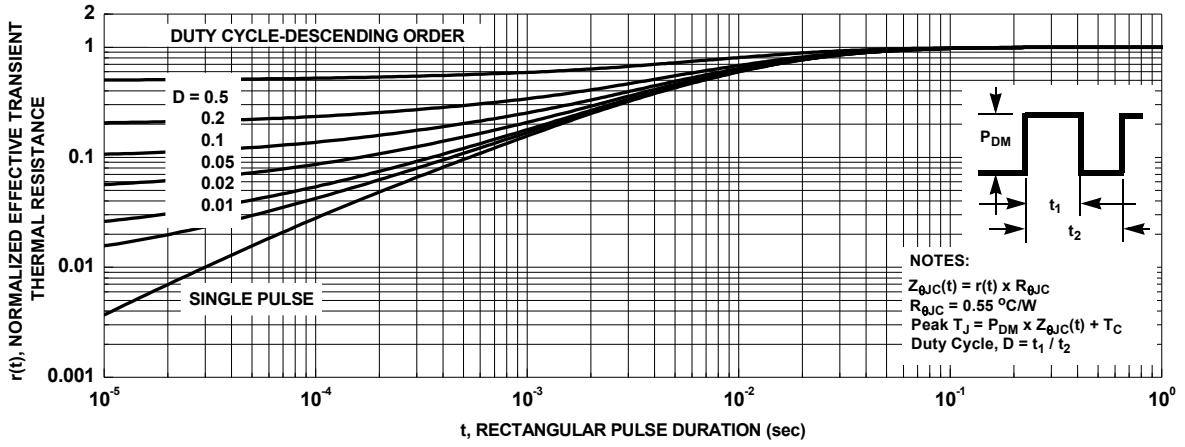


**Figure 11. E\_oss vs. Drain to Source Voltage**



Typical Performance Characteristics (Continued)

Figure 12. Transient Thermal Response Curve



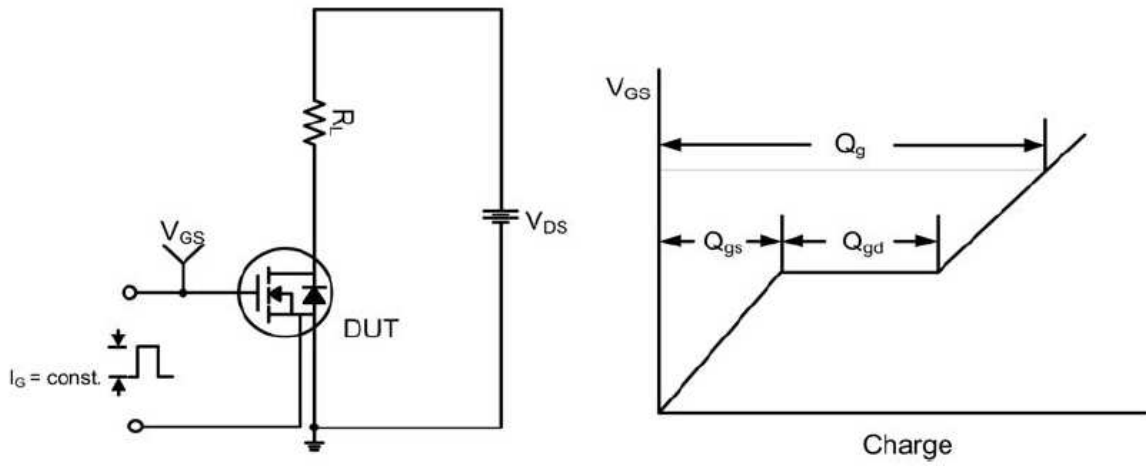


Figure 13. Gate Charge Test Circuit & Waveform

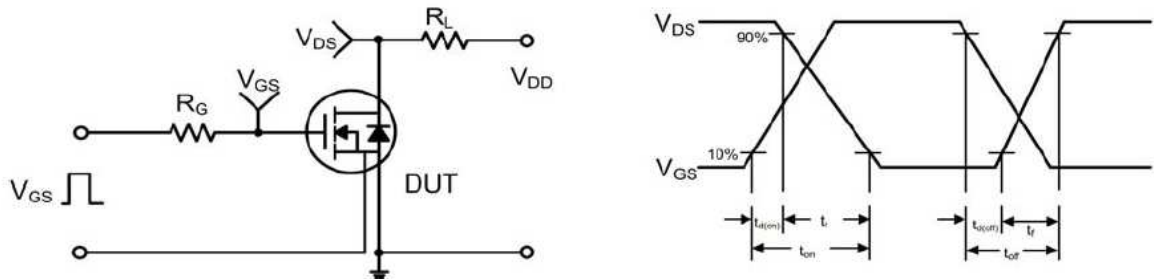


Figure 14. Resistive Switching Test Circuit & Waveforms

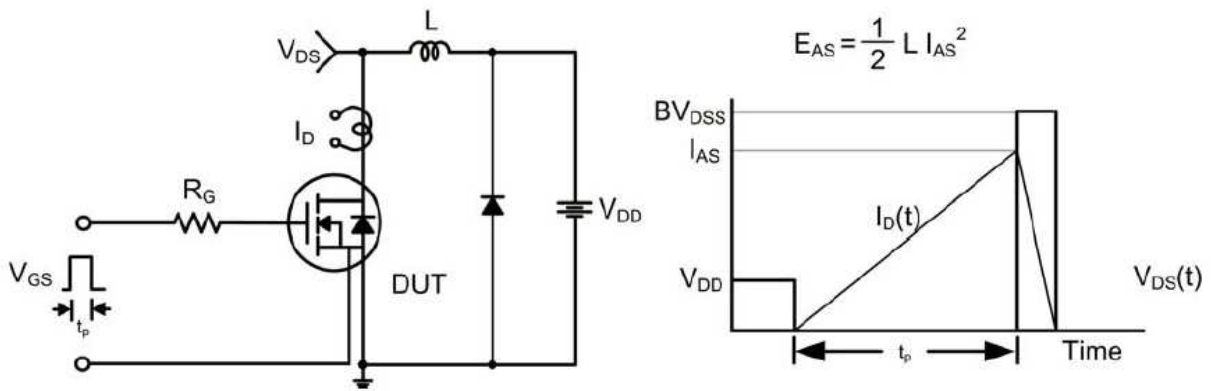


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

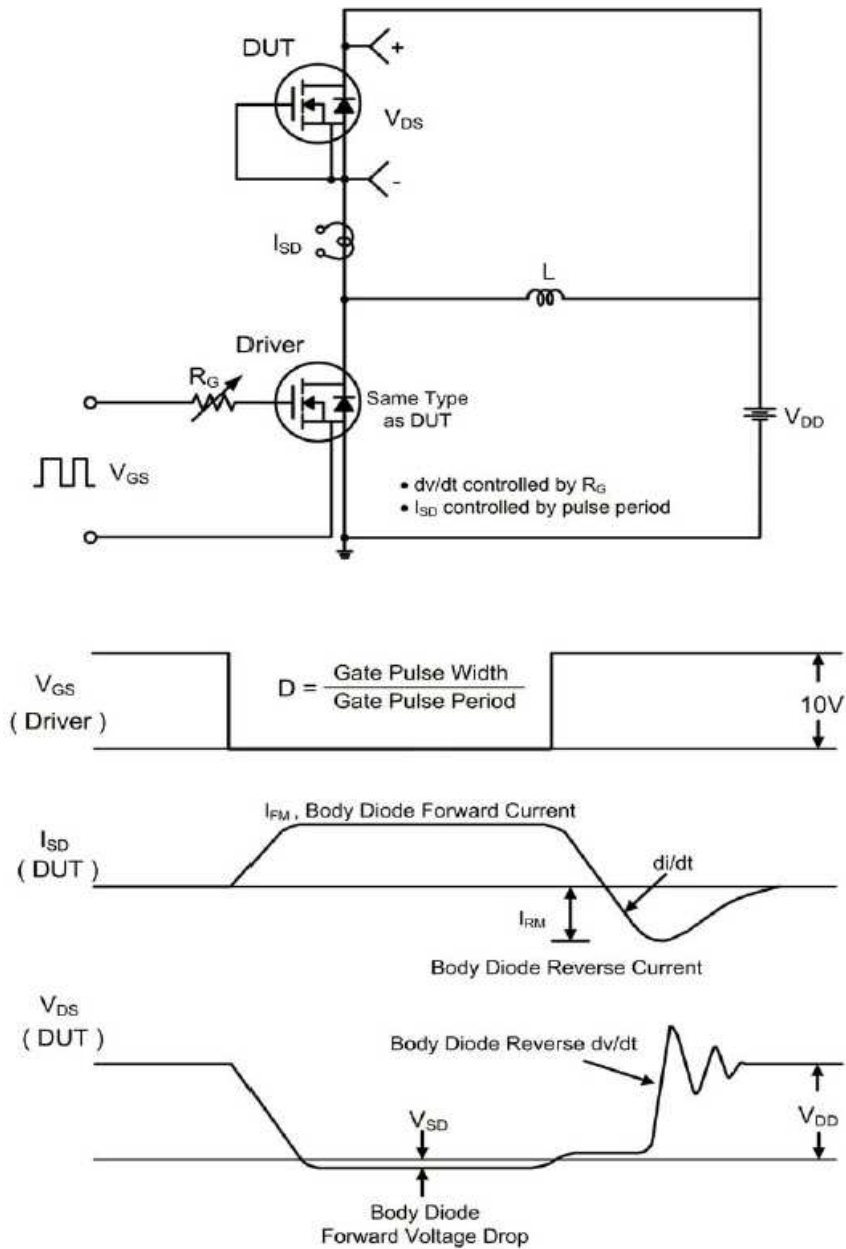
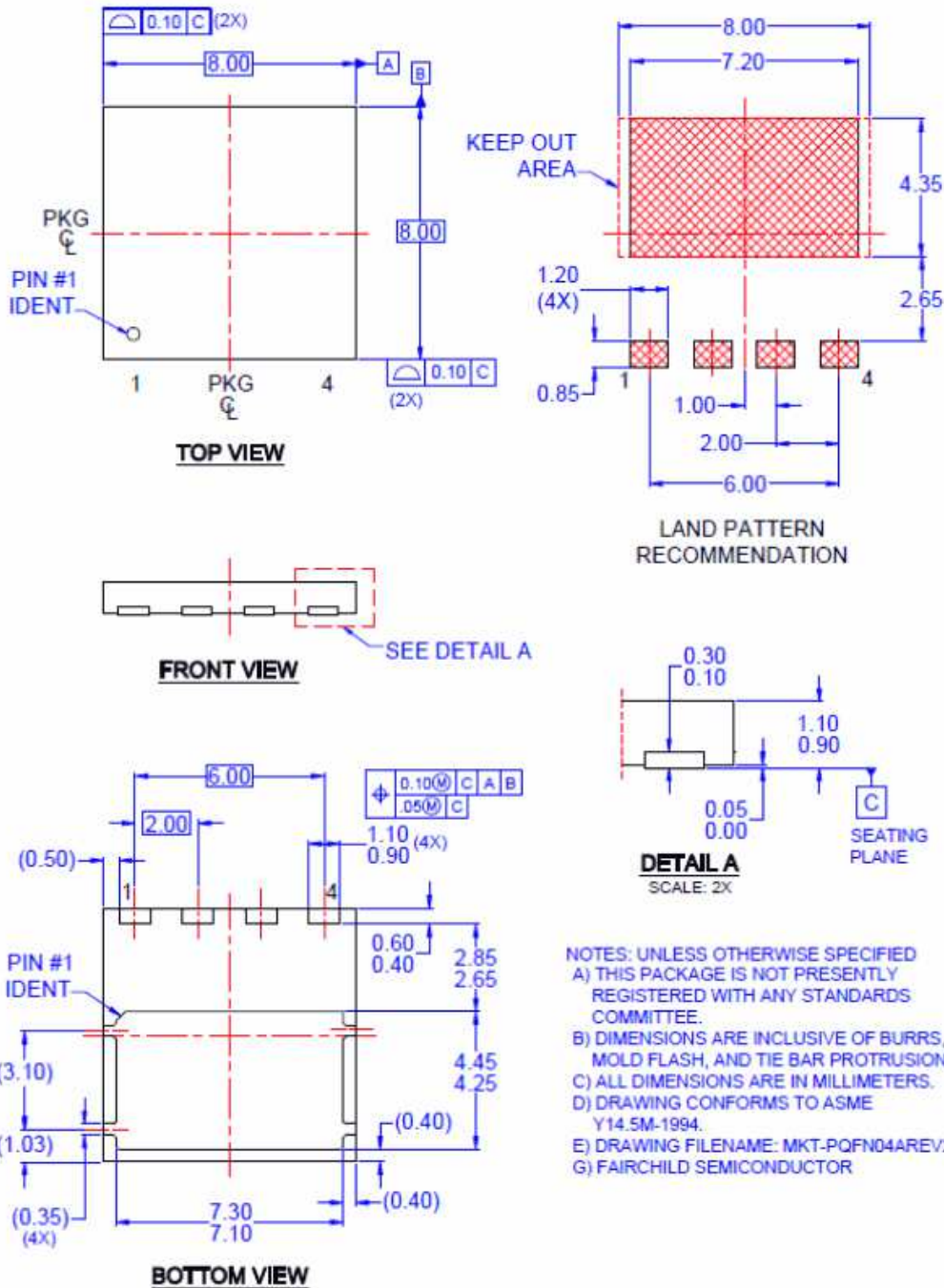


Figure 16. Peak Diode Recovery  $dv/dt$  Test Circuit & Waveforms



## Mechanical Dimensions



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