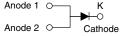
Vishay General Semiconductor

# Surface-Mount Low V<sub>F</sub> Standard Rectifiers



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SE10DX



## **DESIGN SUPPORT TOOLS AVAILABLE**



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	10 A				
V <sub>RRM</sub>	400 V, 600 V				
I <sub>FSM</sub>	150 A				
$V_F$ at $I_F$ = 10 A ( $T_A$ = 125 °C)	0.83 V				
T <sub>J</sub> max.	175 °C				
Package	SMPD (TO-263AC)				
Circuit configuration	Single				

## FEATURES

- Very low profile typical height of 1.7 mm
- Low forward voltage drop
- AEC-Q101 qualified available
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

General purpose, power line polarity protection, in both consumer and automotive applications.

#### **MECHANICAL DATA**

**Case:** SMPD (TO-263AC) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 M3 and HM3 suffix meet JESD 201 class 2 whisker test

Polarity: as marked

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SE10DLG	SE10DLJ	UNIT	
Device marking code		SE10DLG	SE10DLJ		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	400	600	V	
Maximum DC forward current	I <sub>F</sub> <sup>(1)</sup>	10		A	
	I <sub>F</sub> <sup>(2)</sup>	3.6			
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	150		A	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub> <sup>(3)</sup>	-55 to +175		°C	

Notes

<sup>(1)</sup> Mounted on infinite heatsink

<sup>(2)</sup> Free air, mounted on recommended copper pad area

 $^{(3)}$  The heat generated must be less than the thermal conductivity junction to ambient dP<sub>D</sub>/dT<sub>J</sub> < R<sub>thJA</sub>

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HALOGEN

FREE



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C		0.87	-	v
	I <sub>F</sub> = 10 A		V <sub>E</sub> (1)	0.95	1	
	I <sub>F</sub> = 5 A	– T <sub>A</sub> = 125 °C	VF ()	0.73	-	
	I <sub>F</sub> = 10 A			0.83	0.9	
Reverse current	Rated V <sub>B</sub>	T <sub>A</sub> = 25 °C	I <sub>B</sub> <sup>(2)</sup>	-	5	μA
	naleu v <sub>R</sub>	T <sub>A</sub> = 125 °C	IR (=/	10	50	
Typical reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>rr</sub> = 0.25 A		t <sub>rr</sub>	280	-	ns
Typical junction capacitance	4.0 V, 1 MHz		CJ	70	-	pF

Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25$ °c unless otherwise noted)				
PARAMETER	SYMBOL	SE10DLG	SE10DLJ	UNIT
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)(2)</sup>	55		°C/W
Typical thermal resistance	R <sub>0JM</sub> <sup>(3)</sup>	1.5		

#### Notes

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

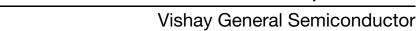
(2) Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance R<sub>0JA</sub> - junction to ambient to follow JEDEC<sup>®</sup> 51-2A

(3) Mounted on infinite heatsink thermal resistance R<sub>thJM</sub> - junction to mount to follow JEDEC<sup>®</sup> 51-14 transient dual interface test method (TDIM)

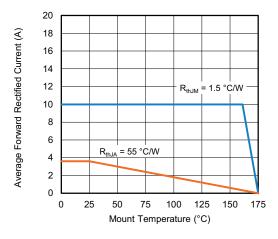
ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
SE10DLJ-M3/I	0.538	I	2000/reel	13" diameter plastic tape and reel	
SE10DLJHM3/I <sup>(1)</sup>	0.538	I	2000/reel	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified



## **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)



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Fig. 1 - Forward Current Derating Curve

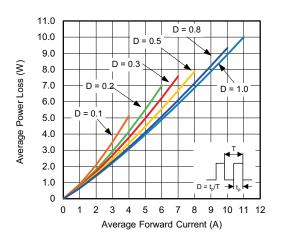


Fig. 2 - Forward Power Loss Characteristics

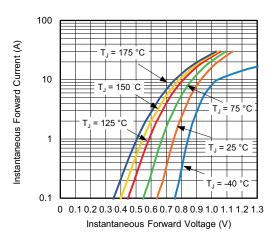


Fig. 3 - Typical Instantaneous Forward Characteristics

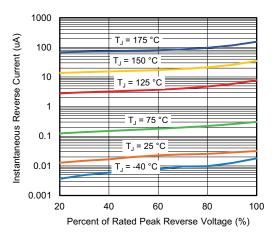


Fig. 4 - Typical Reverse Leakage Characteristics

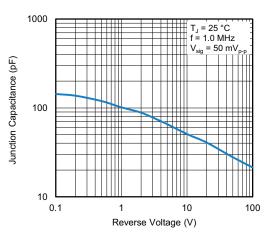
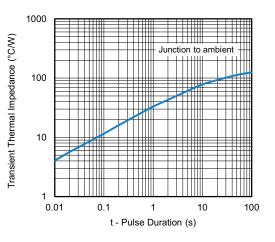
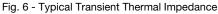


Fig. 5 - Typical Junction Capacitance





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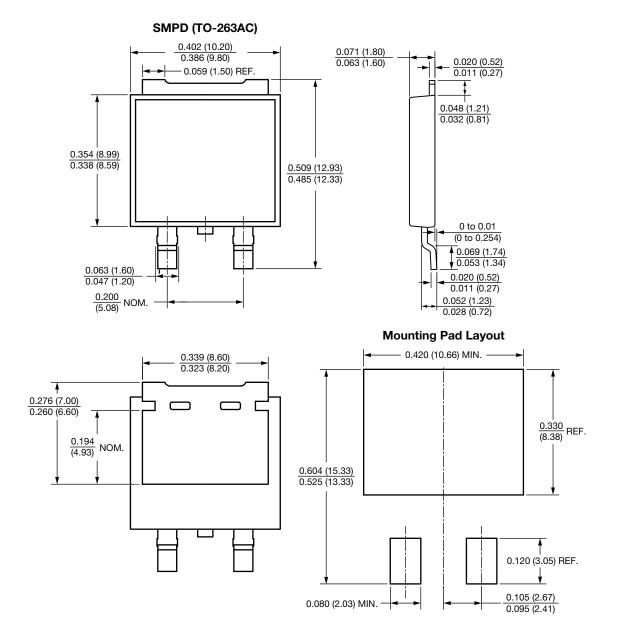
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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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