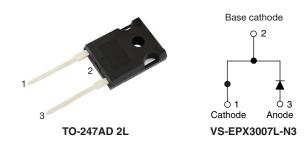
**Vishay Semiconductors** 

Hyperfast Rectifier, 30 A FRED Pt<sup>®</sup>



30 A

650 V

1.6 V

27 ns

175 °C

TO-247AD 2L

Single

**PRIMARY CHARACTERISTICS** 

I<sub>F(AV)</sub>

 $V_{R}$ 

V<sub>F</sub> at I<sub>F</sub>

t<sub>rr</sub> typ.

T<sub>J</sub> max.

Package

Circuit configuration

www.vishay.com

- Low forward voltage drop
- Hyperfast soft recovery time
- 175 °C operating junction temperature
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



RoHS COMPLIANT HALOGEN FREE

#### **DESCRIPTION / APPLICATIONS**

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS					
Repetitive peak reverse voltage	V <sub>RRM</sub>		650	V					
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 111 °C	30	^					
Non-repetitive peak surge current	I <sub>FSM</sub>	$T_C$ = 25 °C, $t_p$ = 8.3 ms, half sine wave	170	A					
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C					

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_J$ = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	650	-	-			
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 30 A	-	2.1	2.5	V		
		I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	1.6	1.7			
Reverse leakage current	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	0.02	30			
neverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	300	μA		
Junction capacitance	CT	V <sub>R</sub> = 650 V	-	22	-	pF		
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH		

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 1
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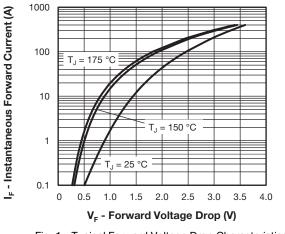
## Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	ONDITIONS	MIN.	TYP.	MAX.	UNITS		
Reverse recovery time	t <sub>rr</sub>	$I_F = 1 \text{ A}, \ dI_F/dt = 100$	-	35	-				
		T <sub>J</sub> = 25 °C		-	27	-	ns		
		T <sub>J</sub> = 125 °C	l <sub>F</sub> = 30 A dl <sub>F</sub> /dt = 1000 A/μs V <sub>R</sub> = 400 V	-	88	-			
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	15	-	A		
		T <sub>J</sub> = 125 °C		-	24	-			
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	330	-			
		T <sub>J</sub> = 125 °C		-	1350	-	110		

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C			
Thermal resistance, junction to case	R <sub>thJC</sub>		-	0.7	1.1	°C/W			
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-				
Weight			-	5.5	-	g			
weight			-	0.2	-	oz.			
Mounting torque			1.2 (10)	-	2.4 (20)	kgf · cm (lbf · in)			
Marking device		Case style TO-247 2L		EPX3	8007L				

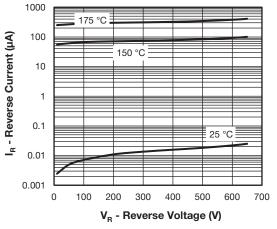


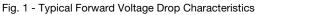
### **Vishay Semiconductors**

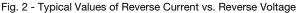


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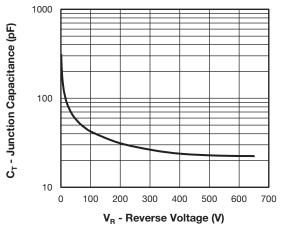


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

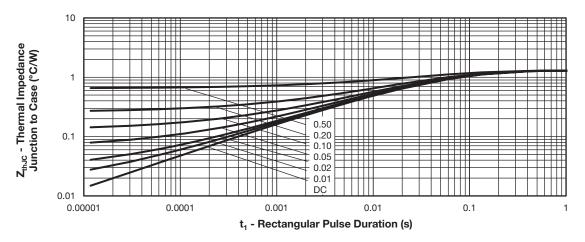
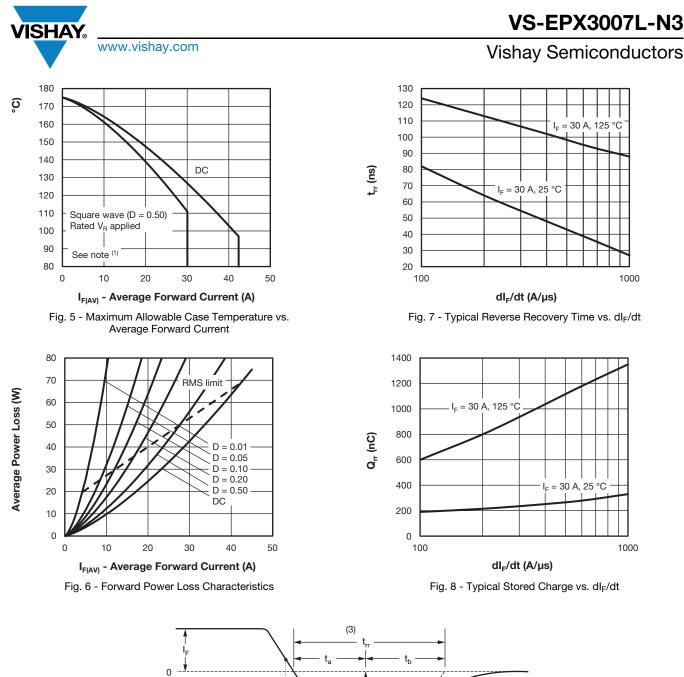


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



°C

1000

1000

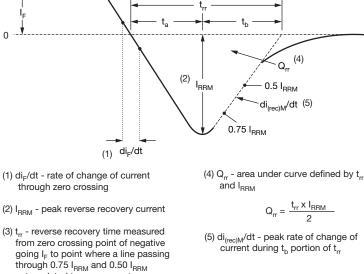


Fig. 9 - Reverse Recovery Waveform and Definitions

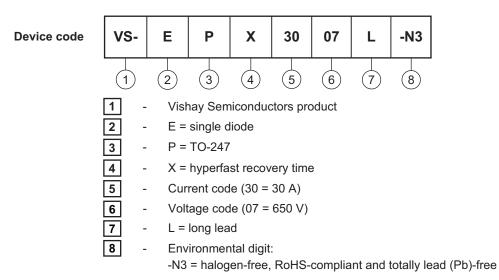
extrapolated to zero current.

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### **ORDERING INFORMATION TABLE**



ORDERING INFORMATION (Example)								
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTI								
VS-EPX3007L-N3	25	500	Antistatic plastic tube					

LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95536						
Part marking information	www.vishay.com/doc?95648					



**Vishay Semiconductors** 

**TO-247AD 2L** 

#### **DIMENSIONS** in millimeters and inches



Section C - C, D - D

(b, b2)

(4)

View	<u>/ B</u>

SYMBOL	MILLIN	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		STMDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			E	15.29	15.87	0.602	0.625	3
A1	2.21	2.59	0.087	0.102			E1	13.46	-	0.53	-	
A2	1.50	2.49	0.059	0.098			е	5.46	BSC	0.215	5 BSC	
b	0.99	1.40	0.039	0.055			ØК	0.2	254	0.0	010	
b1	0.99	1.35	0.039	0.053			L	19.81	20.32	0.780	0.800	
b2	1.65	2.39	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b3	1.65	2.34	0.065	0.092			ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	
D2	0.51	1.35	0.020	0.053				•		•		•

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

(5) Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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