



Product Change Notification / GDOS-04EWFZ700

Date:

07-Feb-2024

Product Category:

Crypto Memory

PCN Type:

Manufacturing Change

Notification Subject:

CCB 6792 Initial Notice: Qualification of MTAI as an additional assembly site for selected AT88SC0104CA, AT88SC0204CA, AT88SC0404CA and AT88SC0808CA device families available in 8L SOIC (.150in) package using CuPdAu wire.

Affected CPNs:

[GDOS-04EWFZ700_Affected_CPN_02072024.pdf](#)

[GDOS-04EWFZ700_Affected_CPN_02072024.csv](#)

Notification Text:

PCN Status:Initial Notification

PCN Type:Manufacturing Change

Microchip Parts Affected:Please open one of the files found in the Affected CPNs section.
Note: For your convenience Microchip includes identical files in two formats (.pdf and .xls)

Description of Change:Qualification of MTAI as an additional assembly site for selected AT88SC0104CA, AT88SC0204CA, AT88SC0404CA and AT88SC0808CA device families available in 8L SOIC (.150in) package using CuPdAu wire.

Pre and Post Change Summary:

Date																				
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Method to Identify Change:

Traceability code

Qualification Plan:

Please open the attachments included with this PCN labeled as PCN_#_Qual_Plan.

Revision History:

February 07, 2024: Issued initial notification.

The change described in this PCN does not alter Microchip’s current regulatory compliance regarding the material content of the applicable products.

Attachments:

- [PCN_GDOS-04EWFZ700_Pre_and_Post_Change_Summary.pdf](#)
- [PCN_GDOS-04EWFZ700_Qual_Plan.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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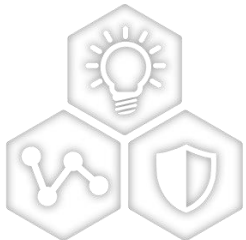
Affected Catalog Part Numbers (CPN)

AT88SC0104CA-SH
AT88SC0204CA-SH
AT88SC0404CA-SH
AT88SC0808CA-SH
AT88SC0104CA-SH-T
AT88SC0204CA-SH-T
AT88SC0404CA-SH-T
AT88SC0808CA-SH-T

CCB 6792
Pre and Post Change Summary
PCN #: GDOS-04EWFZ700

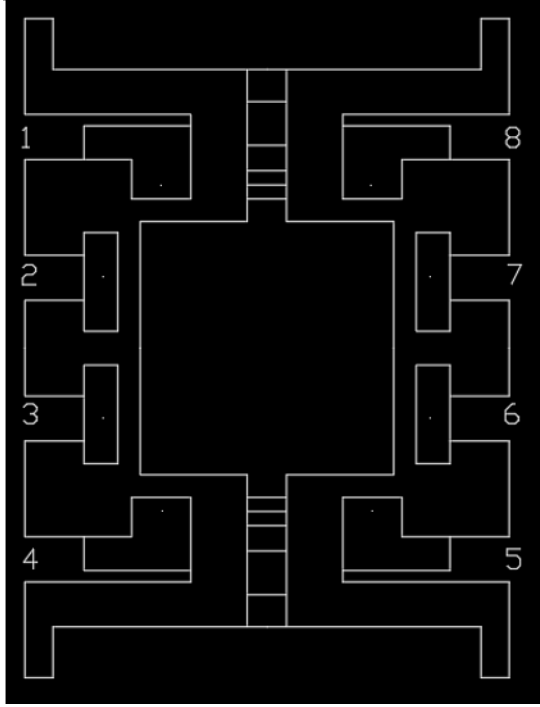
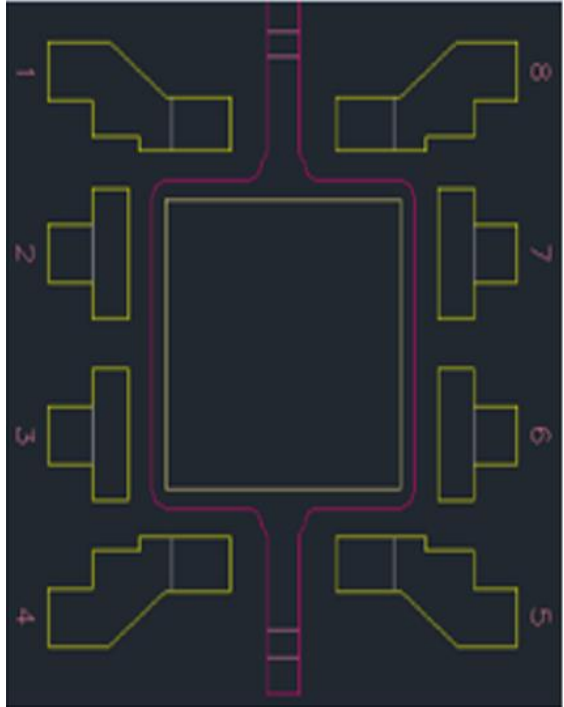


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Pre and Post Change Summary - Lead Frame Comparison

ASSH		MTAI	
 <p>A schematic diagram of an ASSH lead frame. It shows a central square die pad with a complex lead pattern. The leads are numbered 1 through 8, starting from the top-left and moving clockwise. The leads are relatively wide and have a simple rectangular shape.</p>		 <p>A schematic diagram of an MTAI lead frame. It shows a central square die pad with a lead pattern similar to the ASSH but with more complex, stepped lead shapes. The leads are numbered 1 through 8, starting from the top-left and moving clockwise. The leads are narrower and have a more intricate profile.</p>	
Lead frame material	C194	Lead frame material	C194
DAP Surface Prep	PPF	DAP Surface Prep	Bare Cu
Lead Plating	NiPdAu	Lead Plating	Matte tin

**Note: Not fit to scale*



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QUALIFICATION PLAN SUMMARY

PCN #: GDOS-04EWFZ700

**Date:
January 17, 2024**

Qualification of MTAI as an additional assembly site for selected AT88SC0104CA, AT88SC0204CA, AT88SC0404CA and AT88SC0808CA device families available in 8L SOIC (.150in) package using CuPdAu wire.

Purpose: Qualification of MTAI as an additional assembly site for selected AT88SC0104CA, AT88SC0204CA, AT88SC0404CA and AT88SC0808CA device families available in 8L SOIC (.150in) package using CuPdAu wire.

CCB No.: 6792

<u>Misc.</u>	Assembly site	MTAI
	BD Number	BD-001964-03
	MP Code (MPC)	569127C2XQUL
	Part Number (CPN)	56912-MTAI-QUAL-SU
	MSL information	1
	Assembly Shipping Media (T/R, Tube/Tray)	Tube / T&R
	Base Quantity Multiple (BQM)	100 / 4000
	Reliability Site	MTAI
<u>Lead-Frame</u>	Paddle size	90 x 90 mils
	Material	CDA194
	DAP Surface Prep	Bare Cu
	Treatment	BOT
	Process	Stamped
	Lead-lock	No
	Part Number	10100812
	Lead Plating	Matte tin
	Strip Size	MTAI standard
	Strip Density	MTAI standard
<u>Bond Wire</u>	Material	CuPdAu
<u>Die Attach</u>	Part Number	QMI519
	Conductive	Yes
<u>MC</u>	Part Number	G600V
<u>PKG</u>	Package Type	SOIC
	Pin/Ball Count	8
	PKG width/size	150 mils

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Special Instructions
Standard Pb-free Solderability	J-STD-002D ; Perform 8 hour steam aging for Matte tin finish and 1 hour steam aging for NiPdAu finish prior to testing. Standard Pb-free: Matte tin/ NiPdAu finish, SAC solder, wetting temp 245°C for both SMD & through hole packages.	22	5	1	27	> 95% lead coverage	5	Standard Pb-free solderability is the requirement. SnPb solderability (backward solderability- SMD reflow soldering) is required for any plating related changes and highly recommended for other package BOM changes.
Wire Bond Pull - WBP	Mil. Std. 883-2011	5	0	1	5	0 fails after TC	5	30 bonds from a min. 5 devices.
Wire Bond Shear - WBS	CDF-AEC-Q100-001	5	0	1	5		5	30 bonds from a min. 5 devices.
Wire Sweep								Required for any reduction in wire bond thickness.
Physical Dimensions	Measure per JESD22 B100 and B108	10	0	3	30		5	
External Visual	Mil. Std. 883-2009/2010	All devices prior to submission for qualification testing	0	3	ALL	0	5	

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Special Instructions
Preconditioning - Required for surface mount devices MSL 1 @ 260 C	JESD22-A113. +150°C Bake for 24 hours, moisture loading requirements per MSL level + 3X reflow at peak reflow temperature per Jedec-STD-020E for package type; Electrical test pre and post stress at +25°C.	231	15	3	738	0	15	Spares should be properly identified. 77 parts from each lot to be used for HAST, uHAST, Temp Cycle test.
HAST	JESD22-A110. +130°C/85% RH for 96 hours or 110°C/85%RH for 264 hours. Electrical test pre and post stress at +25°C and hot temp. Max test temp at 85 C	77	5	3	246	0	10	Spares should be properly identified. Use the parts which have gone through Pre-conditioning. Post-stress Electrical Test Window Time: Within 48 hours.
UHAST	JESD22-A118. +130°C/85% RH for 96 hrs or +110°C/85% RH for 264 hrs. Electrical test pre and post stress at +25°C	77	5	3	246	0	10	Spares should be properly identified. Use the parts which have gone through Pre-conditioning. Post-stress Electrical Test Window Time: Within 48 hours.

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Special Instructions
Temp Cycle	<p>JESD22-A104. -65°C to +150°C for 500 cycles.</p> <p>Electrical test pre and post stress at hot temp; 3 gram force WBP, on 5 devices from 1 lot, test following Temp Cycle stress.</p> <p>Max test temp at 85 C</p>	77	5	3	246	0	15	<p>Spares should be properly identified. Use the parts which have gone through Pre-conditioning.</p>