LMC7660 Switched Capacitor Voltage Converter



Literature Number: SNOSBZ9B

LMC7660 Switched Capacitor Voltage Converter



## LMC7660 Switched Capacitor Voltage Converter General Description Features

The LMC7660 is a CMOS voltage converter capable of converting a positive voltage in the range of +1.5V to +10V to the corresponding negative voltage of -1.5V to -10V. The LMC7660 is a pin-for-pin replacement for the industry-standard 7660. The converter features: operation over full temperature and voltage range without need for an external diode, low quiescent current, and high power efficiency.

The LMC7660 uses its built-in oscillator to switch 4 power MOS switches and charge two inexpensive electrolytic capacitors.

- Operation over full temperature and voltage range without an external diode
- Low supply current, 200 µA max
- Pin-for-pin replacement for the 7660
- Wide operating range 1.5V to 10V
- 97% Voltage Conversion Efficiency
- 95% Power Conversion Efficiency
- Easy to use, only 2 external components
- Extended temperature range
- Narrow SO-8 Package



### Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

| Supply Voltage              | 10.5V                            |
|-----------------------------|----------------------------------|
| Input Voltage on Pin 6, 7   |                                  |
| (Note 2)                    | -0.3V to (V <sup>+</sup> + 0.3V) |
|                             | for V <sup>+</sup> < 5.5V        |
|                             | $(V^+ - 5.5V)$ to $(V^+ + 0.3V)$ |
|                             | for V <sup>+</sup> > 5.5V        |
| Current into Pin 6 (Note 2) | 20 µA                            |
| Output Short Circuit        |                                  |
| Duration ( $V^+ \le 5.5V$ ) | Continuous                       |

| Power Dissipation (Note 3)  |                                       |
|-----------------------------|---------------------------------------|
| Dual-In-Line Package        | 1.4W                                  |
| Surface-Mount Package       | 0.6W                                  |
| T <sub>J</sub> Max (Note 3) | 150°C                                 |
| θ <sub>JA</sub> (Note 3)    |                                       |
| Dual-In-Line Package        | 90°C/W                                |
| Surface-Mount Package       | 160°C/W                               |
| Storage Temp. Range         | $-65^{\circ}C \le T \le 150^{\circ}C$ |
| Lead Temperature            |                                       |
| (Soldering, 5 sec.)         | 260°C                                 |
| ESD Tolerance (Note 7)      | ± 2000V                               |

#### Electrical Characteristics (Note 4)

|                    |                     |  |            | LMC7660IN/ |        |
|--------------------|---------------------|--|------------|------------|--------|
| Symbol             | Parameter           | Conditions                                 | Typ        | LMC7660IM  | Units  |
|                    |                     |  | Typ        | Limit      | Limits |
|                    |                     |  |            | (Note 5)   |        |
| ls                 | Supply Current      | $R_{L} = \infty$                           | 120        | 200        | μA     |
|                    |                     |  |            | 400        | max    |
| V <sup>+</sup> H   | Supply Voltage      | $R_L = 10 \text{ k}\Omega$ , Pin 6 Open    | 3 to 10    | 3 to 10    | V      |
|                    | Range High (Note 6) | Voltage Efficiency $\ge 90\%$              |            | 3 to 10    |        |
| V+L                | Supply Voltage      | $R_L = 10 \text{ k}\Omega$ , Pin 6 to Gnd. | 1.5 to 3.5 | 1.5 to 3.5 | V      |
|                    | Range Low           | Voltage Efficiency $\geq$ 90%              |            | 1.5 to 3.5 |        |
| R <sub>out</sub>   | Output Source       | I <sub>L</sub> = 20 mA                     | 55         | 100        | Ω      |
|                    | Resistance          |  |            | 120        | max    |
|                    |                     | $V = 2V, I_{L} = 3 \text{ mA}$             | 110        | 200        | Ω      |
|                    |                     | Pin 6 Short to Gnd.                        |            | 300        | max    |
| F <sub>osc</sub>   | Oscillator          |  | 10         |            | kHz    |
|                    | Frequency           |  |            |            |        |
| P <sub>eff</sub>   | Power Efficiency    | $R_{L} = 5 \ k\Omega$                      | 97         | 95         | %      |
|                    |                     |  |            | 90         | min    |
| V <sub>o eff</sub> | Voltage Conversion  | $R_{L} = \infty$                           | 99.9       | 97         | %      |
|                    | Efficiency          |  |            | 95         | min    |
| l <sub>osc</sub>   | Oscillator Sink or  | Pin 7 = Gnd. or $V^+$                      | 3          |            | μA     |
|                    | Source Current      |  |            |            |        |

Note 1: Absolute Maximum ratings indicate limits beyond which damage to the device may occur. DC and AC electrical specifications do not apply when operating the device beyond its rated operating conditions. See Note 4 for conditions.

Note 2: Connecting any input terminal to voltages greater than V<sup>+</sup> or less than ground may cause destructive latchup. It is recommended that no inputs from sources operating from external supplies be applied prior to "power-up" of the LMC7660.

Note 3: For operation at elevated temperature, these devices must be derated based on a thermal resistance of  $\theta_{ja}$  and  $T_j$  max,  $T_j = T_A + \theta_{ja} P_D$ .

Note 4: Boldface numbers apply at temperature extremes. All other numbers apply at  $T_A = 25^{\circ}C$ ,  $V^+ = 5V$ ,  $C_{osc} = 0$ , and apply for the LMC7660 unless otherwise specified. Test circuit is shown in *Figure 1*.

Note 5: Limits at room temperature are guaranteed and 100% production tested. Limits in **boldface** are guaranteed over the operating temperature range (but not 100% tested), and are not used to calculate outgoing quality levels.

Note 6: The LMC7660 can operate without an external diode over the full temperature and voltage range. The LMC7660 can also be used with the external diode Dx, when replacing previous 7660 designs.

Note 7: The test circuit consists of the human body model of 100 pF in series with 1500Ω.



## Typical Performance Characteristics (Continued)

LMC7660





**Output R vs Supply Voltage** 







## **Application Information**

#### CIRCUIT DESCRIPTION

The LMC7660 contains four large CMOS switches which are switched in a sequence to provide supply inversion  $V_{out} = -V_{in}$ . Energy transfer and storage are provided by two inexpensive electrolytic capacitors. *Figure 2* shows how the LMC7660 can be used to generate  $-V^+$  from  $V^+$ . When

switches S1 and S3 are closed, C<sub>p</sub> charges to the supply voltage V<sup>+</sup>. During this time interval, switches S2 and S4 are open. After C<sub>p</sub> charges to V<sup>+</sup>, S1 and S3 are opened, S2 and S4 are then closed. By connecting S2 to ground, C<sub>p</sub> develops a voltage  $-V^{+}/2$  on C<sub>r</sub>. After a number of cycles C<sub>r</sub> will be pumped to exactly  $-V^{+}$ . This transfer will be exact assuming no load on C<sub>r</sub>, and no loss in the switches.

## Application Information (Continued)

In the circuit of *Figure 2*, S1 is a P-channel device and S2, S3, and S4 are N-channel devices. Because the output is biased below ground, it is important that the p<sup>-</sup> wells of S3 and S4 never become forward biased with respect to either their sources or drains. A substrate logic circuit guarantees that these p<sup>-</sup> wells are always held at the proper voltage. Under all conditions S4 p<sup>-</sup> well must be at the lowest potential in the circuit. To switch off S4, a level translator generates  $V_{GS4} = 0V$ , and this is accomplished by biasing the level translator from the S4 p<sup>-</sup> well.

An internal RC oscillator and  $\div 2$  circuit provide timing signals to the level translator. The built-in regulator biases the oscillator and divider to reduce power dissipation on high supply voltage. The regulator becomes active at about V<sup>+</sup> = 6.5V. Low voltage operation can be improved if the LV pin is shorted to ground for V<sup>+</sup>  $\leq$  3.5V. For V<sup>+</sup>  $\geq$  3.5V, the LV pin must be left open to prevent damage to the part.

#### POWER EFFICIENCY AND RIPPLE

It is theoretically possible to approach 100% efficiency if the following conditions are met:

- 1. The drive circuitry consumes little power.
- 2. The power switches are matched and have low Ron.
- 3. The impedance of the reservoir and pump capacitors are negligibly small at the pumping frequency.

The LMC7660 closely approaches 1 and 2 above. By using a large pump capacitor  $C_{\rm p}$ , the charge removed while supplying the reservoir capacitor is small compared to  $C_{\rm p}$ 's total charge. Small removed charge means small changes in the pump capacitor voltage, and thus small energy loss and high efficiency. The energy loss by  $C_{\rm p}$  is:

$$E = \frac{1}{2}C_{p}(V1^{2} - V2^{2})$$

By using a large reservoir capacitor, the output ripple can be reduced to an acceptable level. For example, if the load current is 5 mA and the accepted ripple is 200 mV, then the reservoir capacitor can omit approximately be calculated from:

LMC7660

$$\begin{split} \text{Is} &= C_{\text{r}} \frac{dv}{dt} \\ \sim C_{\text{r}} \times \frac{V_{\text{ripple p-p}}}{4/F_{\text{osc}}} \qquad C_{\text{r}} = \frac{0.5 \text{ mA}}{0.5 \text{V/ms}} = 10 \ \mu\text{F} \end{split}$$

#### PRECAUTIONS

- 1. Do not exceed the maximum supply voltage or junction temperature.
- 2. Do not short pin 6 (LV terminal) to ground for supply voltages greater than 3.5V.
- 3. Do not short circuit the output to V<sup>+</sup>.
- 4. External electrolytic capacitors  $C_r$  and  $C_p$  should have their polarities connected as shown in *Figure 1*.

#### **REPLACING PREVIOUS 7660 DESIGNS**

To prevent destructive latchup, previous 7660 designs require a diode in series with the output when operated at elevated temperature or supply voltage. Although this prevented the latchup problem of these designs, it lowered the available output voltage and increased the output series resistance.

The National LMC7660 has been designed to solve the inherent latch problem. The LCM7660 can operate over the entire supply voltage and temperature range without the need for an output diode. When replacing existing designs, the LMC7660 can be operated with diode Dx.



00913606

FIGURE 2. Idealized Voltage Converter

## **Typical Applications**

#### CHANGING OSCILLATOR FREQUENCY

It is possible to dramatically reduce the quiescent operating current of the LMC7660 by lowering the oscillator frequency. The oscillator frequency can be lowered from a nominal 10 kHz to several hundred hertz, by adding a slow-down capacitor  $C_{\rm osc}$  (*Figure 3*). As shown in the Typical Performance Curves the supply current can be lowered to the 10  $\mu$ A range. This low current drain can be extremely useful when used in  $\mu$ Power and battery back-up equipment. It must be

understood that the lower operating frequency and supply current cause an increased impedance of C<sub>r</sub> and C<sub>p</sub>. The increased impedance, due to a lower switching rate, can be offset by raising C<sub>r</sub> and C<sub>p</sub> until ripple and load current requirements are met.

#### SYNCHRONIZING TO AN EXTERNAL CLOCK

*Figure 4* shows an LMC7660 synchronized to an external clock. The CMOS gate overrides the internal oscillator when it is necessary to switch faster or reduce power supply

5

## Typical Applications (Continued)

circuit in the 7660, so the pumping frequency will be  $1\!\!/_2$  the external clock frequency.

interference. The external clock still passes through the  $\div 2$ 









#### LOWERING OUTPUT IMPEDANCE

Paralleling two or more LMC7660's lowers output impedance. Each device must have it's own pumping capacitor  $C_p$ , but the reservoir capacitor  $C_r$  is shared as depicted in *Figure 5*. The composite output resistance is:  $\mathsf{R}_{out} = \frac{\mathsf{R}_{out} \text{ of one LMC7660}}{\mathsf{Number of devices}}$ 

## Typical Applications (Continued)

#### INCREASING OUTPUT VOLTAGE

Stacking the LMC7660s is an easy way to produce a greater negative voltage. It should be noted that the input current required for each stage is twice the load current on that stage as shown in *Figure 6*. The effective output resistance

is approximately the sum of the individual  ${\rm R}_{\rm out}$  values, and so only a few levels of multiplication can be used.

LMC7660

It is possible to generate -15V from +5V by connecting the second 7660's pin 8 to +5V instead of ground as shown in *Figure 7*. Note that the second 7660 sees a full 20V and the input supply should not be increased beyond +5V.



FIGURE 5. Lowering Output Resistance by Paralleling Devices



FIGURE 6. Higher Voltage by Cascade





#### SPLIT V<sup>+</sup> IN HALF

*Figure 8* is one of the more interesting applications for the LMC7660. The circuit can be used as a precision voltage divider (for very light loads), alternately it is used to generate a  $\frac{1}{2}$  supply point in battery applications. In the  $\frac{1}{2}$  cycle when

S1 and S3 are closed, the supply voltage divides across the capacitors in a conventional way proportional to their value. In the  $\frac{1}{2}$  cycle when S2 and S4 are closed, the capacitors switch from a series connection to a parallel connection. This forces the capacitors to have the same voltage; the charge

### Typical Applications (Continued)

redistributes to maintain precisely V<sup>+</sup>/2, across C<sub>p</sub> and C<sub>r</sub>. In this application all devices are only V<sup>+</sup>/2, and the supply voltage can be raised to 20V giving exactly 10V at V<sub>out</sub>.

#### GETTING UP ... AND DOWN

The LMC7660 can also be used as a positive voltage multiplier. This application, shown in *Figure 9*, requires 2 additional diodes. During the first  $\frac{1}{2}$  cycle S2 charges  $C_p1$  through D1; D2 is reverse biased. In the next  $\frac{1}{2}$  cycle S2 is

open and S1 is closed. Since  $C_p1$  is charged to  $V^+ - V_{D1}$  and is referenced to  $V^+$  through S1, the junction of D1 and D2 is at  $V^+ + (V^+ - V_{D1})$ . D1 is reverse biased in this interval. This application uses only two of the four switches in the 7660. The other two switches can be put to use in performing a negative conversion at the same time as shown in *Figure 10*. In the  $1/_2$  cycle that D1 is charging  $C_p1$ ,  $C_p2$  is connected from ground to  $-V_{out}$  via S2 and S4, and  $C_r2$  is storing  $C_p2$ 's charge. In the interval that S1 and S3 are closed,  $C_p1$  pumps the junction of D1 and D2 above V<sup>+</sup>, while  $C_p2$  is refreshed from V<sup>+</sup>.



FIGURE 8. Split V<sup>+</sup> in Half



FIGURE 9. Positive Voltage Multiplier



FIGURE 10. Combined Negative Converter and Positive Multiplier

#### THERMOMETER SPANS 180°C

Using the combined negative and positive multiplier of *Figure 11* with an LM35 it is possible to make a µPower thermometer that spans a 180°C temperature range. The LM35 temperature sensor has an output sensitivity of 10 mV/°C, while drawing only 50 µA of quiescent current. In order for

the LM35 to measure negative temperatures, a pull down to a negative voltage is required. *Figure 11* shows a thermometer circuit for measuring temperatures from  $-55^{\circ}$ C to +125°C and requiring only two 1.5V cells. End of battery life can be extended by replacing the up converter diodes with Schottky's.

## Typical Applications (Continued)

#### **REGULATING -VOUT**

It is possible to regulate the output of the LMC7660 and still maintain  $\mu$ Power performance. This is done by enclosing the LMC7660 in a loop with a LP2951. The circuit of *Figure 12* will regulate V<sub>out</sub> to -5V for I<sub>L</sub> = 10 mA, and V<sub>in</sub> = 6V. For V<sub>in</sub> > 7V, the output stays in regulation up to I<sub>L</sub> = 25 mA. The error flag on pin 5 of the LP2951 sets low when the regulated output at pin 4 drops by about 5%. The LP2951 can be shutdown by taking pin 3 high; the LMC7660 can be shutdown by shorting pin 7 and pin 8.

The LP2951 can be reconfigured to an adjustable type regulator, which means the LMC7660 can give a regulated output from -2.0V to -10V dependent on the resistor ratios R1 and R2, as shown in *Figure 13*, V<sub>ref</sub> = 1.235V:

$$V_{out} = V_{ref} \left( 1 + \frac{R1}{R2} \right)$$



\*For lower voltage operation, use Schottky rectifiers





FIGURE 12. Regulated -5V with 200 µA Standby Current

## Typical Applications (Continued)



#### V<sub>ref</sub> = 1.235V \*Low voltage operation

FIGURE 13. LMC7660 and LP2951 Make a Negative Adjustable Regulator





#### **IMPORTANT NOTICE**

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

| Products               |                                 | Applications                  |                                   |
|------------------------|---------------------------------|-------------------------------|-----------------------------------|
| Audio                  | www.ti.com/audio                | Communications and Telecom    | www.ti.com/communications         |
| Amplifiers             | amplifier.ti.com                | Computers and Peripherals     | www.ti.com/computers              |
| Data Converters        | dataconverter.ti.com            | Consumer Electronics          | www.ti.com/consumer-apps          |
| DLP® Products          | www.dlp.com                     | Energy and Lighting           | www.ti.com/energy                 |
| DSP                    | dsp.ti.com                      | Industrial                    | www.ti.com/industrial             |
| Clocks and Timers      | www.ti.com/clocks               | Medical                       | www.ti.com/medical                |
| Interface              | interface.ti.com                | Security                      | www.ti.com/security               |
| Logic                  | logic.ti.com                    | Space, Avionics and Defense   | www.ti.com/space-avionics-defense |
| Power Mgmt             | power.ti.com                    | Transportation and Automotive | www.ti.com/automotive             |
| Microcontrollers       | microcontroller.ti.com          | Video and Imaging             | www.ti.com/video                  |
| RFID                   | www.ti-rfid.com                 |                               |                                   |
| OMAP Mobile Processors | www.ti.com/omap                 |                               |                                   |
| Wireless Connectivity  | www.ti.com/wirelessconnectivity |                               |                                   |
|                        |                                 |                               |                                   |

**TI E2E Community Home Page** 

e2e.ti.com

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2011, Texas Instruments Incorporated