



ROHS



Features

- ♦ UL Recognized File # E-326243
- ♦ Dual rectifier construction, positive center-tap
- Plastic package has Underwriters Laboratory Flammability Classifications 94V-0
- ♦ Metal silicon junction, majority carrier conduction
- ♦ Low power loss, high efficiency
- ♦ High current capability, low VF
- ♦ High surge reliabbility
- ♦ Epitaxial construction
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection
- ♦ Guardring for transient protection
- High temperature soldering guaranteed: 260°C/10 seconds, 0.17"(4.3mm) lead lengths at 5 lbs., (2.3kg) tension
- ♦ Green compound with suffix "G" on packing code & prefix "G" on datecode.

Mechanical Data

- ♦ Cases: JEDEC TO-3P/TO-247AD molded plastic
- Terminals: Pure tin plated, lead free, solderable per MIL-STD-750, Method 2026
- ♦ Polarity: As marked
- ♦ Mounting position: Any
- ♦ Weight: 6.10 grams

.225(5.7) .205(5.2) .044(1.12)

PIN 1

PIN 3

.795(20.2) .775(19.7) SR2020PT - SR20150PT

.078(1.98)

.193(4.90)

BOTH SIDES

20.0AMPS. Schottky Barrier Rectifiers

TO-3P/TO-247AD

.323(8.2)

.17(4.3)

Dimensions in inches and (millimeters)

Marking Diagram SR20XXPT = Specific Device Code G = Green Compound Y = Year WW = Work Week

Maximum Ratings and Electrical Characteristics

Rating at 25 $^{\circ}$ C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%

		2020 PT	2030 PT	2040 PT	2050 PT	2060 PT	2090 PT	20100 PT	20150 PT	Units
/oltage	V_{RRM}	20	30	40	50	60	90	100	150	V
	V_{RMS}	14	21	28	35	42	63	70	105	V
	V_{DC}	20	30	40	50	60	90	100	150	V
i	I _{F(AV)}	20							Α	
-	I _{FSM}	200							Α	
oltage (Note 1)	V _F	0.55			0.	70	0.92		1.02	V
@ T _A =25 °C							0.1		mA	
@ T _A =100°C	I_R	15			10			-		mA
@ T _A =125°C				-			5.0		mA	
2)	Cj	600			400			350		pF
(Note 3)	$R_{\theta JC}$	1.5					°C/W			
nge	TJ	- 65 to + 125 - 65 to + 15				50		οС		
	T _{STG}	- 65 to + 150						οС		
	@ T _A =100°C	$\begin{array}{c c} V_{RMS} \\ V_{DC} \\ \end{array}$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	V _{RMS}	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	V _{RMS} 14 21 28 35 V _{DC} 20 30 40 50 d I _{F(AV)} 2 s Single Half Sine- (JEDEC method) I _{FSM} 20 oltage (Note 1) V _F 0.55 0. @ T _A =25 °C @ T _A =100°C I _R 15 1 @ T _A =125°C - - - 20 Cj 600 44 (Note 3) R _{θ,JC} 1 -65 to + 125	V _{RMS} 14 21 28 35 42 V _{DC} 20 30 40 50 60 60 1 I _{F(AV)} 20 20 30 40 50 60 60 60 60 60 60 6	V _{RMS} 14 21 28 35 42 63 V _{DC} 20 30 40 50 60 90 Single Half Sine- (JEDEC method) I _{FSM} 200 20 200 <td>V_{RMS} 14 21 28 35 42 63 70 V_{DC} 20 30 40 50 60 90 100 Single Half Sine- (JEDEC method) I_{FSM} 200<td>V_{RMS} 14 21 28 35 42 63 70 105 V_{DC} 20 30 40 50 60 90 100 150 Single Half Sine- (JEDEC method) I_{FSM} 200</td></td>	V _{RMS} 14 21 28 35 42 63 70 V _{DC} 20 30 40 50 60 90 100 Single Half Sine- (JEDEC method) I _{FSM} 200 200 <td>V_{RMS} 14 21 28 35 42 63 70 105 V_{DC} 20 30 40 50 60 90 100 150 Single Half Sine- (JEDEC method) I_{FSM} 200</td>	V _{RMS} 14 21 28 35 42 63 70 105 V _{DC} 20 30 40 50 60 90 100 150 Single Half Sine- (JEDEC method) I _{FSM} 200 200

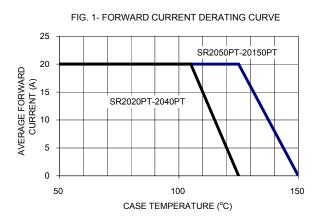
Note 1:300 us Pulse Width, 2% Duty Cycle

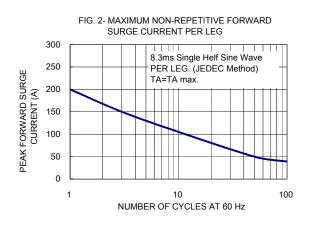
Note 2: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.

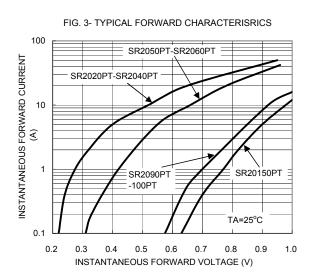
Note 3: Mount on Heatsink size of 4" x 6" x 0.25" Al-Plate.

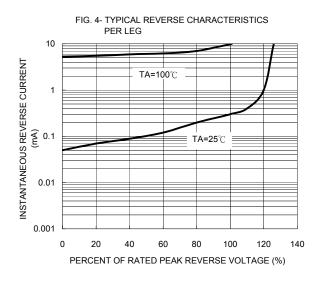


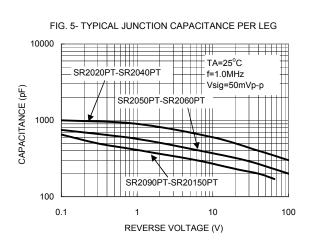
RATINGS AND CHARACTERISTIC CURVES (SR2020PT THRU SR20150PT)

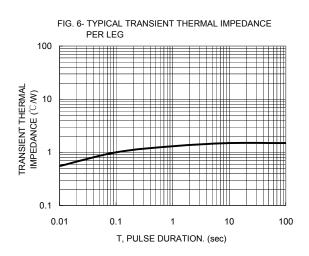












Version:D10