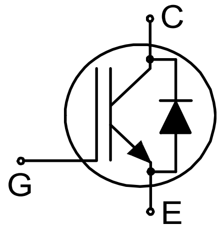
IKW75N65ET7

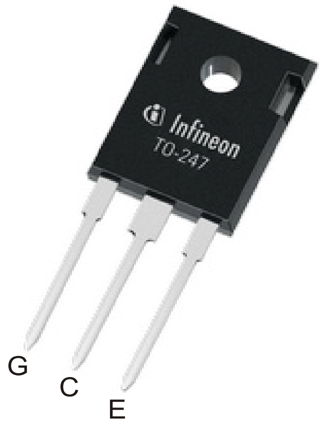
TRENCHSTOP^{TM} IGBT 7

Low Loss Duopack: IGBT with Trench and Fieldstop technology



Features:

TRENCHSTOP^{TM} IGBT 7 technology offering
- Very low V_{CEsat}
- Low turn-off losses
- Short tail current
- Reduced EMI
- Humidity robust design
- Very soft, fast recovery anti-parallel diode
- Maximum junction temperature 175°C
- Qualified according to JEDEC for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models:
http://www.infineon.com/igbt7/



Applications:

- Drives
  - -Servo
  - -GPD
- Industrial Power Supplies
  - -Industrial UPS
  - -Residential UPS
- Energy Generation
  - -Solar Central Inverter
  - -Solar String Inverter
  - -Solar Pump

Product Validation:

Qualified for industrial applications according to the relevant tests of JEDEC47/20/22

Package pin definition:

- Pin C & backside - Collector
- Pin E - Emitter
- Pin G - Gate

Key Performance and Package Parameters

Type	V_{CE}	I_C	V_{CEsat}, T_{vj}=25°C	T_{vjmax}	Marking	Package
IKW75N65ET7	650V	75A	1.35V	175°C	K75EET7	PG-TO247-3

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Maximum Ratings

For optimum lifetime and reliability, Infineon recommends operating conditions that do not exceed 80% of the maximum ratings stated in this datasheet.

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_{vj} \geq 25°C$	V_{CE}	650	V
DC collector current, limited by T_{vjmax} $T_c = 25°C$ value limited by bondwire $T_c = 100°C$	I_C	80.0 78.5	A
Pulsed collector current, t_p limited by T_{vjmax} [1]	I_{Cpuls}	225.0	A
Turn off safe operating area $V_{CE} \leq 650V$, $T_{vj} \leq 175°C$, $t_p = 1\mu s$ [2]	-	225.0	A
Diode forward current, limited by T_{vjmax} $T_c = 25°C$ value limited by bondwire $T_c = 100°C$	I_F	80.0 74.0	A
Diode pulsed current, t_p limited by T_{vjmax} [1]	I_{Fpuls}	225.0	A
Gate-emitter voltage Transient Gate-emitter voltage ($t_p \leq 10\mu s$, $D < 0.010$)	V_{GE}	±20 ±30	V
Short circuit withstand time $V_{GE} = 15.0V$, $V_{CC} \leq 400V$ Allowed number of short circuits < 1000 Time between short circuits: ≥ 1.0s $T_{vj} = 150°C$	t_{SC}	3	µs
Short circuit withstand time $V_{GE} = 15.0V$, $V_{CC} \leq 330V$ Allowed number of short circuits < 1000 Time between short circuits: ≥ 1.0s $T_{vj} = 100°C$	t_{SC}	5	µs
Power dissipation $T_c = 25°C$ Power dissipation $T_c = 100°C$	P_{tot}	333.0 167.0	W
Operating junction temperature	T_{vj}	-40...+175	°C
Storage temperature	T_{stg}	-55...+150	°C
Soldering temperature, wave soldering 1.6mm (0.063in.) from case for 10s		260	°C
Mounting torque, M3 screw Maximum of mounting processes: 3	M	0.6	Nm

Thermal Resistance

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
R_{th} Characteristics						
IGBT thermal resistance, junction - case	$R_{th(j-c)}$		-	-	0.45	K/W
Diode thermal resistance, junction - case	$R_{th(j-c)}$		-	-	0.60	K/W
Thermal resistance junction - ambient	$R_{th(j-a)}$		-	-	40	K/W

1) Defined by design. Not subject to production test.
2) Clamped inductive load current test for each device, I_C=225A, V_{CC}=400V, T_c=25°C, V_{GE}=20V, L=80µH, R_G=10Ω.

Electrical Characteristic, at T_{vj} = 25°C, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
Collector-emitter saturation voltage	V_{CEsat}	V_{GE} = 15.0V, I_C = 75.0A T_{vj} = 25°C T_{vj} = 125°C T_{vj} = 175°C	- - -	1.35 1.50 1.60	1.65 - -	V
Diode forward voltage	V_F	V_{GE} = 0V, I_F = 75.0A T_{vj} = 25°C T_{vj} = 125°C T_{vj} = 175°C	- - -	1.65 1.60 1.55	2.00 - -	V
Gate-emitter threshold voltage	$V_{GE(th)}$	I_C = 0.75mA, V_{CE} = V_{GE}	4.3	5.0	5.7	V
Zero gate voltage collector current	I_{CES}	V_{CE} = 650V, V_{GE} = 0V T_{vj} = 25°C T_{vj} = 175°C	- -	- 1200	40 -	µA
Gate-emitter leakage current	I_{GES}	V_{CE} = 0V, V_{GE} = 20V	-	-	100	nA
Transconductance	g_{fs}	V_{CE} = 20V, I_C = 75.0A	-	40.0	-	S

Electrical Characteristic, at T_{vj} = 25°C, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Dynamic Characteristic						
Input capacitance	C_{ies}	V_{CE} = 25V, V_{GE} = 0V f = 1000kHz	-	4460	-	pF
Output capacitance	C_{oes}		-	135	-	
Reverse transfer capacitance	C_{res}		-	46	-	
Gate charge	Q_G	V_{CC} = 520V, I_C = 75.0A, V_{GE} = 15V	-	435.0	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	13.0	-	nH
Short circuit collector current Max. 1000 short circuits Time between short circuits: ≥ 1.0s	$I_{C(SC)}$	V_{GE} = 15.0V, V_{CC} ≤ 400V, t_{SC} ≤ 3µs T_{vj} = 150°C	-	350	-	A

Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic, at T_{vj} = 25°C						
Turn-on delay time	$t_{d(on)}$	T_{vj} = 25°C, V_{CC} = 400V, I_C = 75.0A, V_{GE} = 0.0/15.0V, $R_{G(on)}$ = 4.7Ω, $R_{G(off)}$ = 4.7Ω, $L\sigma$ = 32nH, $C\sigma$ = 30pF $L\sigma$, $C\sigma$ from Fig. E Energy losses include “tail” and diode reverse recovery.	-	28	-	ns
Rise time	t_r		-	25	-	ns
Turn-off delay time	$t_{d(off)}$		-	310	-	ns
Fall time	t_f		-	15	-	ns
Turn-on energy	E_{on}		-	2.17	-	mJ
Turn-off energy	E_{off}		-	1.23	-	mJ
Total switching energy	E_{ts}		-	3.40	-	mJ

Turn-on delay time	$t_{d(on)}$	T_{vj} = 25°C, V_{CC} = 400V, I_C = 37.5A, V_{GE} = 0.0/15.0V, $R_{G(on)}$ = 4.7Ω, $R_{G(off)}$ = 4.7Ω, $L\sigma$ = 32nH, $C\sigma$ = 30pF $L\sigma$, $C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	26	-	ns
Rise time	t_r		-	13	-	ns
Turn-off delay time	$t_{d(off)}$		-	330	-	ns
Fall time	t_f		-	11	-	ns
Turn-on energy	E_{on}		-	0.79	-	mJ
Turn-off energy	E_{off}		-	0.56	-	mJ
Total switching energy	E_{ts}		-	1.35	-	mJ

Diode Characteristic, at T_{vj} = 25°C

Diode reverse recovery time	t_{rr}	T_{vj} = 25°C, V_R = 400V, I_F = 75.0A, di_F/dt = 1650A/µs	-	100	-	ns
Diode reverse recovery charge	Q_{rr}		-	1.50	-	µC
Diode peak reverse recovery current	I_{rrm}		-	22.0	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-480	-	A/µs

Diode reverse recovery time	t_{rr}	T_{vj} = 25°C, V_R = 400V, I_F = 37.5A, di_F/dt = 2725A/µs	-	70	-	ns
Diode reverse recovery charge	Q_{rr}		-	1.15	-	µC
Diode peak reverse recovery current	I_{rrm}		-	34.0	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-600	-	A/µs

Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic, at T_{vj} = 175°C						
Turn-on delay time	$t_{d(on)}$	T_{vj} = 175°C, V_{CC} = 400V, I_C = 75.0A, V_{GE} = 0.0/15.0V, $R_{G(on)}$ = 4.7Ω, $R_{G(off)}$ = 4.7Ω, $L\sigma$ = 32nH, $C\sigma$ = 30pF $L\sigma$, $C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	31	-	ns
Rise time	t_r		-	30	-	ns
Turn-off delay time	$t_{d(off)}$		-	365	-	ns
Fall time	t_f		-	25	-	ns
Turn-on energy	E_{on}		-	3.45	-	mJ
Turn-off energy	E_{off}		-	2.05	-	mJ
Total switching energy	E_{ts}		-	5.50	-	mJ

Turn-on delay time	$t_{d(on)}$	T_{vj} = 175°C, V_{CC} = 400V, I_C = 37.5A, V_{GE} = 0.0/15.0V, $R_{G(on)}$ = 4.7Ω, $R_{G(off)}$ = 4.7Ω, $L\sigma$ = 32nH, $C\sigma$ = 30pF $L\sigma$, $C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	28	-	ns
Rise time	t_r		-	18	-	ns
Turn-off delay time	$t_{d(off)}$		-	415	-	ns
Fall time	t_f		-	20	-	ns
Turn-on energy	E_{on}		-	1.52	-	mJ
Turn-off energy	E_{off}		-	1.11	-	mJ
Total switching energy	E_{ts}		-	2.63	-	mJ

Diode Characteristic, at T_{vj} = 175°C

Diode reverse recovery time	t_{rr}	T_{vj} = 175°C, V_R = 400V, I_F = 75.0A, di_F/dt = 1650A/µs	-	155	-	ns
Diode reverse recovery charge	Q_{rr}		-	4.40	-	µC
Diode peak reverse recovery current	I_{rrm}		-	41.0	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-590	-	A/µs

Diode reverse recovery time	t_{rr}	T_{vj} = 175°C, V_R = 400V, I_F = 37.5A, di_F/dt = 2260A/µs	-	125	-	ns
Diode reverse recovery charge	Q_{rr}		-	3.36	-	µC
Diode peak reverse recovery current	I_{rrm}		-	43.0	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-500	-	A/µs

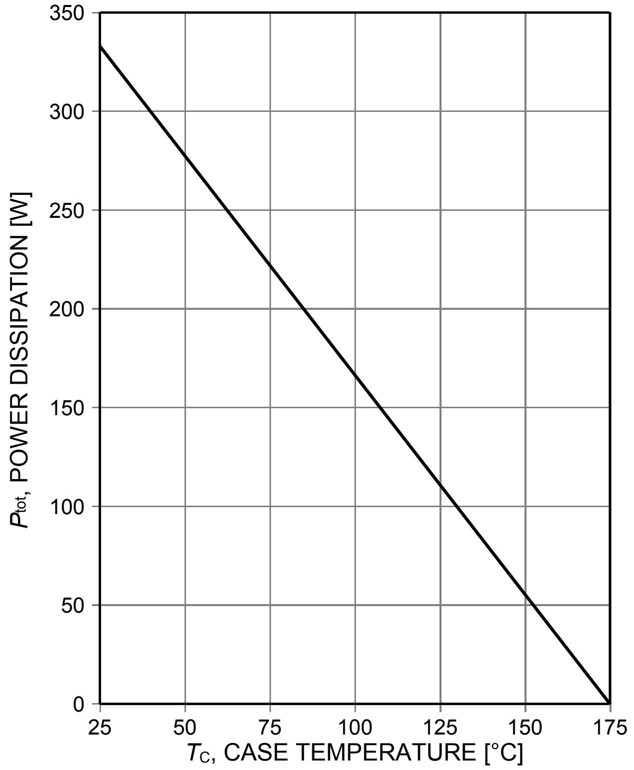


Figure 1. **Power dissipation as a function of case temperature**
(T_{vj}≤175°C)

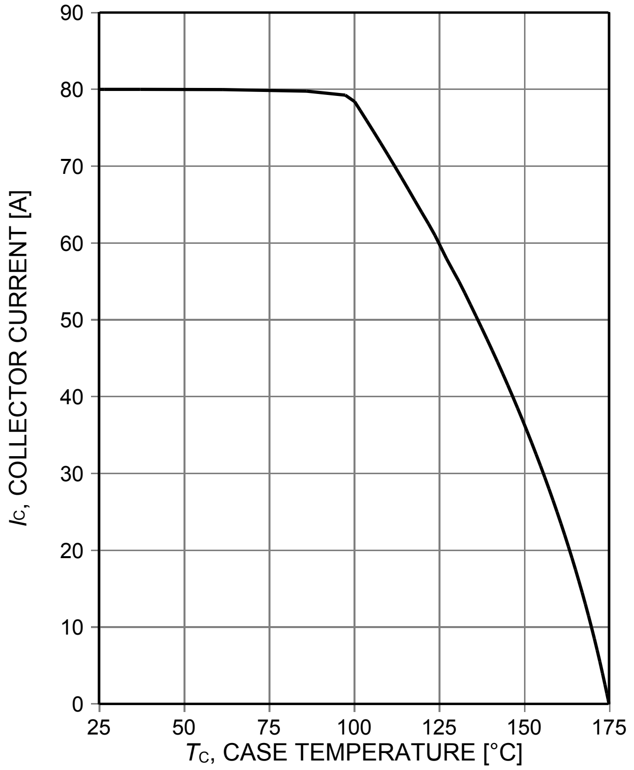


Figure 2. **Collector current as a function of case temperature**
(V_{GE}≥15V, T_{vj}≤175°C)

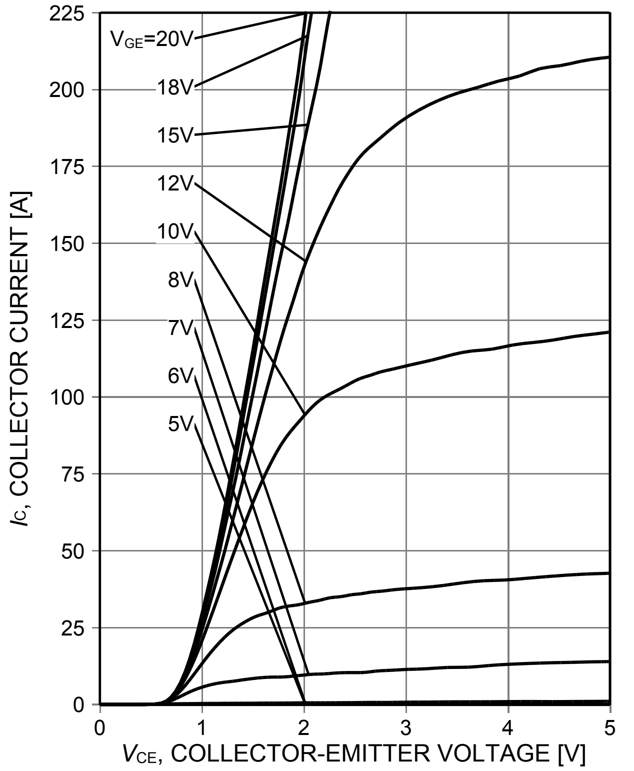


Figure 3. **Typical output characteristic**
(T_{vj}=25°C)

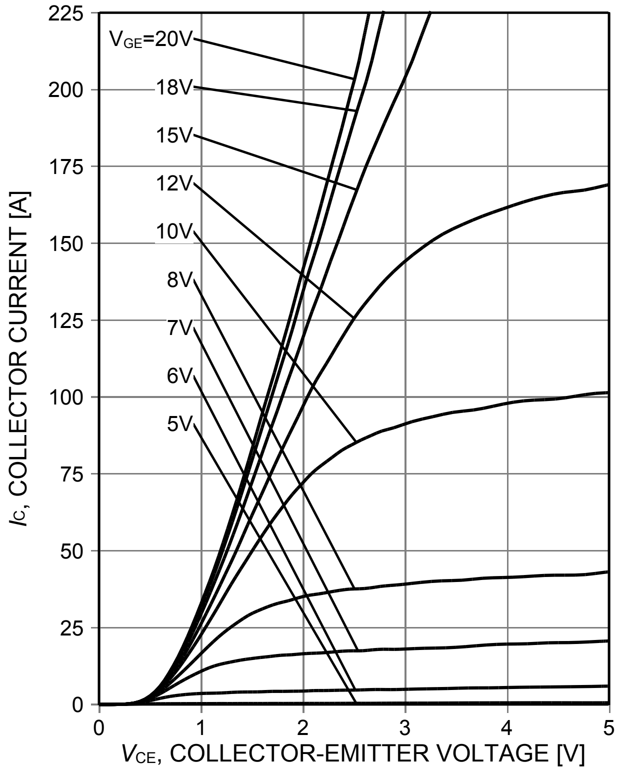


Figure 4. **Typical output characteristic**
(T_{vj}=175°C)

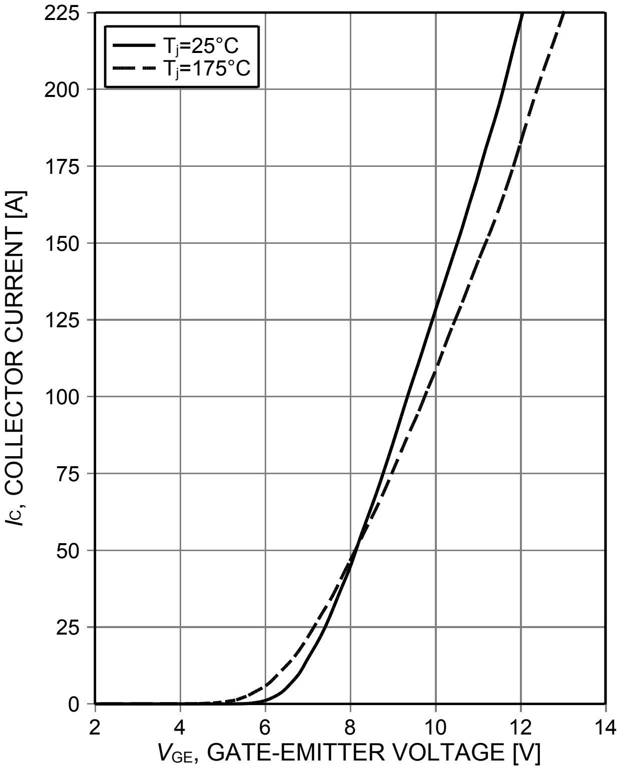


Figure 5. **Typical transfer characteristic**
(V_{CE}=20V)

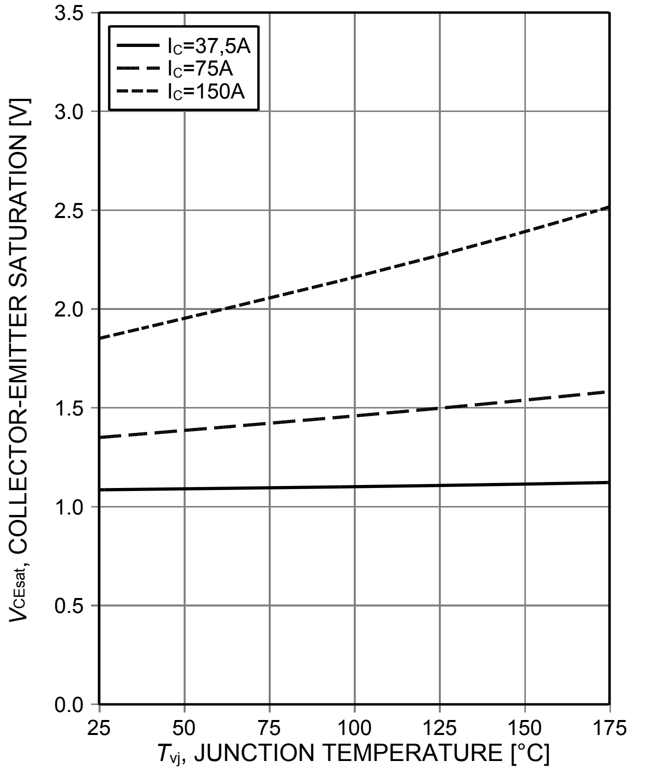


Figure 6. **Typical collector-emitter saturation voltage as a function of junction temperature**
(V_{GE}=15V)

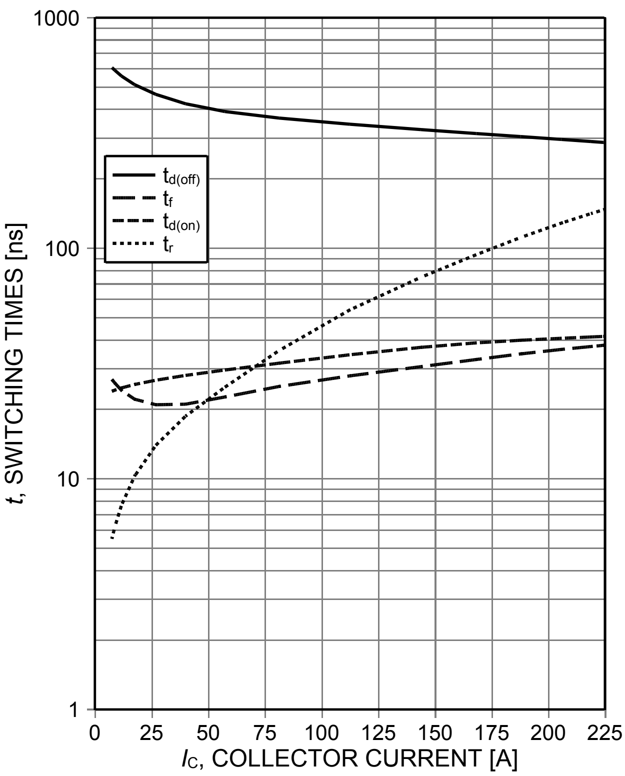


Figure 7. **Typical switching times as a function of collector current**
(inductive load, T_{vj}=175°C, V_{CE}=400V, V_{GE}=15/0V, R_G=4.7Ω, Dynamic test circuit in Figure E)

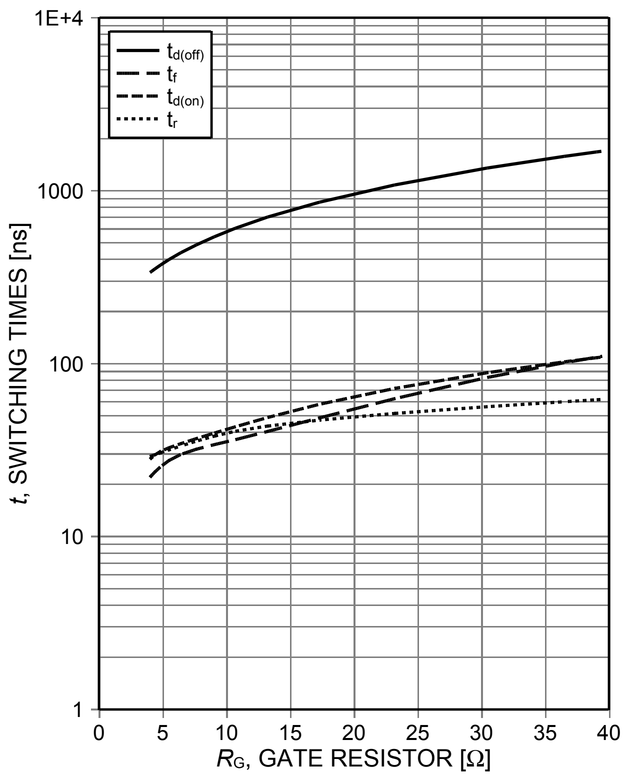


Figure 8. **Typical switching times as a function of gate resistor**
(inductive load, T_{vj}=175°C, V_{CE}=400V, V_{GE}=15/0V, I_C=75A, Dynamic test circuit in Figure E)

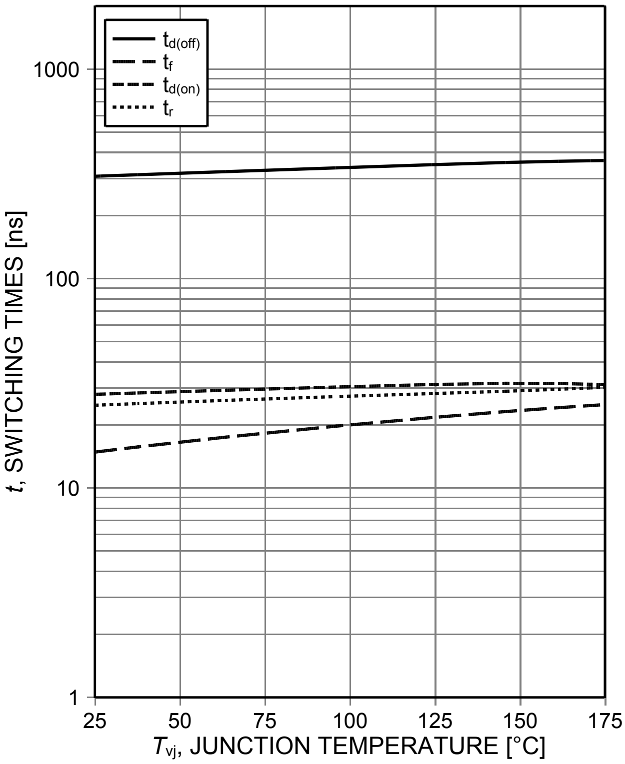


Figure 9. **Typical switching times as a function of junction temperature**
(inductive load, V_{CE}=400V, V_{GE}=15/0V, I_C=75A, R_G=4.7Ω, Dynamic test circuit in Figure E)

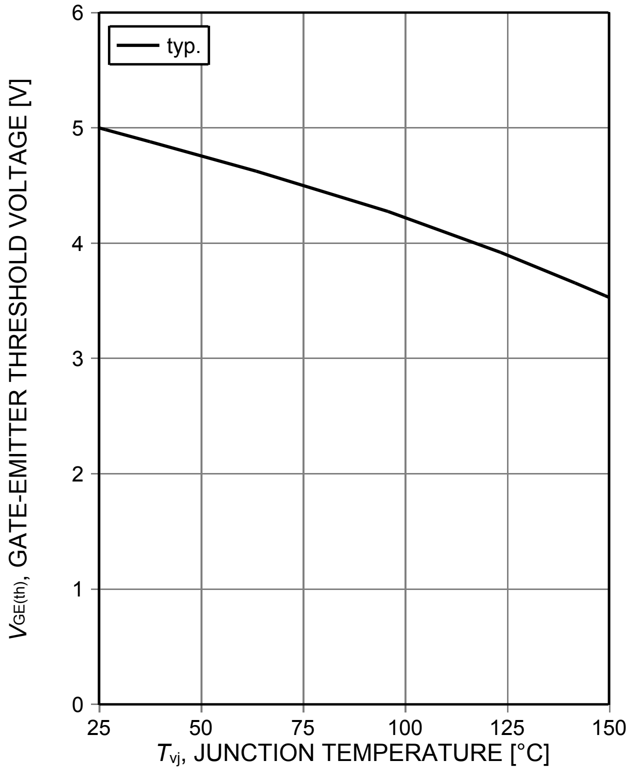


Figure 10. **Gate-emitter threshold voltage as a function of junction temperature**
(I_C=0.75mA)

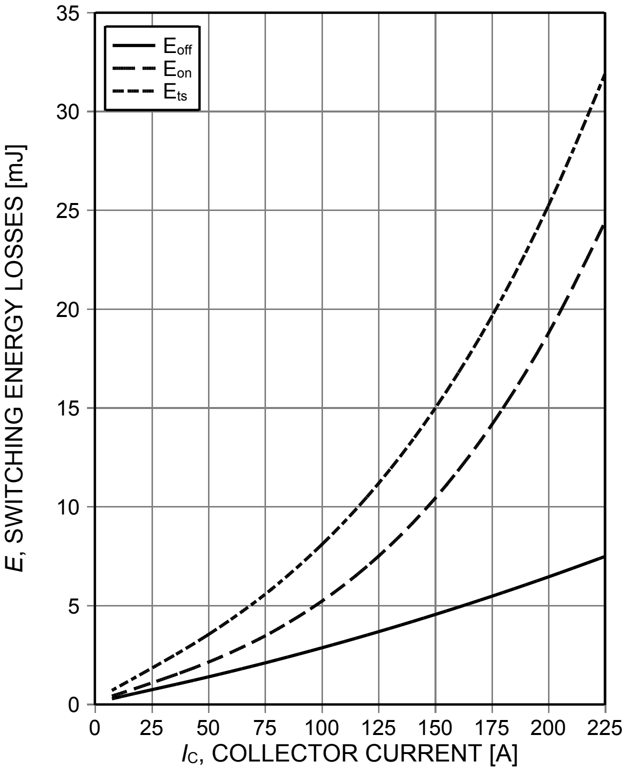


Figure 11. **Typical switching energy losses as a function of collector current**
(inductive load, T_{vj}=175°C, V_{CE}=400V, V_{GE}=15/0V, R_G=4.7Ω, Dynamic test circuit in Figure E)

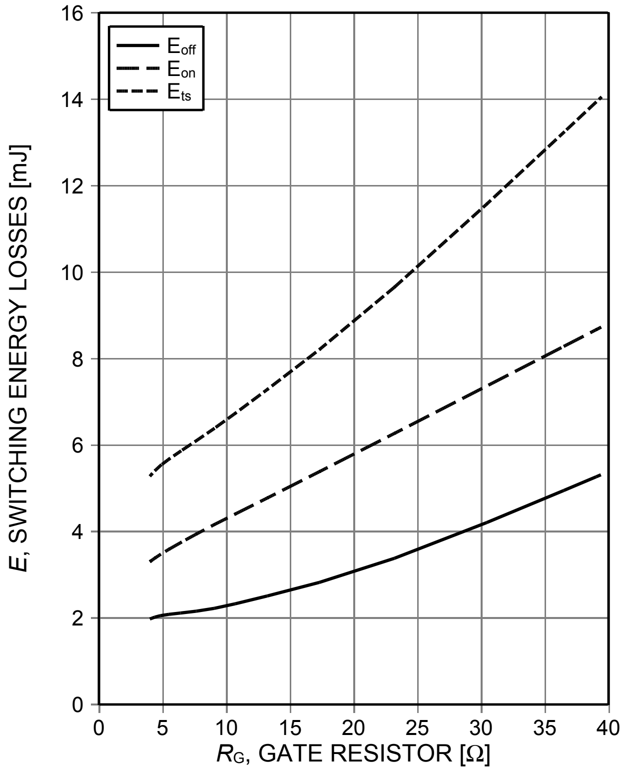


Figure 12. **Typical switching energy losses as a function of gate resistor**
(inductive load, T_{vj}=175°C, V_{CE}=400V, V_{GE}=15/0V, I_C=75A, Dynamic test circuit in Figure E)

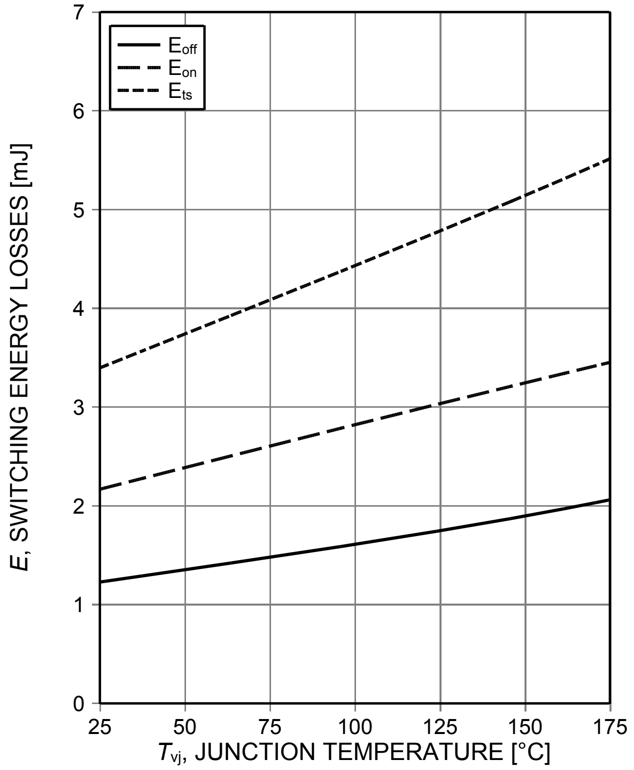


Figure 13. **Typical switching energy losses as a function of junction temperature**
(inductive load, V_{CE}=400V, V_{GE}=15/0V, I_C=75A, R_G=4.7Ω, Dynamic test circuit in Figure E)

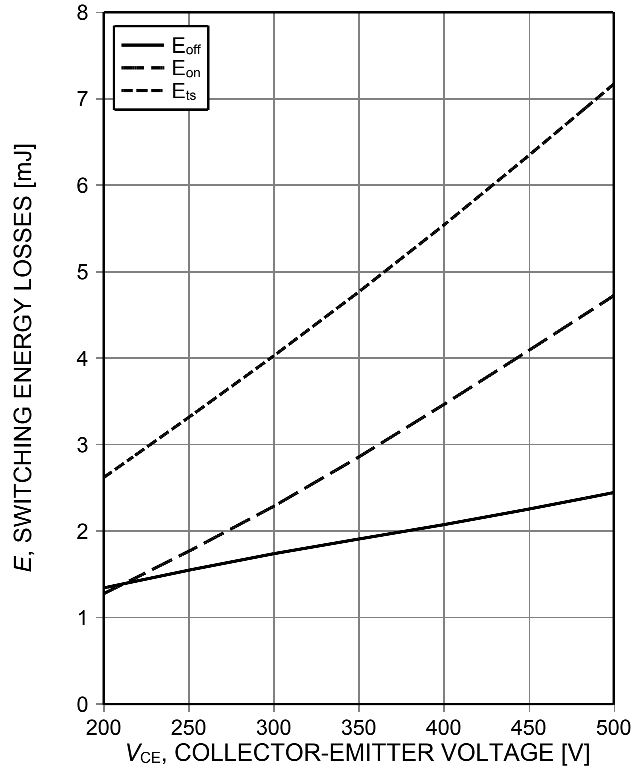


Figure 14. **Typical switching energy losses as a function of collector emitter voltage**
(inductive load, T_{vj}=175°C, V_{GE}=15/0V, I_C=75A, R_G=4.7Ω, Dynamic test circuit in Figure E)

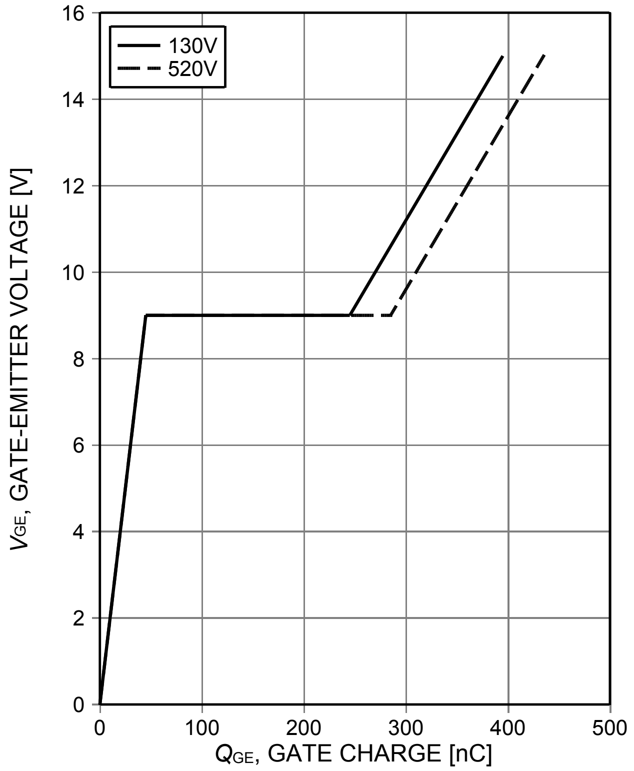


Figure 15. **Typical gate charge**
(I_C=75A)

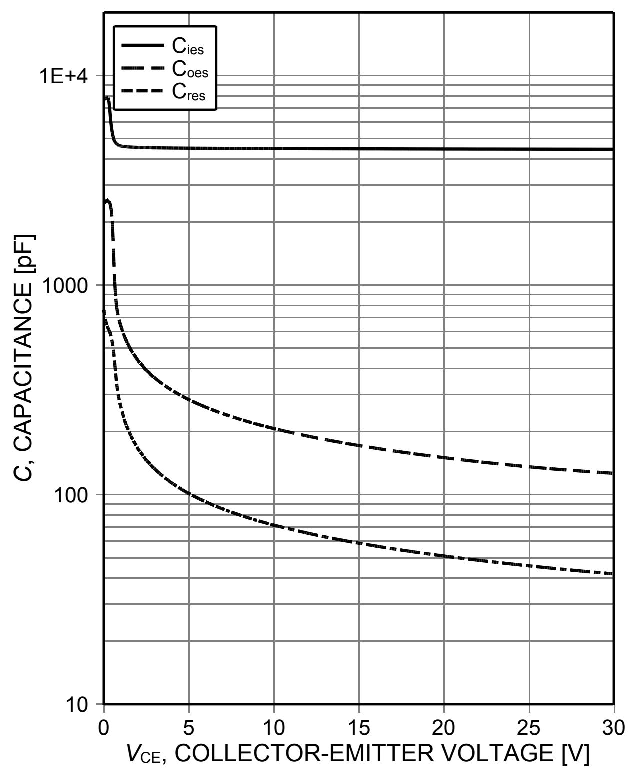


Figure 16. **Typical capacitance as a function of collector-emitter voltage**
(V_{GE}=0V, f=1MHz)

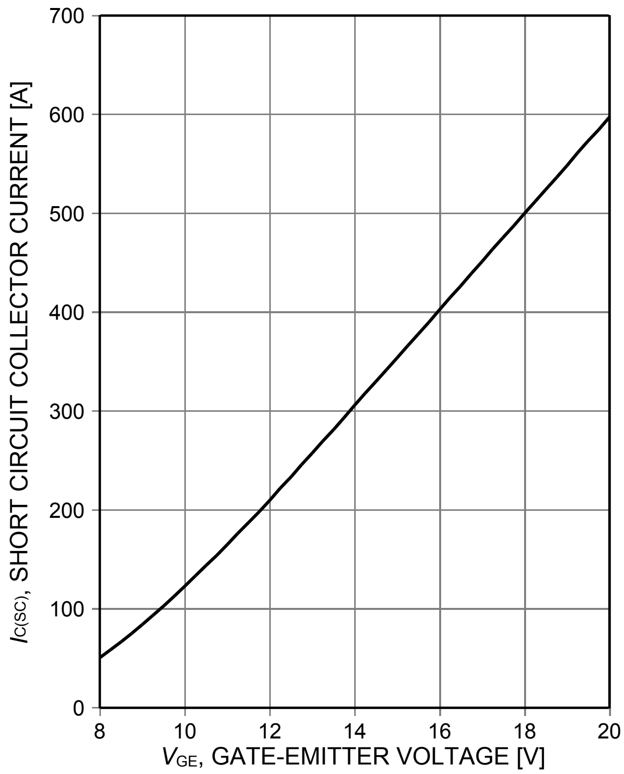


Figure 17. **Typical short circuit collector current as a function of gate-emitter voltage**
(V_{CE}=400V, T_{vj}=150°C)

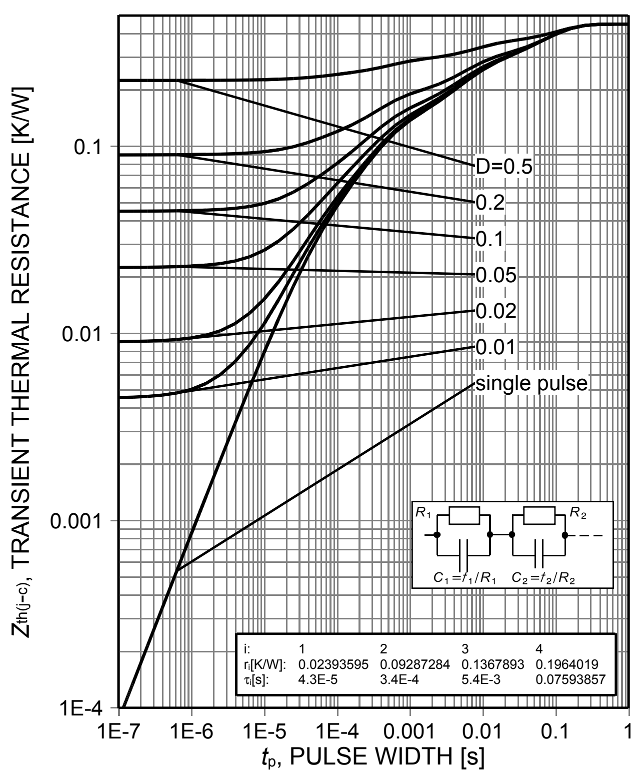


Figure 18. **IGBT transient thermal resistance**
(D=t_p/T)

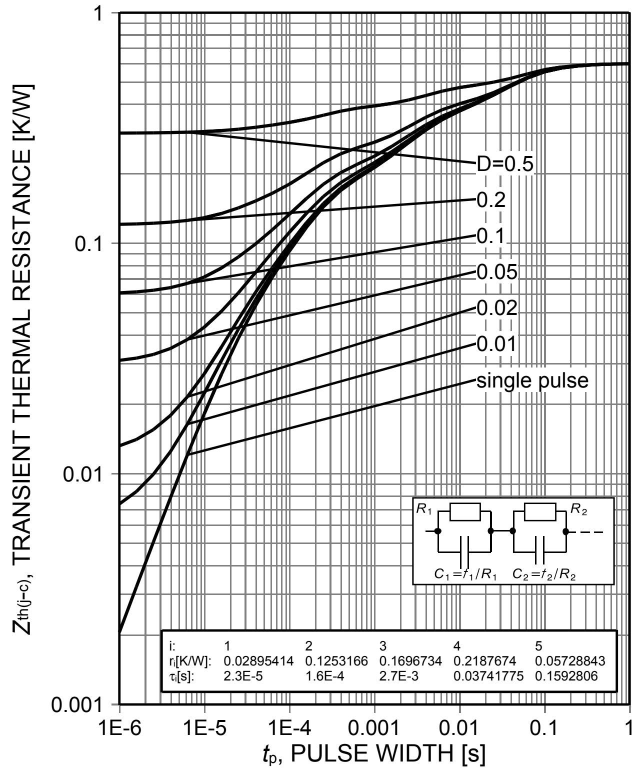


Figure 19. **Diode transient thermal impedance as a function of pulse width**
(D=t_p/T)

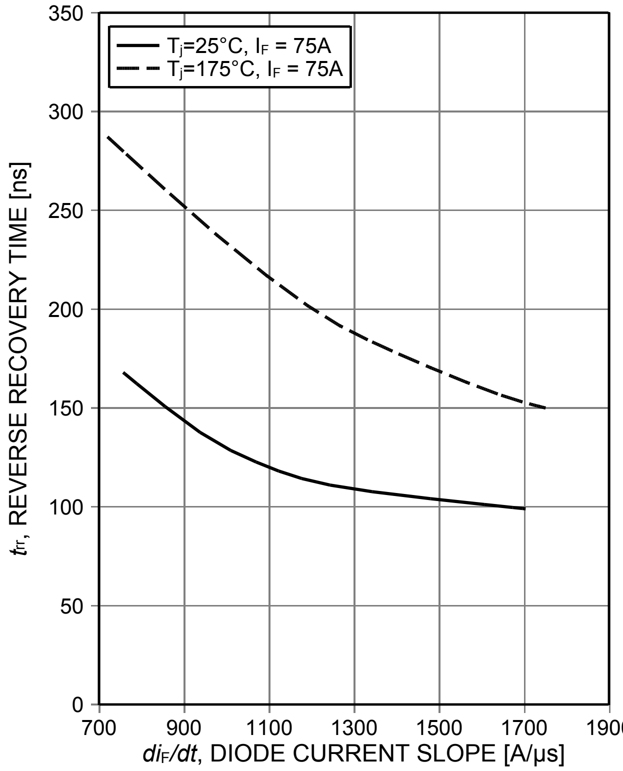


Figure 20. **Typical reverse recovery time as a function of diode current slope**
(V_R=400V)

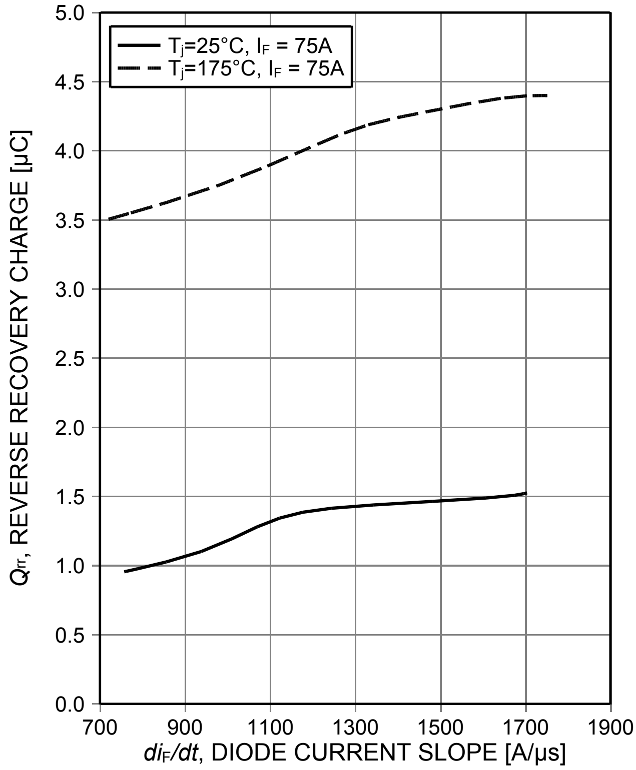


Figure 21. **Typical reverse recovery charge as a function of diode current slope**
(V_R=400V)

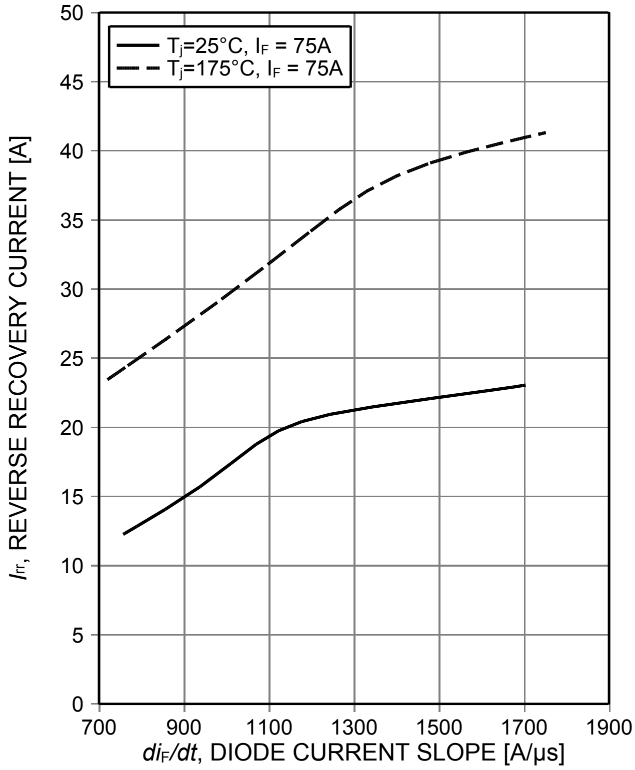


Figure 22. **Typical reverse recovery current as a function of diode current slope**
(V_R=400V)

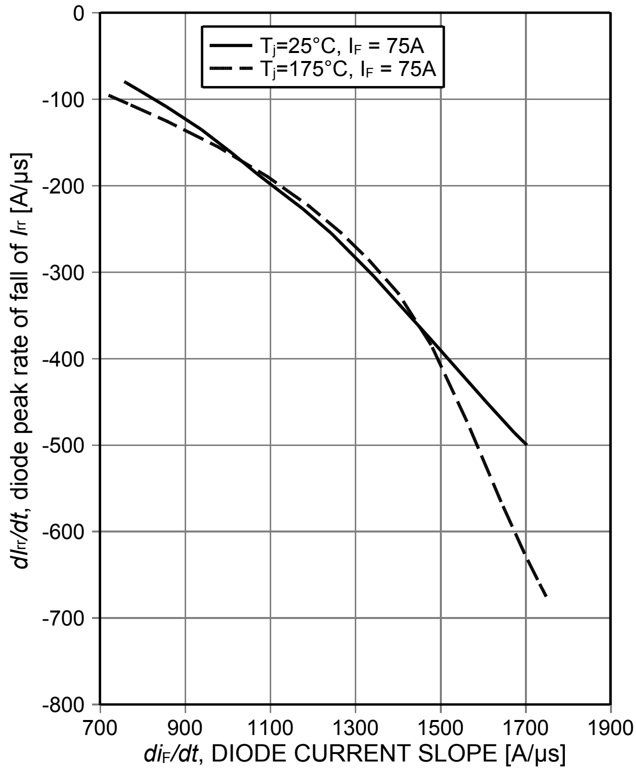


Figure 23. **Typical diode peak rate of fall of reverse recovery current as a function of diode current slope**
(V_R=400V)

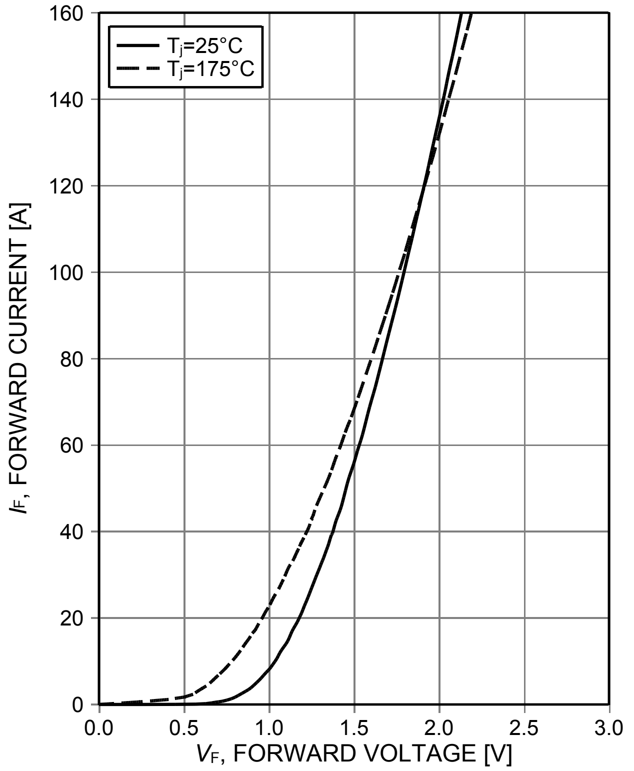


Figure 24. **Typical diode forward current as a function of forward voltage**

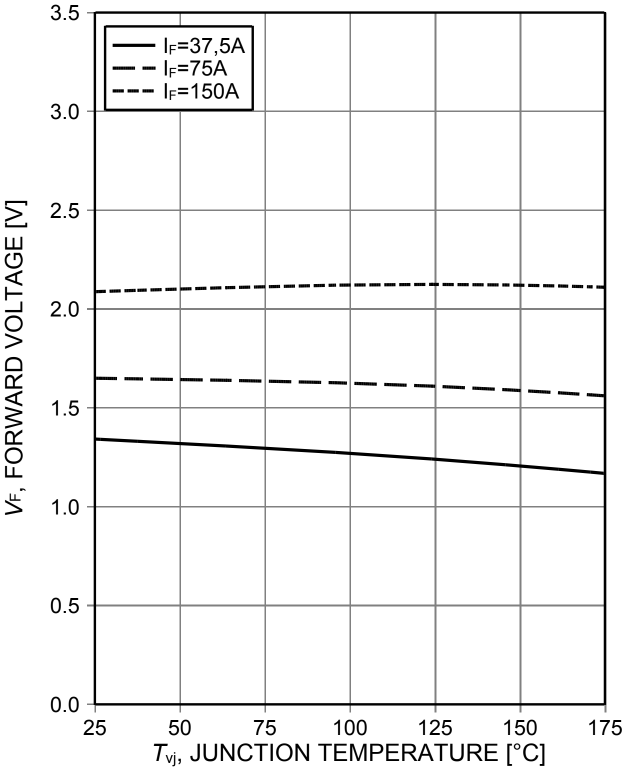
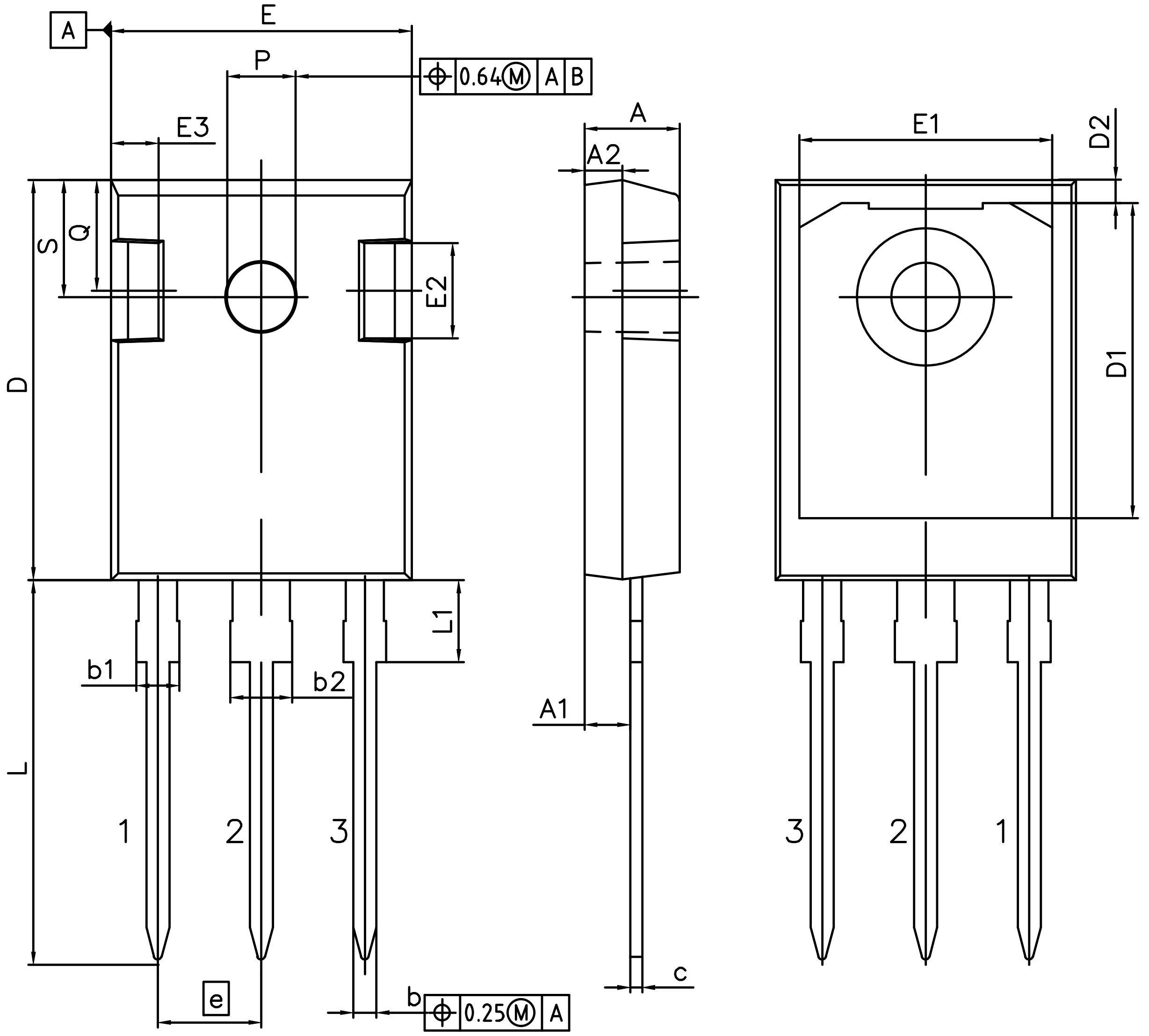


Figure 25. **Typical diode forward voltage as a function of junction temperature**

Package Drawing PG-TO247-3



DIMENSIONS	MILLIMETERS	
	MIN.	MAX.
A	4.70	5.30
A1	2.20	2.60
A2	1.50	2.50
b	1.00	1.40
b1	1.60	2.41
b2	2.57	3.43
c	0.38	0.89
D	20.70	21.50
D1	13.08	17.65
D2	0.51	1.35
E	15.50	16.30
E1	12.38	14.15
E2	3.40	5.10
E3	1.00	2.60
e	5.44	
L	19.80	20.40
L1	3.85	4.50
P	3.50	3.70
Q	5.35	6.25
S	6.04	6.30

DOCUMENT NO. Z8B00003327	
REVISION 06	
SCALE 3:1 0 1 2 3 4 5mm	
EUROPEAN PROJECTION	
ISSUE DATE 25.07.2018	

Testing Conditions

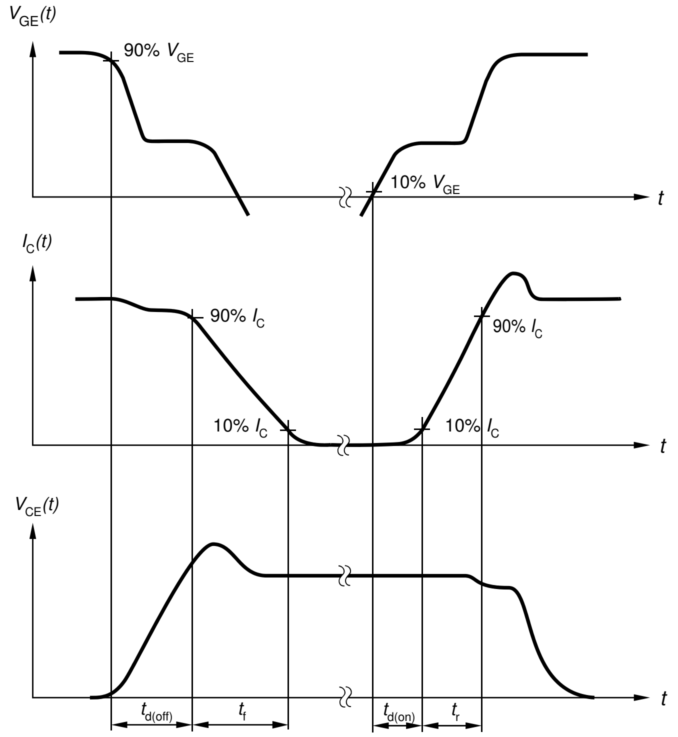


Figure A. **Definition of switching times**

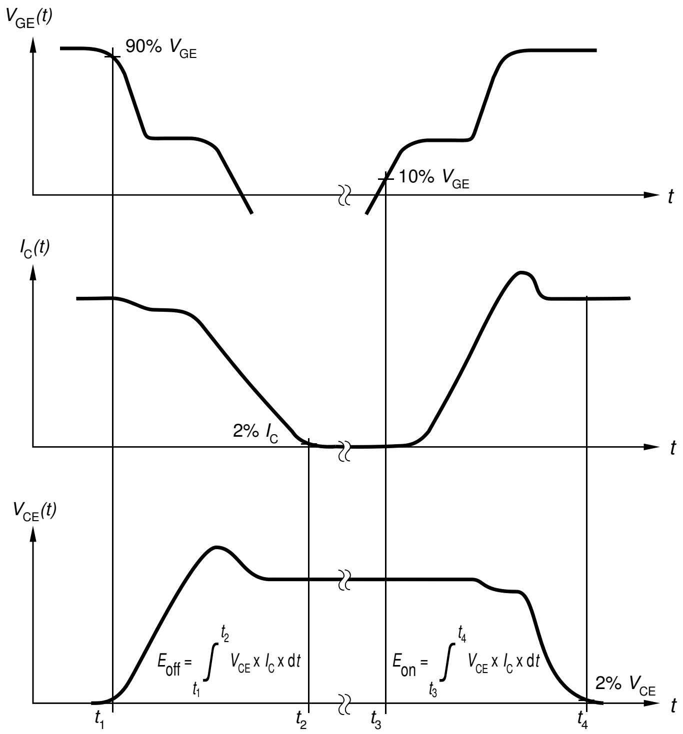


Figure B. **Definition of switching losses**

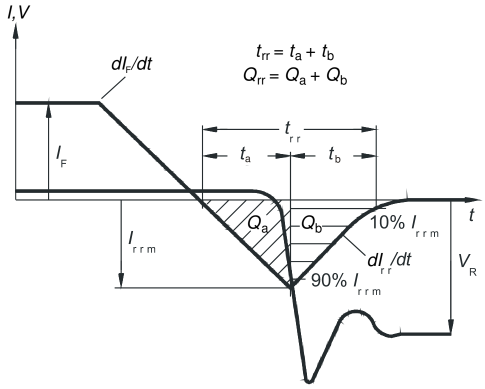


Figure C. **Definition of diode switching characteristics**

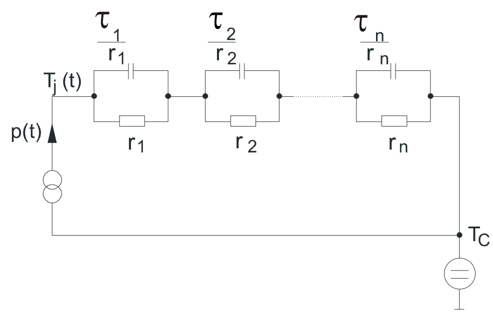


Figure D. **Thermal equivalent circuit**

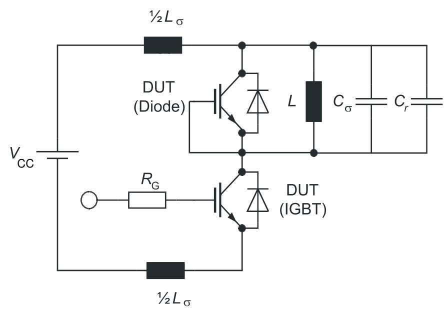


Figure E. **Dynamic test circuit**
Parasitic inductance L_{σ},
parasitic capacitor C_{σ},
relief capacitor C_r,
(only for ZVT switching)

Revision History

IKW75N65ET7

Revision: 2020-11-11, Rev. 2.2

Previous Revision

Revision	Date	Subjects (major changes since last revision)
1.1	2020-04-20	Preliminary data sheet
2.1	2020-05-12	Final data sheet
2.2	2020-11-11	Additional short circuit specification

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