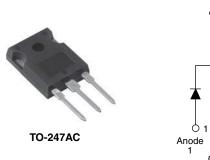
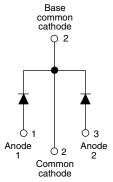
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VS-30CPQ1.0PbF Series, VS-30CPQ1.0-N3 Series

Vishay Semiconductors

Schottky Rectifier, 2 x 15 A





PRODUCT SUMMARY							
Package	TO-247AC						
I _{F(AV)}	2 x 15 A						
V _R	140 V, 150 V						
V _F at I _F	0.78 V						
I _{RM} max.	15 mA at 125 °C						
T _J max.	175 °C						
Diode variation	Common cathode						
E _{AS}	11.25 mJ						

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- RoHS COMPLIANT HALOGEN
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

The VS-30CPQ... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I _{F(AV)}	Rectangular waveform	30	А					
V _{RRM}		150	V					
I _{FSM}	t _p = 5 μs sine	1000	А					
V _F	15 Apk, T _J = 125 °C (per leg)	0.78	V					
TJ		- 55 to 175	°C					

VOLTAGE RATINGS										
PARAMETER	SYMBOL	VS-30CPQ140PbF	VS-30CPQ140-N3	VS-30CPQ150PbF	VS-30CPQ150-N3	UNITS				
Maximum DC reverse voltage	V _R									
Maximum working peak reverse voltage	V _{RWM}	140	140	150	150	V				

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST COND	ITIONS	VALUES	UNITS			
Maximum average	per device				30				
forward current See fig. 5	per leg	I _{F(AV)}	50 % duty cycle at T_C = 135 °C, rectangular waveform		15				
Maximum peak one cycle non-repetitive		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1000	A			
See fig. 7	je current per leg fig. 7		10 ms sine or 6 ms rect. pulse	V _{RRM} applied	340				
Non-repetitive avalanche energy	on-repetitive avalanche energy per leg E_{AS} $T_J = 25 \text{ °C}, I_{AS} = 0.50 \text{ A}, L = 90 \text{ mH}$		11.25	mJ					
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.50	А			

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VS-30CPQ1.0PbF Series, VS-30CPQ1.0-N3 Series

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		15 A	T _{.1} = 25 °C	1.00				
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	30 A	1)=25 0	1.19	v			
		15 A	T _{.1} = 125 °C	0.78				
		30 A	$1_{\rm J} = 125$ C	0.93				
Maximum reverse leakage current per leg	I _{BM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.1	mA			
See fig. 2	IRM \''	T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	15				
Maximum junction capacitance per leg	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		340	pF			
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		7.5	nH			
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,\,duty\,cycle$ < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C				
Maximum thermal resistance, junction to case per leg		P	DC operation See fig. 4	2.20					
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.10	°C/W				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.24					
Approvimate weight				6	g				
Approximate weight				0.21	oz.				
Mounting torque	minimum			6 (5)	kgf ⋅ cm (lbf ⋅ in)				
Mounting torque —	maximum			12 (10)					
Marking davias					Q140				
Marking device			Case style TO-247AC (JEDEC)	30CP	Q150				

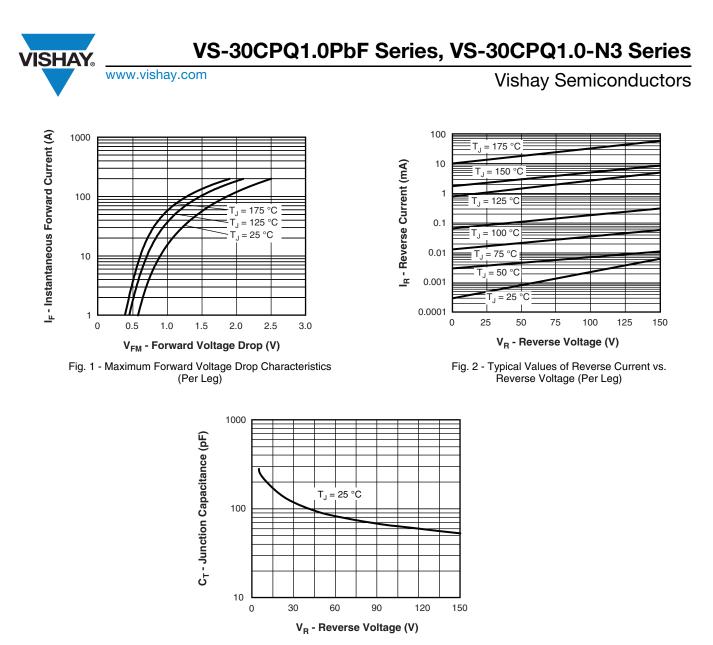
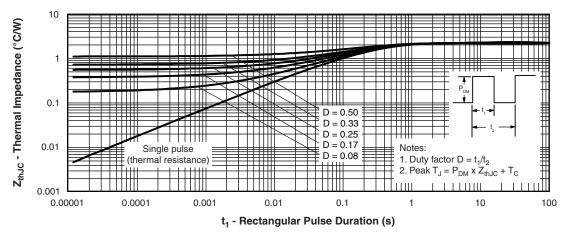
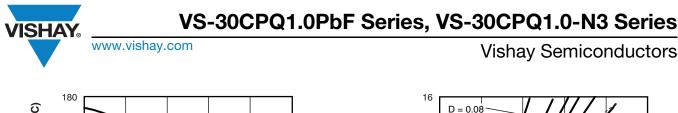


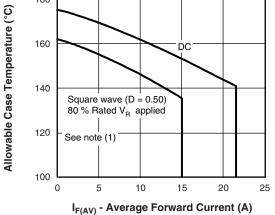
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

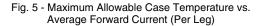


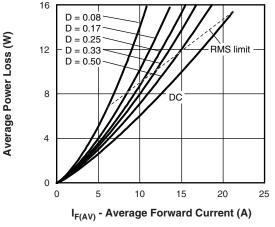


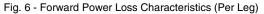
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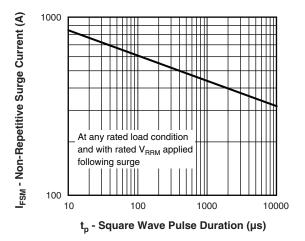


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

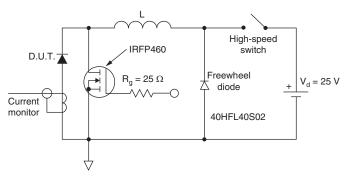


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % rated V_R

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ORDERING INFORMATION TABLE

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-30CPQ140PbF	25	500	Antistatic plastic tube						
VS-30CPQ140-N3	25	500	Antistatic plastic tube						
VS-30CPQ150PbF	25	500	Antistatic plastic tube						
VS-30CPQ150-N3	25	500	Antistatic plastic tube						

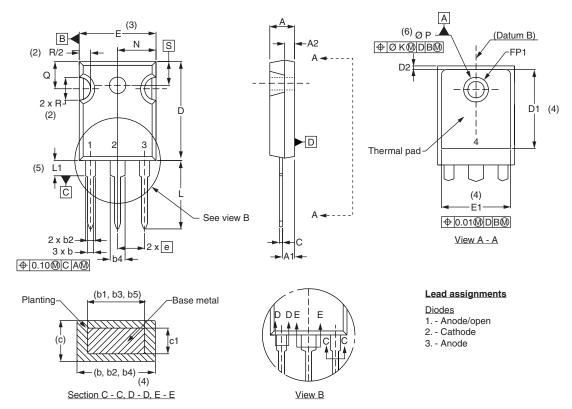
LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95223						
Part marking information	TO-247ACPbF	www.vishay.com/doc?95226				
	TO-247AC-N3	www.vishay.com/doc?95007				

Outline Dimensions





DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209		D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102		E	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098		E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055		e	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053		FK	2.	54	0.0)10	
b2	1.65	2.39	0.065	0.094		L	14.20	16.10	0.559	0.634	
b3	1.65	2.37	0.065	0.094		L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135		Ν	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133		ΦP	3.56	3.66	0.14	0.144	
с	0.38	0.86	0.015	0.034		Φ P1	-	6.98	-	0.275	
c1	0.38	0.76	0.015	0.030		Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3	R	4.52	5.49	1.78	0.216	
D1	13.08	_	0.515	-	4	S	5.51	BSC	0.217	BSC	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁴⁾ Thermal pad contour optional with dimensions D1 and E1

⁽⁵⁾ Lead finish uncontrolled in L1

(6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC outline TO-247 with exception of dimension c

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