

Current Transducer LA 25-NP

For the electronic measurement of currents: DC, AC, pulsed..., with galvanic separation between the primary circuit and the secondary circuit.

		HS			
Ele	ctrical data				
I _{pn}	Primary nominal RMS current		25		At
I _{PM}	Primary current, measuring range	0 ±	At		
R _M	Measuring resistance @	$T_{\rm A}$ = 70 °	C T _A	= 85 °C	
	with ±15 V @ ±25 At $_{max}$ @ ±36 At $_{max}$	R _{M min} R _M 100 32 100 19	max R _{M r} 0 100 0 100	nin R _{M max}) 315) 185	Ω Ω
I _{s n}	Secondary nominal RMS current		25		mA
$N_{\rm P}/N_{\rm S}$	Turns ratio		1-2-3-	4-5 : 1000)
U_{c}	Supply voltage (±5 %)		±15		V
I _c	Current consumption		10 + I _s	;	mA
Ac	curacy - Dynamic performance	data			
$\varepsilon_{\rm tot}$	Total error @ I_{PN} , T_{A} = 25 °C		±0.5		%
εL	Linearity error		< 0.2		%
-			Тур	Max	
Io	Offset current ¹⁾ @ $I_{\rm P}$ = 0, $T_{\rm A}$ = 25 °C		±0.05	±0.15	mA
$I_{\rm OM}$	Magnetic offset current $^{2)}$ @ $I_{\rm P}$ = 0 and s	pecified R_{M} ,			
	after an overloa	d of 3 × I_{PN}	±0.05	±0.15	mA
I _{OT}	Temperature variation of I_{o} 0 °C	C +25 °C	±0.06	±0.25	mA
	+25 °C	C +70 °C	±0.10	±0.35	mA
	−25 °C +85 °C			±0.5	mA
	-40 °C	C +85 °C		±1.2	mA
t _{D 90}	Delay time ³⁾ to 90 % of the final output v	value for I_{PN}	step < 1		μs
BW	Frequency bandwidth (−1 dB)		DC	150	kHz
Ge	neral data				
T _A	Ambient operating temperature		-40	+85	°C
$T_{\rm Ast}$	Ambient storage temperature		-45	+90	°C
R _P	Resistance of primary (winding) @ $T_{A} = 25 \text{ °C}$			< 1.25	
R _s	Resistance of secondary winding @ T_{A} :	= 70 °C	110		Ω
÷	@ T_	= 85 °C	115		Ω
R _{INS}	Insulation resistance @ 500 V, T_{A} = 25 °	С	> 1500)	MΩ
m	Mass Standards		22 EN 50 UL 50	178: 199 [:] 8: 2010	g 7

I_{PN} = 25 At



Features

- Closed loop (compensated) current transducer using the Hall effect
- Insulating plastic case recognized according to UL 94-V0.

Advantages

- Excellent accuracy
- Very good linearity
- Low temperature drift
- Optimized response time
- Wide frequency bandwidth
- No insertion losses
- High immunity to external interference
- Current overload capability.

Applications

- AC variable speed drives and servo motor drives
- Static converters for DC motor drives
- Battery supplied applications
- Uninterruptible Power Supplies (UPS)
- Switched Mode Power Supplies (SMPS)
- Power supplies for welding applications.

Application domain

Industrial.

Notes: ¹⁾ Measurement carried out after 15 mn functioning

²⁾ The result of the coercive field of the magnetic circuit

³⁾ For a di/dt = 100 A/µs.

N° 97.08.19.000.0

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Insulation coordination					
U_{d}	RMS voltage for AC insulation test, 50 Hz, 1 min	2.5	kV		
$U_{\rm Ni}$	Impulse withstand voltage 1.2/50 µs	9	kV		
		Min			
$d_{\rm Cp}$	Creepage distance	10.63	mm		
d _{CI}	Clearance	10.63	mm		
CTI	Comparative tracking index (group IIIa)	175			

Applications examples

According to EN 50178 and IEC 61010-1 standards and following conditions:

- Over voltage category OV 3
- Pollution degree PD2
- Non-uniform field

	EN 50178	IEC 61010-1		
$\overline{d_{\rm Cp}}, d_{\rm Cl}, U_{\rm Ni}$	Rated insulation voltage	Nominal voltage		
Basic insulation	1000 V	1000 V		
Reinforced insulation	500 V	300 V		

Safety

This tran*sducer* must be used in limited-energy secondary circuits according to IEC 61010-1.



This transducer must be used in electric/electronic equipment with respect to applicable standards and safety requirements in accordance with the manufacturer's operating instructions.



Caution, risk of electrical shock

When operating the transducer, certain parts of the module can carry hazardous voltage (eg. primary busbar, power supply).

Ignoring this warning can lead to injury and/or cause serious damage.

This transducer is a build-in device, whose conducting parts must be inaccessible after installation.

A protective housing or additional shield could be used. Main supply must be able to be disconnected.

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Dimensions LA 25-NP (in mm)



	Primary current		Nominal	Turno rotio	Primary	Primary insertion			
Number of primary turns	nominal I _{PN} [A]	maximum I _P [A]	output current I _{S N} [mA]	t $\frac{N_{\rm P}/N_{\rm S}}{N_{\rm P}/N_{\rm S}}$ $R_{\rm P}$ [m		inductance <i>L</i> _P [μH]	Recommended connections		
1	25	36	25	1 / 1000	0.3	0.023	5 4 3 2 1 IN 		
2	12	18	24	2 / 1000	1.1	0.09	5 4 3 2 1 IN 0-0 0-0-0 0-0 0-0-0 OUT 6 7 8 9 10		
3	8	12	24	3 / 1000	2.5	0.21	5 4 3 2 1 IN 0-0 0 0-0 0-0 0 0-0 OUT 6 7 8 9 10		
4	6	9	24	4 / 1000	4.4	0.37	5 4 3 2 1 IN 0		
5	5	7	25	5 / 1000	6.3	0.58	5 4 3 2 1 IN 0 0 0 0 0 0 0 0 0 OUT 6 7 8 9 10		

Mechanical characteristics

- General tolerance
- Fastening & connection of primary
- Fastening & connection of secondary
- Recommended PCB hole

±0.2 mm 10 pins 0.7 × 0.6 mm 3 pins Ø 1 mm 1.2 mm

Remarks

• *I*_s is positive when *I*_p flows from terminals 1, 2, 3, 4, 5 to terminals 10, 9, 8, 7, 6.

• This is a standard model. For different versions (supply voltages, turns ratios, unidirectional measurements...), please contact us.

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