

# **Glass Passivated Bridge Rectifiers**

### **FEATURES**

- Glass passivated junction
- Integrally molded heatsink provide very low thermal resistance for maximum heat dissipation
- Universal 4-way terminals: snap-on, wrap-around, solder or P.C. board mounting
- High surge current capability
- UL Recognized File # E-326243
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC







GBPC40

GBPC40-M

### **MECHANICAL DATA**

Case: GBPC40

Molding compound, UL flammability classification rating 94V-0 **Terminal:** Matte tin plated leads, solderable per JESD22-B102 Meet JESD 201 class 1A whisker test

Polarity: Polarity as marked on the body

Mounting torque: 20 in-lbs maximum

Weight: 17.3 g (approximately)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS ( $T_A$ =25 $^{\circ}C$ unless otherwise noted)								
SYMBOL	-005	-01	-02	-04	-06	-08	-10	UNIT
V <sub>RRM</sub>	50	100	200	400	600	800	1000	V
V <sub>RMS</sub>	35	70	140	280	420	560	700	V
V <sub>DC</sub>	50	100	200	400	600	800	1000	V
I <sub>F(AV)</sub>				40 50				A
I <sub>FSM</sub>				400				A
num instantaneous GBPC40 20 A d voltage drop per GBPC50 25 A V <sub>F</sub> 1.1 nt at specified current (Note 1)					V			
I <sub>R</sub>	10				μA			
R <sub>θJC</sub>	1.5				<sup>o</sup> C/W			
TJ	- 55 to +150				OC			
T <sub>STG</sub>	- 55 to +150				OC			
	SYMBOL $V_{RRM}$ $V_{RMS}$ $V_{DC}$ $I_{F(AV)}$ $I_{FSM}$ $V_F$ $I_R$ $R_{\theta JC}$ $T_J$	SYMBOL       -005 $V_{RRM}$ 50 $V_{RMS}$ 35 $V_{DC}$ 50 $I_{F(AV)}$ 50 $I_{FSM}$ - $V_F$ - $I_R$ - $R_{\theta,JC}$ - $T_J$ -	SYMBOL         -005         -01 $V_{RRM}$ 50         100 $V_{RMS}$ 35         70 $V_{DC}$ 50         100 $V_{DC}$ 50         100 $I_{F(AV)}$ 50         100 $I_{F(AV)}$	SYMBOL       -005       -01       -02 $V_{RRM}$ 50       100       200 $V_{RMS}$ 35       70       140 $V_{DC}$ 50       100       200 $I_{F(AV)}$ 50       100       200 $I_{F(AV)}$ $I_{F(AV)}$ $I_{FSM}$ $I_{FSM}$ $I_{FSM}$ $V_F$ $I_R$ $I_R$ $I_R$ $I_R$ $I_R$ $I_R$ $I_R$ $I_R$ $T_J$ $I_T$ $I_R$ $I_R$	SYMBOL         -005         -01         -02         -04 $V_{RRM}$ 50         100         200         400 $V_{RMS}$ 35         70         140         280 $V_{DC}$ 50         100         200         400 $V_{DC}$ 50         100         200         400 $I_{F(AV)}$ -50         100         200         400 $I_{F(AV)}$ -50         100         200         400 $V_{F}$ -10         -50         1.1 $I_R$ -10         -55 to +15         -55 to +15	SYMBOL         -005         -01         -02         -04         -06 $V_{RRM}$ 50         100         200         400         600 $V_{RMS}$ 35         70         140         280         420 $V_{DC}$ 50         100         200         400         600 $V_{DC}$ 50         100         200         400         600 $I_{F(AV)}$ 40         50         40         50         50 $I_{FSM}$ 400         50         400         50         1.1         50 $V_F$ 1.1         10         10         10         1.5         1.5         1.5 $R_{0JC}$ 1.5         1.5         55 to +150         1.5         1.5         1.5         1.5	SYMBOL         -005         -01         -02         -04         -06         -08 $V_{RRM}$ 50         100         200         400         600         800 $V_{RMS}$ 35         70         140         280         420         560 $V_{DC}$ 50         100         200         400         600         800 $V_{DC}$ 50         100         200         400         600         800 $I_{F(AV)}$ 50         100         200         400         600         800 $I_{F(AV)}$ 40         50         100         200         400         600         800 $V_{F}$ 400         50         400         50         50         1.1         50 $V_F$ 1.1         400         50         50         1.5         50         50 $I_R$ 10         1.5         1.5         1.5         55         55         55         50	$\begin{array}{c c c c c c c c c c c c c c c c c c c $

Note 1: Pulse test with PW=300µs, 1% duty cycle

Note 2: Suffix "M" - Terminal Location Face to Face



Taiwan Semiconductor

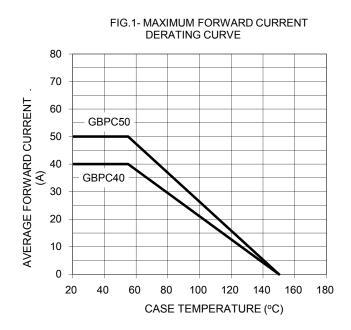
ORDERING INFORMATION				
PART NO.	PACKING CODE	PACKAGE	PACKING	
GBPC**-xx (Note 1)	ТО	GBPC	Tray	

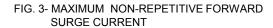
Note 1: "\*\*" defines current from 40A (GBPC40-xx) to 50A (GBPC50-xx), "xx" defines voltage from 50V (GBPC\*\*-005) to 1000V (GBPC\*\*-10)

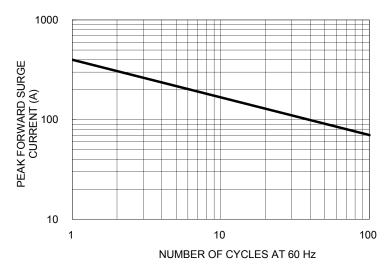
EXAMPLE				
PREFERRED P/N	PART NO.	PACKING CODE	DESCRIPTION	
GBPC40-10 T0	GBPC40-10	TO		

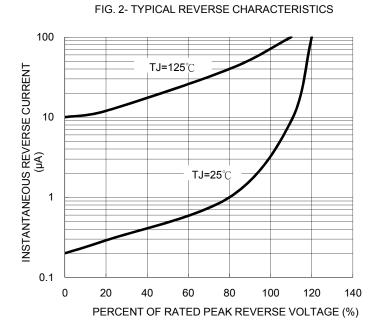
# **RATINGS AND CHARACTERISTICS CURVES**

(TA=25 $^{\circ}$ C unless otherwise noted)









#### FIG. 4- TYPICAL FORWARD CHARACTERISTICS

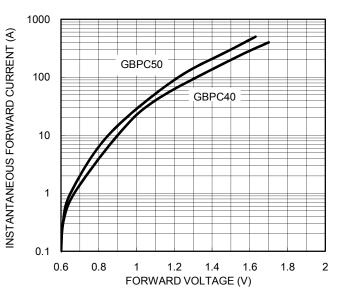
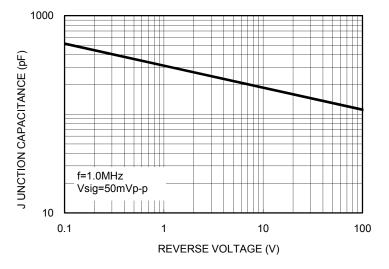
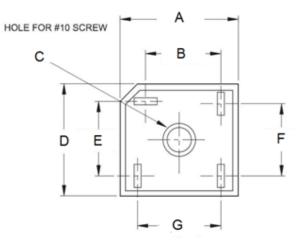


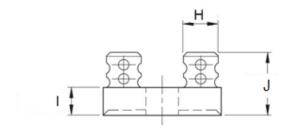


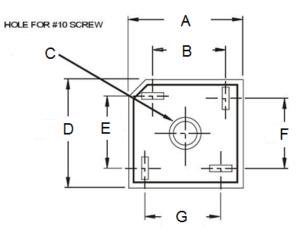
FIG. 5 TYPICAL JUNCTION CAPACITANCE

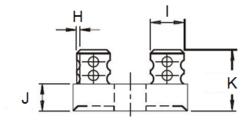


### PACKAGE OUTLINE DIMENSIONS









GBPC40					
DIM.	Unit	(mm)	Unit (inch)		
DINI.	Min	Max	Min	Max	
А	28.50	29.00	1.122	1.142	
В	15.50	17.60	0.610	0.693	
С	5.08	5.59	0.200	0.220	
D	28.50	29.00	1.122	1.142	
E	15.50	17.60	0.610	0.693	
F	13.30	15.30	0.524	0.602	
G	17.10	19.10	0.673	0.752	
Н	6.60 (TYP)		0.26 (TYP)		
Ι	7.36	7.87	0.290	0.310	
J	21.26	24.57	0.837	0.967	

GBPC40-M					
DIM.	Unit	(mm)	Unit (inch)		
	Min	Max	Min	Max	
Α	28.50	29.00	1.122	1.142	
В	15.50	17.60	0.610	0.693	
С	5.08	5.59	0.200	0.220	
D	28.50	29.00	1.122	1.142	
Е	15.50	17.60	0.610	0.693	
F	15.50	17.60	0.610	0.693	
G	15.50	17.60	0.610	0.693	
Н	0.76	0.86	0.030	0.034	
I	6.60 (TYP)		0.26 (TYP)		
J	7.36	7.87	0.290	0.310	
К	21.26	24.57	0.837	0.967	

Document Number: DS\_D1312016



# MARKING DIAGRAM

÷	RA AC	P/N
D/N	MAAAIE	YWW
P/N	TVVVVF	F

= Specific Device Code

- WW = Date Code
  - = Factory Code



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