



Final Product Change Notification

201912031F01

Issue Date: 10-Jan-2020

Effective Date: 09-Apr-2020

Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)

This notice is NXP Company Proprietary.



QUALITY

Management Summary

Microcontroller products LPC80x will be sourced from the new product revision "1C" to resolve the VDD.1 errata.

Change Category

- | | | | | |
|----------------------------------------------|---------------------------------------------|----------------------------------------------------|-----------------------------------------|---------------------------------------------------------|
| <input type="checkbox"/> Wafer Fab Process | <input type="checkbox"/> Assembly Process | <input type="checkbox"/> Product Marking | <input type="checkbox"/> Test Location | <input checked="" type="checkbox"/> Design |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification | <input type="checkbox"/> Test Process | <input type="checkbox"/> Errata |
| <input type="checkbox"/> Wafer Fab Location | <input type="checkbox"/> Assembly Location | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware | <input type="checkbox"/> Other | | | |

LPC80x Revision
Change to 1C

Description of Change

Microcontroller products LPC80x will be sourced from the new product revision "1C".

- This design change fixes the VDD.1 errata as documented in the device errata sheet, https://www.nxp.com/docs/en/errata/ES_LPC804.pdf.

- The design change is a logic only change and there is no reliability impact.

Reason for Change

- To fix the VDD.1 functional deviation as documented in the errata sheet.

Identification of Affected Products

Top side marking

The last 2 characters of the last line in the top side marking changes to "1C".

Product Availability

Sample Information

Samples are available upon request

Production

Planned first shipment 07-Apr-2020

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Data Sheet Revision

No impact to existing datasheet

Disposition of Old Products

Existing inventory will be shipped until depleted

Additional information

Affected products and sales history information: see attached file

Self qualification: [view online](#)



Timing and Logistics

In compliance with JEDEC J-STD-046, your acknowledgement of this change is expected by 09-Feb-2020.

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name Tim Camenzind

Position Quality Manager - LPC Microcontroller Products

e-mail address tim.camenzind@nxp.com

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Customer Focus, Passion to Win.

NXP Quality Management Team.

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