

# RX140 Group

## Renesas Solution Starter Kit Capacitive Touch Evaluation System User's Manual

RENESAS 32-Bit MCU  
RX Family/RX100 Series

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The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

### 1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

### 2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

### 3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

### 4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

### 5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

### 6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between  $V_{IL}$  (Max.) and  $V_{IH}$  (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between  $V_{IL}$  (Max.) and  $V_{IH}$  (Min.).

### 7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

### 8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

# How to Use This Manual

## 1. Purpose and Target Readers

This is a manual for users to understand the outline and hardware functions of the RX140 Capacitive Touch Evaluation System (RTK0EG0039S01001BJ). This manual is intended for users who use this CPU board. A basic knowledge of electric circuits, logical circuits, and MCUs is necessary in order to use this manual.

Particular attention should be paid to the precautionary notes when using the manual. These notes occur within the body of the text, at the end of each section, and in the Usage Notes section.

The revision history summarizes the locations of revisions and additions. It does not list all revisions. Refer to the text of the manual for details.

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## Precautions

This Evaluation Kit is only intended for use in a laboratory environment under ambient temperature and humidity conditions. A safe separation distance should be used between this and any sensitive equipment. Its use outside the laboratory, classroom, study area, or similar such area invalidates conformity with the protection requirements of the Electromagnetic Compatibility Directive and could lead to prosecution.

The product generates, uses, and can radiate radio frequency energy and may cause harmful interference to radio communications. There is no guarantee that interference will not occur in a particular installation. If this equipment causes harmful interference to radio or television reception, which can be determined by turning the equipment off or on, you are encouraged to try to correct the interference by one or more of the following measures:

- Ensure attached cables do not lie across the equipment.
- Reorient the receiving antenna.
- Increase the distance between the equipment and the receiver.
- Connect the equipment into an outlet on a circuit different from that which the receiver is connected.
- Power down the equipment when not in use.
- Consult the dealer or an experienced radio/TV technician for help.

Note: It is recommended that wherever possible shielded interface cables are used.

The product is potentially susceptible to certain EMC phenomena. To mitigate against them it is recommended that the following measures be undertaken:

- The user is advised that mobile phones should not be used within 10 m of the product when in use.
- The user is advised to take ESD precautions when handling the equipment.

The Evaluation Kit does not represent an ideal reference design for an end product and does not fulfill the regulatory standards for an end product.

## Safety Items

### Definitions of Symbols

A variety of symbols are used in this document and on the product itself to prevent in advance danger to you the user or any third parties and to prevent in advance damage to any physical property.

This section, Safety Items, presents these symbols and their meanings. It also presents safety notes to assure that this produce is used safely and correctly.

This product should only be used after fully understanding the material presented in this section.



### Warning

Warning items indicate things that, if not avoided, could lead to death or serious injury.



### Caution

Caution items indicate both latent dangers that can lead to minor or moderately severe injury and latent dangers that can lead to property damage if not avoided.

In addition to the above two markings, the following are displayed at the same time if appropriate.

**[Important]** Indicates a point that can lead to equipment failure or malfunction if incorrectly set when setting up this product.

A triangular mark  $\triangle$  indicates a warning or caution.

Example:



### Electrical Shock Hazard

The



mark indicates something that is forbidden.

Example: :



### Do Not Disassemble

## Warning



### Warning

#### Handling related warnings:

Do not disassemble or modify this product. Renesas does not guarantee this product if it has been disassembled or modified.



The power supply for this product can be selected to be either the USB bus or a DC jack. A jumper is used to select the power supply.

If, during either use or storage of this product, any abnormality in the product itself (including abnormal odors, heating, color changes, or shape changes to the product itself) are observed, disconnect the USB cable and power supply cable immediately.

#### Installation:



Do not install this equipment in a location that has a high humidity or where water or other fluids could get on it. This equipment may be damaged if water or other fluids could get on it.

#### Ambient temperature:



The upper limit for the ambient temperature under which this product may be used is 35°C.

This maximum rated ambient temperature must not be exceeded.

## Caution

### **Caution**

#### Handling related cautions:

Use the antistatic band. Failure to do so could cause malfunction or unstable motion or be damaged Internal components.

This product must be handled carefully. Do not drop, knock over, or apply any strong mechanical shocks to this product.



When connecting or disconnecting cables from this product, hold the parts of the cable intended to be grasped (such as the plugs) and avoid putting stress on the cable. Do not pull on this product when it is connected to a communications cable or user system connection cable. Stress on the cable can result in internal disconnections in the cable. When connecting a cable to a connector, be careful not to insert the plug in the reverse orientation. Reverse insertion can result in damage to this product itself or to connected equipment.

The power supply for this product can be selected from two options (the DC jack or the USB cable). The jumper JP4 (on the top side of the circuit board) is used to select the power supply. Always check the jumper position before connecting a power source. An incorrect jumper position can result in damage to this product or the PC connected over the USB cable.

Do not handle this product with wet hands. This can lead to failure of the product.

#### Transport methods:



**When transporting this product, use the product's packing box and cushioning materials and ship it with precision equipment handling.** If the products packing is insufficient, it may be damaged during shipping.

If it must be transported by some other method, pack it carefully as precision equipment.

When packing this product, always use the antistatic pouch included with this product.

If some other pouch is used, damage to the product may be caused by electrostatic discharge.

#### Abnormal operation:



If operation of this product becomes abnormal due to interference from, for example, external noise, apply the following procedure.

1. Turn off the power.
2. Wait 10 seconds and then turn the power back on.

#### Disposal:



When disposing of this product, handle it as industrial waste according to all applicable laws.



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The WEEE (Waste Electrical and Electronic Equipment) regulations put responsibilities on producers for the collection and recycling or disposal of electrical and electronic waste. Return of WEEE under these regulations is applicable in the European Union only. This equipment (including all accessories) is not intended for household use. After use the equipment cannot be disposed of as household waste, and the WEEE must be treated, recycled and disposed of in an environmentally sound manner. Renesas Electronics Europe GmbH can take back end of life equipment, register for this service at "<http://www.renesas.eu/weee>".



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## RX140 Group

### Capacitive Touch Evaluation CPU Board User's Manual

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## 1. Overview

### 1.1 Purpose

The RX140 Capacitive Touch Evaluation System (RTK0EG0039S01001BJ) is a kit created for evaluating the Renesas Electronics RX140 Group of MCUs. This manual describes the RTK0EG0039S01001BJ's hardware.

### 1.2 Main Characteristics and Functions

The main functions of the RX140 Capacitive Touch Evaluation System are as follows:

- Renesas Microcontroller programming and debugging
  - E2/E2 Lite debugger connector
- General purpose switches and LEDs
- Capacitive Touch Sensor (CTS02SL)
  - 36 channels available
- Pmod™ interface (Type 6A)
- Connects to Renesas Capacitive Touch Evaluation System Application Board (option)
  - Includes self-capacitance touch electrode application board

### 1.3 Board Specifications

**Table 1-1 CPU Board Specifications**

Item	Specifications
MCU	Model No.: R5F51406ADFN
	Package: 80-pin LFQFP
	On-chip memory: ROM 256KB+8KB、RAM 64KB
	High-speed on-chip oscillator (HOCO): 48MHz
Input clock	Main clock: 16MHz (option)
	Sub clock: 32.768KHz (option)
Power supply	Operation voltage: 5.0 to 3.3V DC jack 5.0V to 3.3 input USB bus powered (VBUS): 5V Voltage regulator (LDO): 3.3V
Debug interface	Renesas Electronics E2/E2 Lite 14-pin box header
Slide switch	MCU operating mode selection: 1 pole x 1 TS pins / SCI Boot Mode programming pins switch: 2 poles x 1 CTSU temperature compensation circuit selection: 1 pole x 1
Push switches	Reset switch: x 1
	User switches: x 2
LEDs	Power status: red x 1
	User LEDs: green x 1, yellow x 1
USB serial conversion interface	Connector: USB Micro B
	Driver: FT234XD USB serial IC manufactured by FTDI
Application board interface (GPIO)	2.54mm pitch, 16 pins x 1 (CN1)
Application board interface (CTSU)	2.54mm pitch, 40 pins x 1 (CN2)
Pmod interface	2.54mm pitch, 12 pins x 1 (CN3) Type 6A
Current consumption	500mA or less
Operating Temperature Range	When operating: 10 to 35°C, in storage: -10 to 50°C (no condensation)
Board dimensions (L x W x H)	89mm x 95mm x 18mm (including connectors)

**Table 1-2 Application Board Specifications**

<b>Item</b>	<b>Specifications</b>
Self-capacitance detection touch electrodes	Buttons: 3 Wheel (4-electrode configuration): 1 Sliders (5-electrode configuration): 1
Active shields	1 shield each for button, wheel, and slider areas: 1 shield each
LEDs	16
Renesas MCU Cap Touch CPU board interface	2.54mm pitch, 16 pins x 1 (CN1) 2.54mm pitch, 40 pins x 1 (CN2)
Overlay panel	2mm-thick acrylic panel
Current consumption	500mA or less
Operating Temperature Range	When operating: 10 to 35°C, in storage: -10 to 50°C (no condensation)
Board dimensions (L x W x H)	110mm x 116mm x 11mm (including connectors)

## 1.4 Regulatory Compliance Notices

### 1.4.1 European Union regulatory notices

This product complies with the following EU Directives. (These directives are only valid in the European Union.)

CE Certifications:

- Electromagnetic Compatibility (EMC) Directive 2014/30/EU  
EN61326-1 : 2013 Class A

---

**WARNING:** This is a Class A product. This equipment can cause radio frequency noise when used in the residential area. In such cases, the user/operator of the equipment may be required to take appropriate countermeasures under his responsibility.

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  - Trademark and Type name
    - Trademark: Renesas
    - Product name: RX140 Group Capacitive Touch Evaluation System
    - Type name: RTK0EG0039S01001BJ

Environmental Compliance and Certifications:

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## 2. CPU Board

### 2.1 System Block Diagram

Figure 2-1 shows the system block diagram of the CPU board.

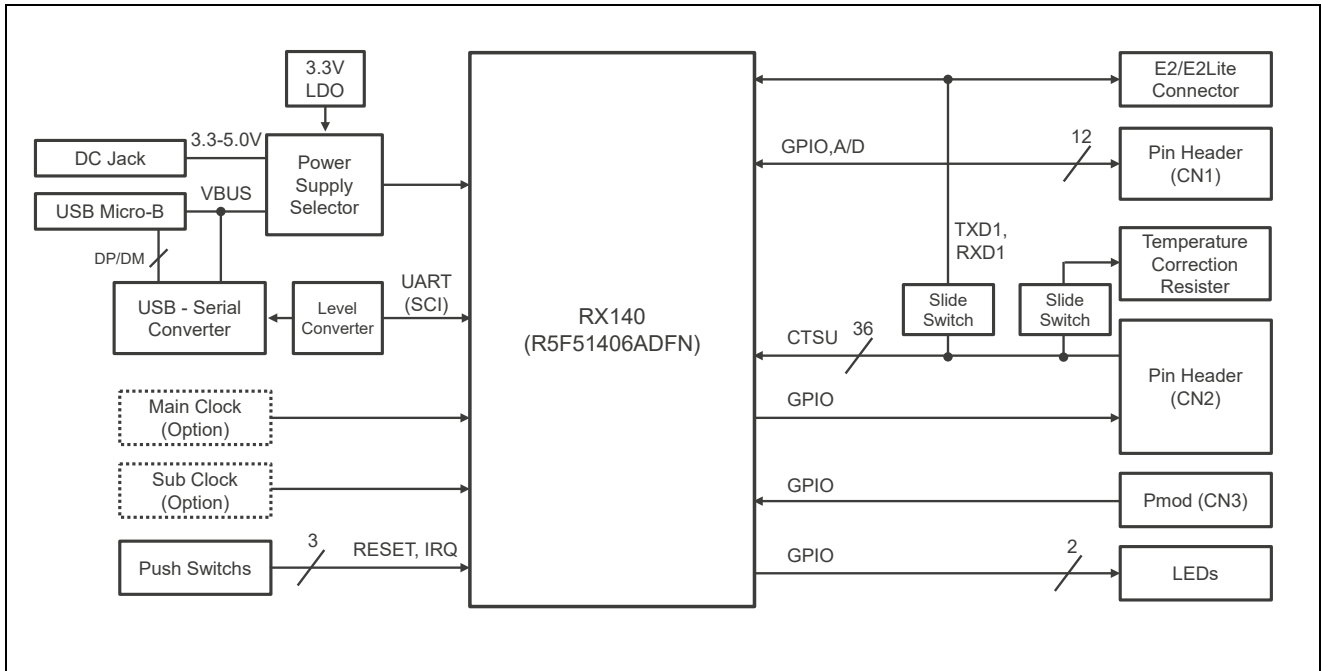


Figure 2-1. System Block Diagram

## 2.2 Product Configuration

Figure 2-2 shows the parts location. Figure 2-3 shows the board dimensions.

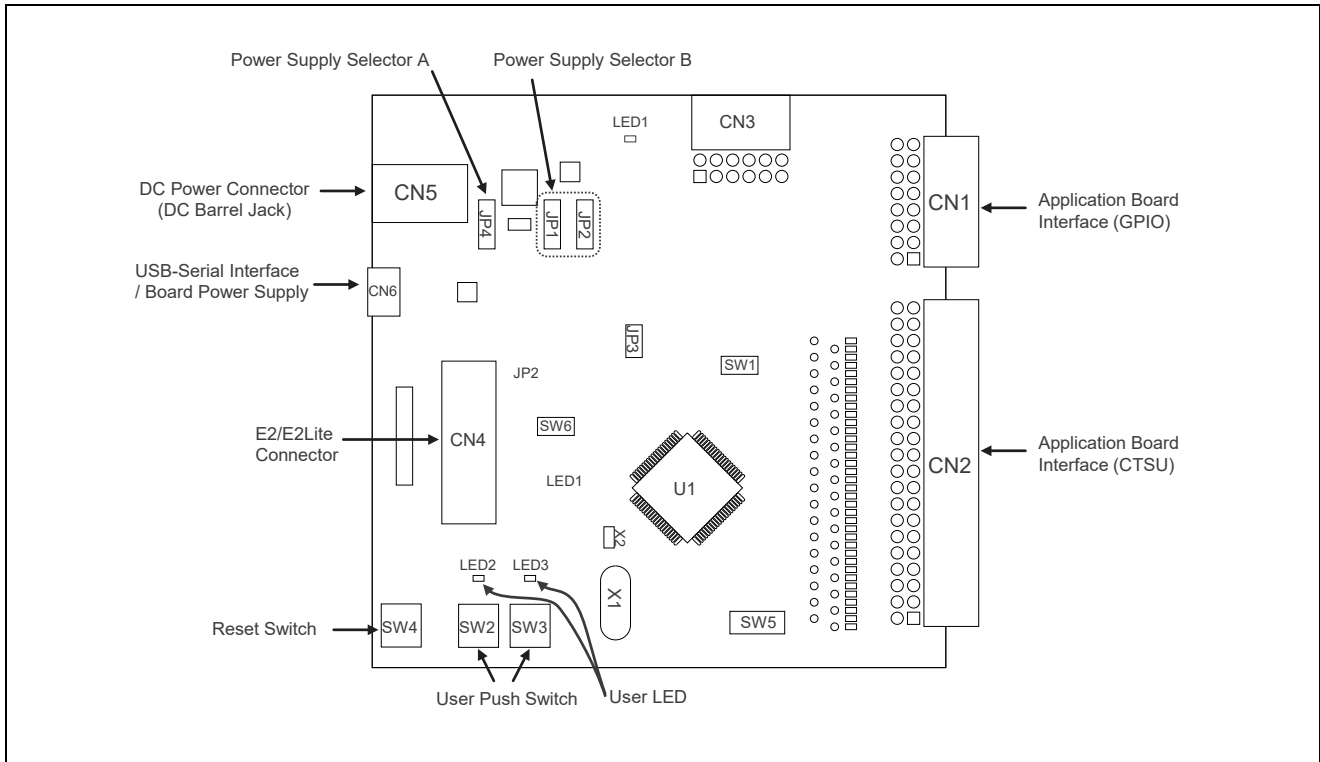


Figure 2-2. Parts Location

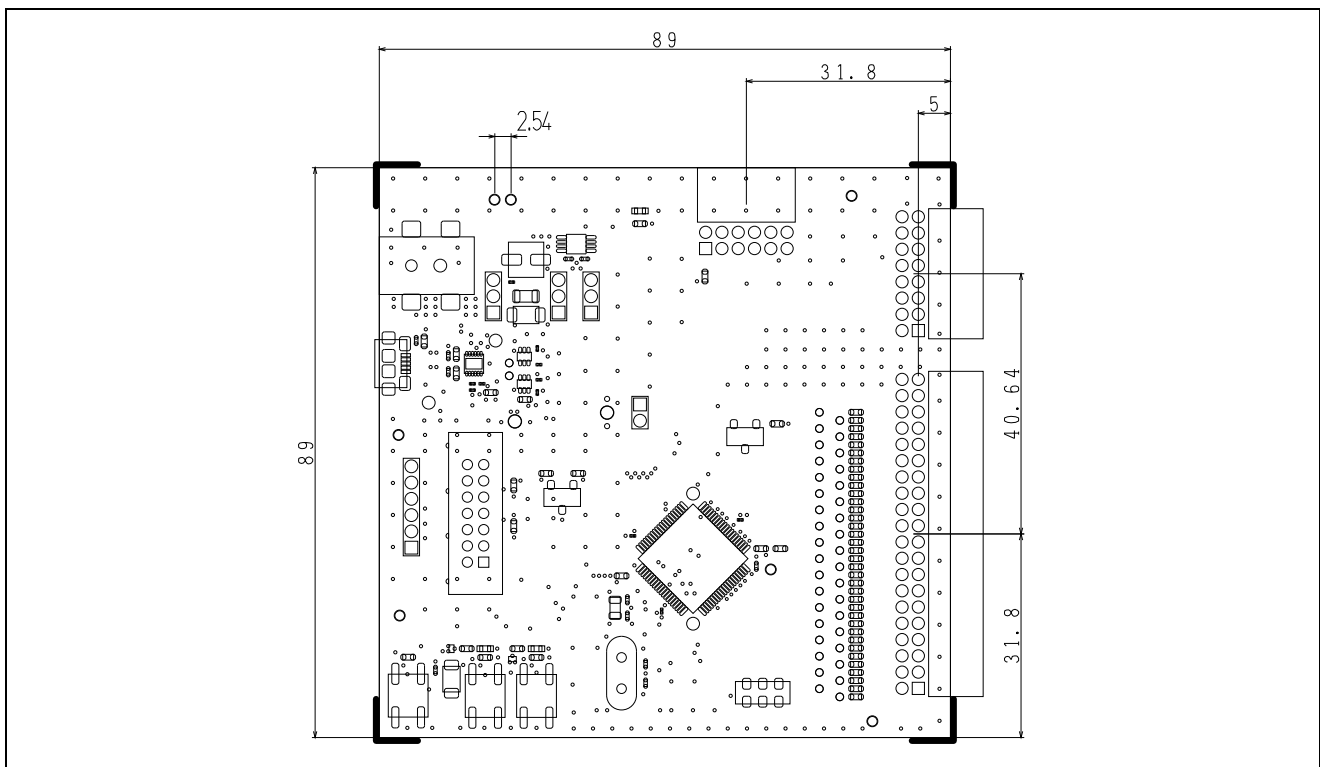


Figure 2-3. Board Dimensions

## 2.3 Hardware Details

### 2.3.1 Default Jumper Settings

Figure 2-4 shows the default positions of the jumpers. Table 2-1 lists the default jumper settings.

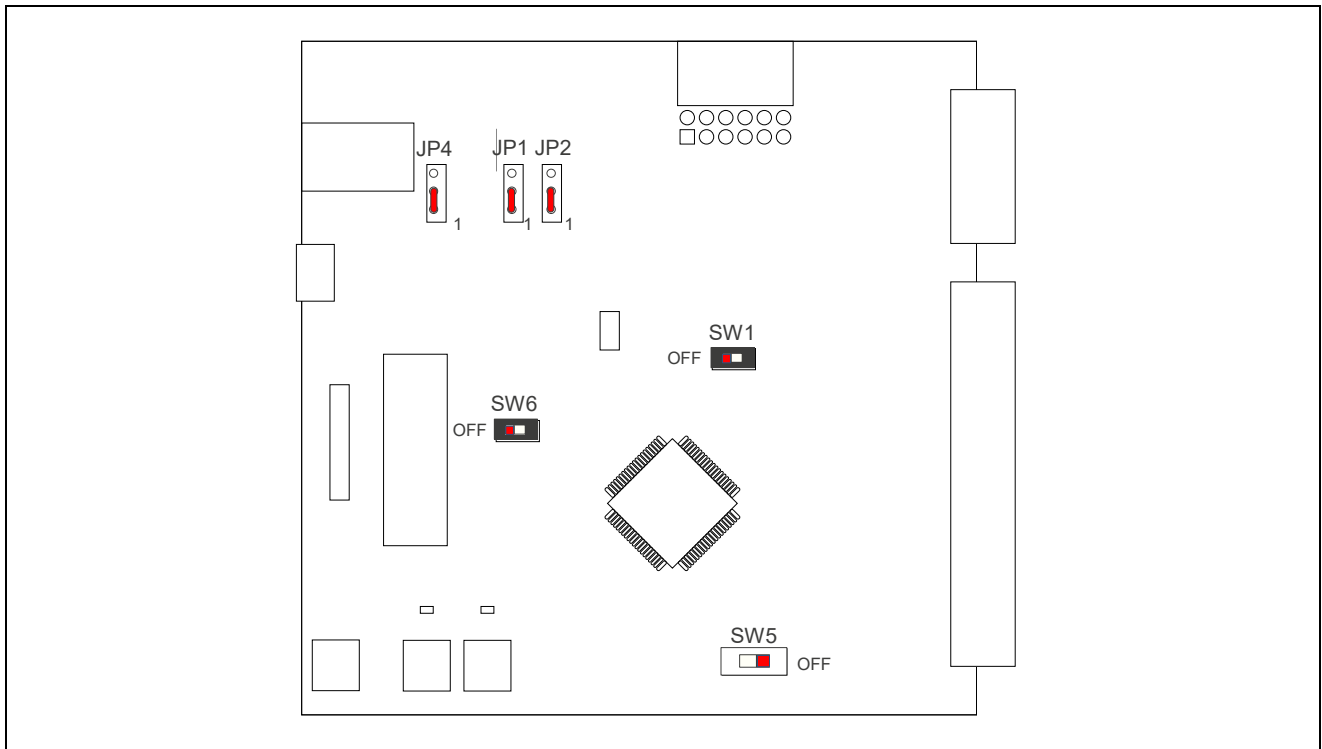


Figure 2-4. Default Jumper Positions

Table 2-1. Default Jumper Settings

Reference	Circuit Group	Default State	Description
JP4	Power supply	Pins 1-2 closed	Supplies USB power to board USB
JP1		Pins 1-2 closed	Supplies JP4 power to board devices
JP2		Pins 1-2 closed	
SW1	Capacitive touch	OFF	Sets TS35 as normal CTSU pin
SW5	Capacitive touch / Debug interface	OFF	P26/TS4/TXD1 and P30/TS2/RXD1 become TS pins
SW6	Debug interface	OFF	MCU operates in single chip mode



### 2.3.2 MCU Operating Mode Settings

Table 2-2 provides the RX140 operating mode settings.

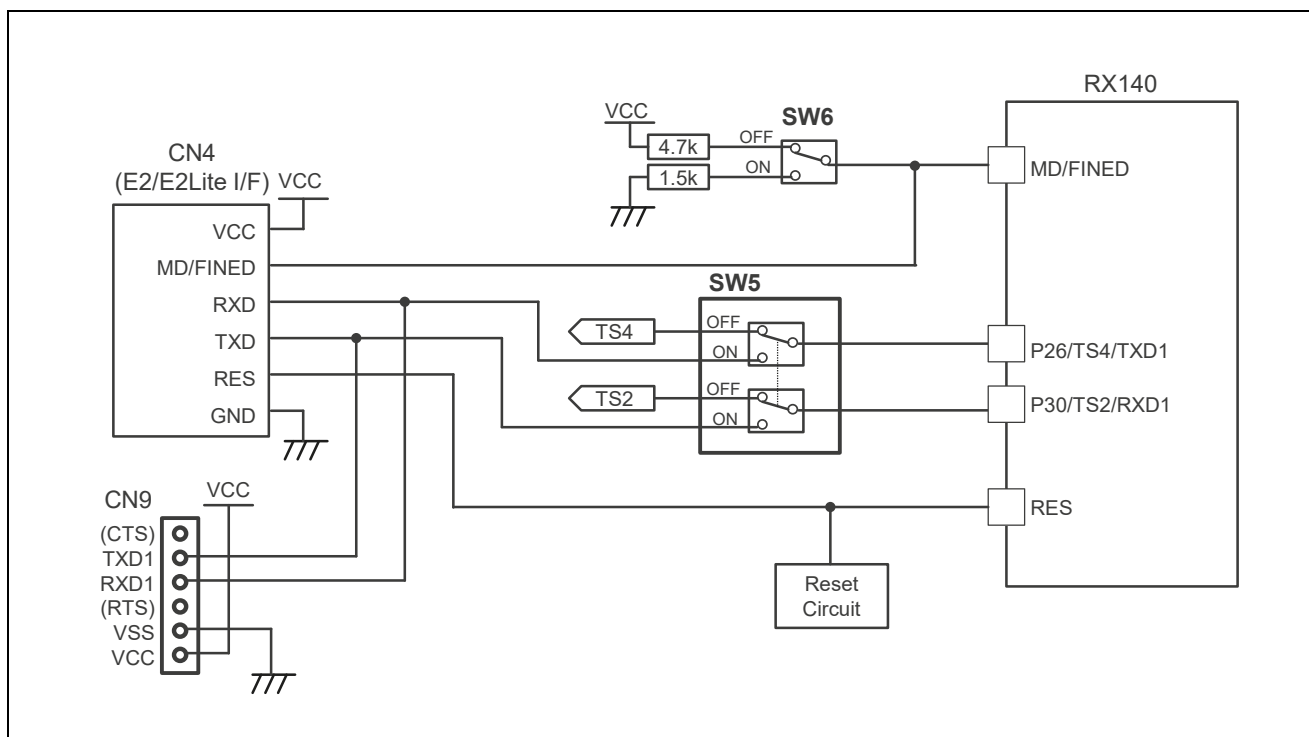
**Table 2-2. RX140 Operating Mode Settings**

Reference	Position	Default Setting (X)	MCU Operating Mode
SW6	OFF (pins 2-3)	X	Single Chip Mode
	ON (pins 1-2)		SCI Boot Mode

Table 2-3 lists the switch specifications for programming in SCI Boot Mode. Figure 2-5 shows the RX140 operating mode setting circuit. Use these settings for CN4 (E2/E2Lite) or CN9 to write a program in the SCI Boot Mode. Do not connect E2/E2 Lite when using CN9 to write a program in SCI Boot Mode.

**Table 2-3. Switch Specifications for Programming in SCI Boot Mode**

Reference	Position	Default Setting (X)	Description
SW5	OFF(pins 1-2)	X	Uses P26/TS4/TXD1 and P30/TS2/RXD1 as TS pins. Cannot be used when programming in SCI Boot Mode.
	ON(pins 2-3)		Uses P26/TS4/TXD1 P30/TS2/RXD1 programming pins. Can be used when programming in SCI Boot Mode



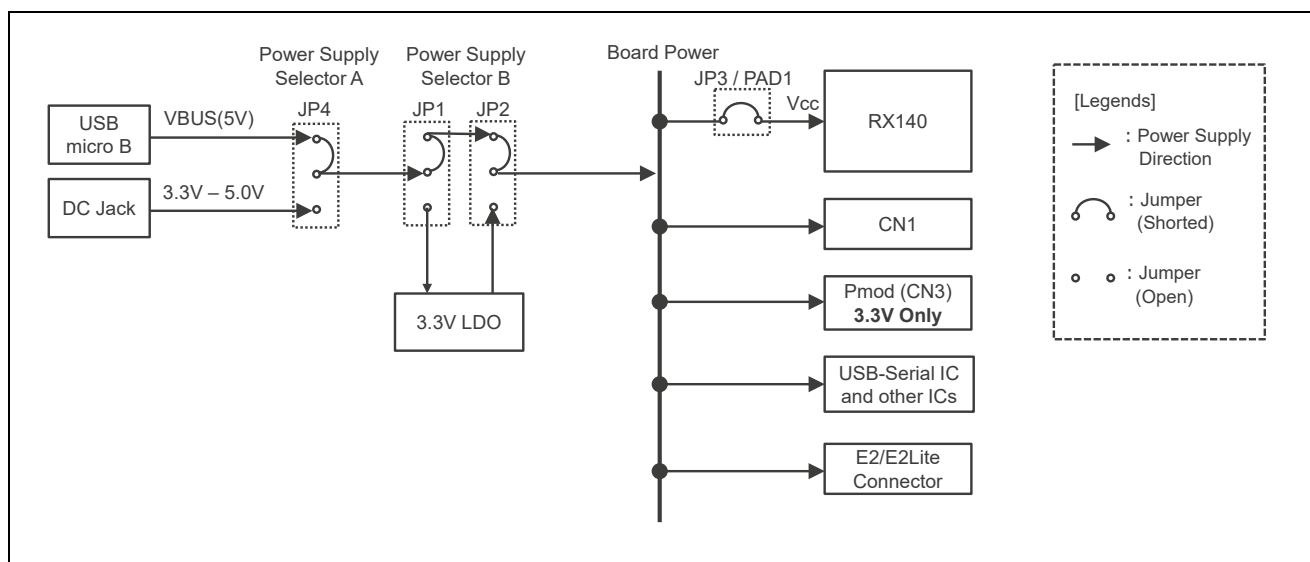
**Figure 2-5. RX140 Operating Mode Setting Circuit**

### 2.3.3 Power Supply

Table 2-4 provides the power supply jumper settings. Figure 2-6 the power source system diagram. This CPU board can be supplied with power by USB or a 2.1mm DC barrel jack connector (center + pin).

**Table 2-4. Power Source Jumper Settings**

Reference	Jumper Setting	Default Setting (X)	Description
JP4	Shorted Pins 1-2	X	Supplies USB power source to power source selector B (JP4)
	Shorted Pins 2-3		Supplies DC jack (CN5) to power source selector B (JP4)
JP1, JP2	Shorted Pins 1-2	X	Supplies JP4 power source to board power source
	Shorted Pins 2-3		Supplies LDO (3.3V) to board power source
JP3 / PAD1	Shorted Pins 1-2	X	Supplies board power source to MCU
	Open		MCU current consumption measurement setting



**Figure 2-6. Power Source System Diagram**

### 2.3.4 Clock Circuit

Table 2-5 lists the clock specifications of the CPU board.

**Table 2-5. Clock Specifications**

Clock	Function	Default State	Frequency	Package
X1	Main clock (crystal resonator)	Not mounted	16MHz	HC-49/S
X2	Sub clock	Not mounted	32.768kHz	3.2mm x 1.5mm SMD

Figure 2-7 shows the clock circuit.

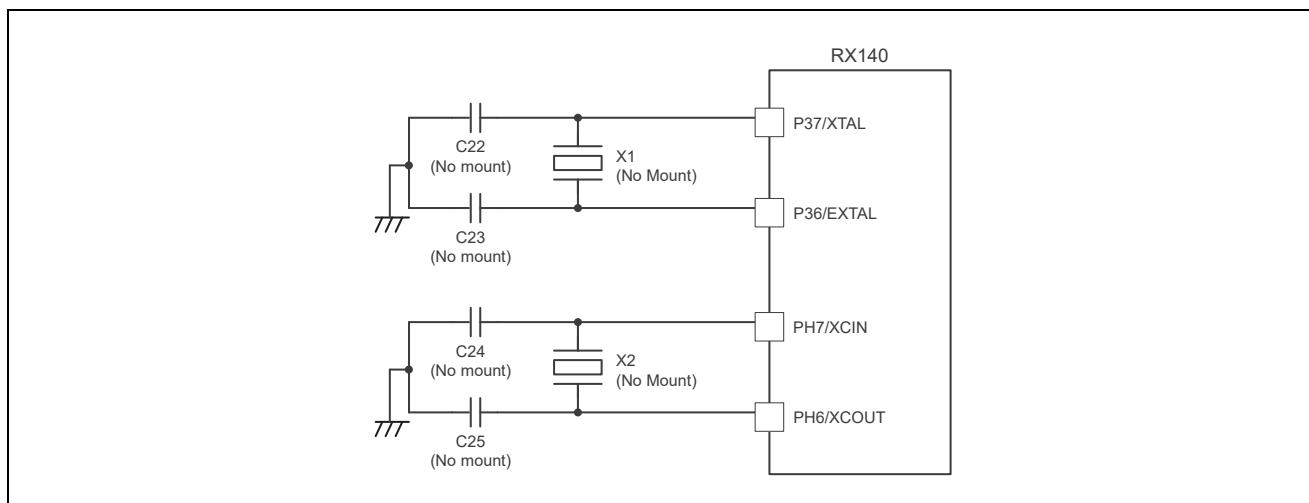


Figure 2-7. Clock Circuit

### 2.3.5 Reset Circuit

The RX140 CPU Board is equipped with a reset switch (SW4) which generates a reset signal to restart the main MCU.

### 2.3.6 Push Switch

Table 2-6. lists the push switch specifications. Figure 2-8 shows the push switch circuit.

Table 2-6. Push Switch Specifications

Reference	MCU Control Port	Function
SW4 (Reset)	RES#	Resets the MCU.
SW2	P35 / NMI	User controllable switch.
SW3	P34 / IRQ4	User controllable switch.

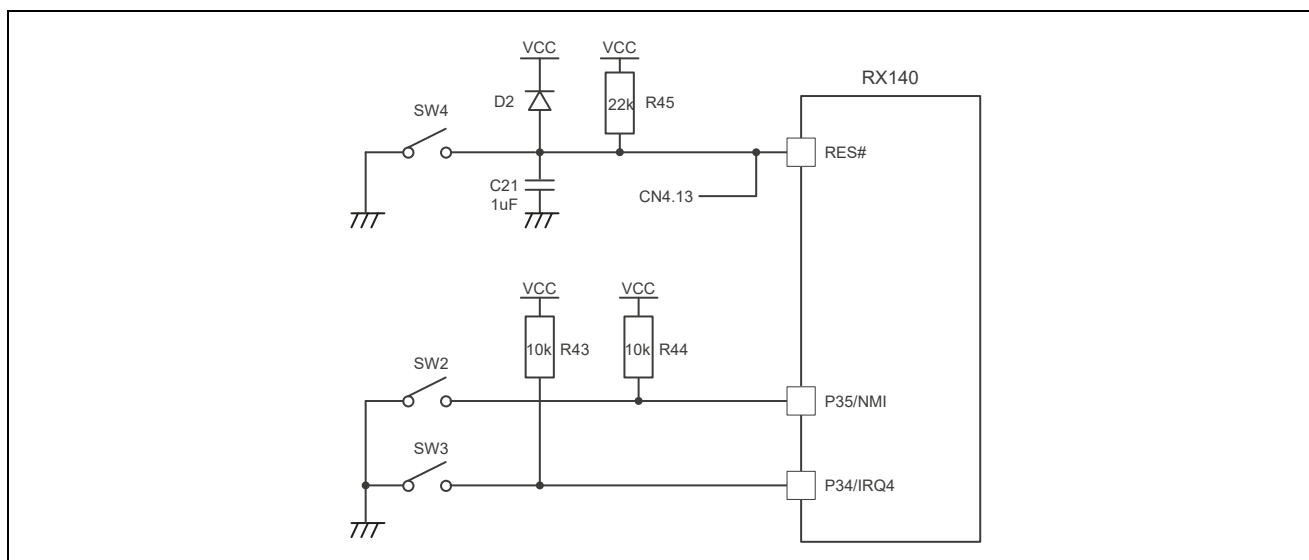


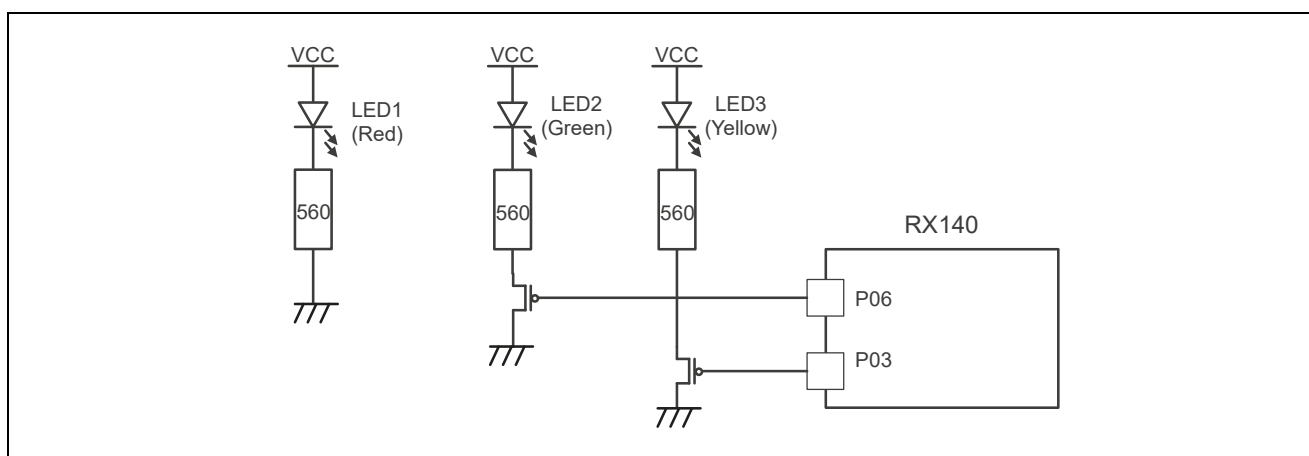
Figure 2-8. Push Switch Circuit

### 2.3.7 LEDs

Table 2-7 provides the connection port and function of each LED. Figure 2-9 shows the LED circuit.

**Table 2-7. LED Functions and Connections**

LED	MCU Control Port	Function	Color
LED1 (Power)	VCC	Power status display	Red
LED2	P06	User LED	Green
LED3	P03	User LED	Yellow



**Figure 2-9. LED Circuit**

### 2.3.8 USB Serial Conversion

Table 2-8 lists connections for USB serial IC and MCU controller. The RX140 Cap Touch Evaluation System Board is equipped with USB serial IC FT234XD (FTDI Ltd.) for USB serial conversion. The FT234XD is connected to the RX140's serial communication interface (SCI) module.

**Table 2-8. USB Serial Conversion**

Signal Name	MCU Control Port	Function
TXD	PD0 / TXD6	SCI6 transmit data signal
RXD	PD1 / RXD6	SCI6 receive data signal

### 2.3.9 Debug Interface

The CPU board is equipped with an E2/E2 Lite connector (14-pin box connector) for use as debugging interface. For details, refer to Reference Materials [2].

### 2.3.10 Application Header

The two application headers, CN1 and CN2, can be used as interface for the user's own board.

CN1 is used for GPIO pins. For more details regarding peripheral functions not listed here, refer to the User's Manual Hardware Version.

CN2 is mainly used for CTSU pins. Fix the GPIO pins to low by software. Do not connect anything to TSCAP.

**Table 2-9. Application Header (CN1)**

CN1			MCU			CN1			MCU		
Pin	Port	Peripheral	Pin	Port	Peripheral	Pin	Port	Peripheral	Pin	Port	Peripheral
16	VSS (GND)		15	VCC		13	PJ7		11	P40	
14	PE5	AN021	9	P42		7	P44		5	P46	
12	PJ6		3	PD2		1	PE1	TXD12/SSDA12			
10	P41										
8	P43										
6	P45										
4	P47										
2	PE0	RXD12/SSCL12									

**Table 2-10. Application Header (CN12)**

CN2			MCU			CN2			MCU		
Pin	Port	CTSU	Pin	Port	CTSU	Pin	Port	CTSU	Pin	Port	CTSU
40	PC4	TSCAP	39	—	—	35	PE3	TS34	33	PA0	TS32
38	—	—	37	—	—	31	PA2	TS30	29	PA4	TS28
36	PE2	TS35	27	PA6	TS26	25	PB1	TS24	23	PB3	TS22
34	PE4	TS33	21	PB5	TS20	17	PC3	TS16	15	PC6	TS14
32	PA1	TS31	13	P54	TS12	11	PH0	TS10	9	PH2	TS8
30	PA3	TS29	7	P14	TS6	5	P27	TS3	3	P32	TS0
28	PA5	TS27									
26	PB0	TS25									
24	PB2	TS23									
22	PB4	TS21									
18	PC2	TS17									
16	PC5	TS15									
14	PC7	TS13									
12	P55	TS11									
10	PH1	TS9									
8	PH3	TS7									
6	P15	TS5									
4	P31	TS1									
2	P26/TXD1	TS4	1	P30/RXD1	TS2						

— : Not Applicable

### 2.3.11 CTSU Related Circuits

#### 2.3.11.1 CTSU correction circuit

This circuit improves the absolute accuracy of the CTSU's capacitive measurement. This function requires the use of additional control software (sold separately). When using the CTSU pin in normal conditions make sure you it set to the OFF position.

**Table 2-11. CTSU Correction Circuit Switch**

Reference	Position	Default Setting (X)	MCU Operating Mode
SW1	OFF	X	Uses TS35 as the normal CTSU pin.
	ON		Uses TS35 as the CTSU correction pin.

### 2.3.12 Pmod Interface (CN3)

Header CN3 is the interface for Pmod Type 6A. When using this connector, set the power supply voltage to 3.3V.

**Table 2-12. CN3 Pin Assignments**

Pin	Pmod Type6A Function	MCU Port
1	NC	P13/IRQ3
2	NC	P20
3	SCL	P16/SCL0-DS
4	SDA	P17/SDA0-DS
5	GND	VSS(GND)
6	VCC	VDD
7	GPIO	P12/IRQ2
8	GPIO	P21
9	GPIO	P07
10	GPIO	P05
11	GND	VSS(GND)
12	VCC	VCC

### 3. Application Board (Self-Capacitance Electrode Board)

#### 3.1 Board Layout

Figure 3-1 shows the layout of the RX140 application board.

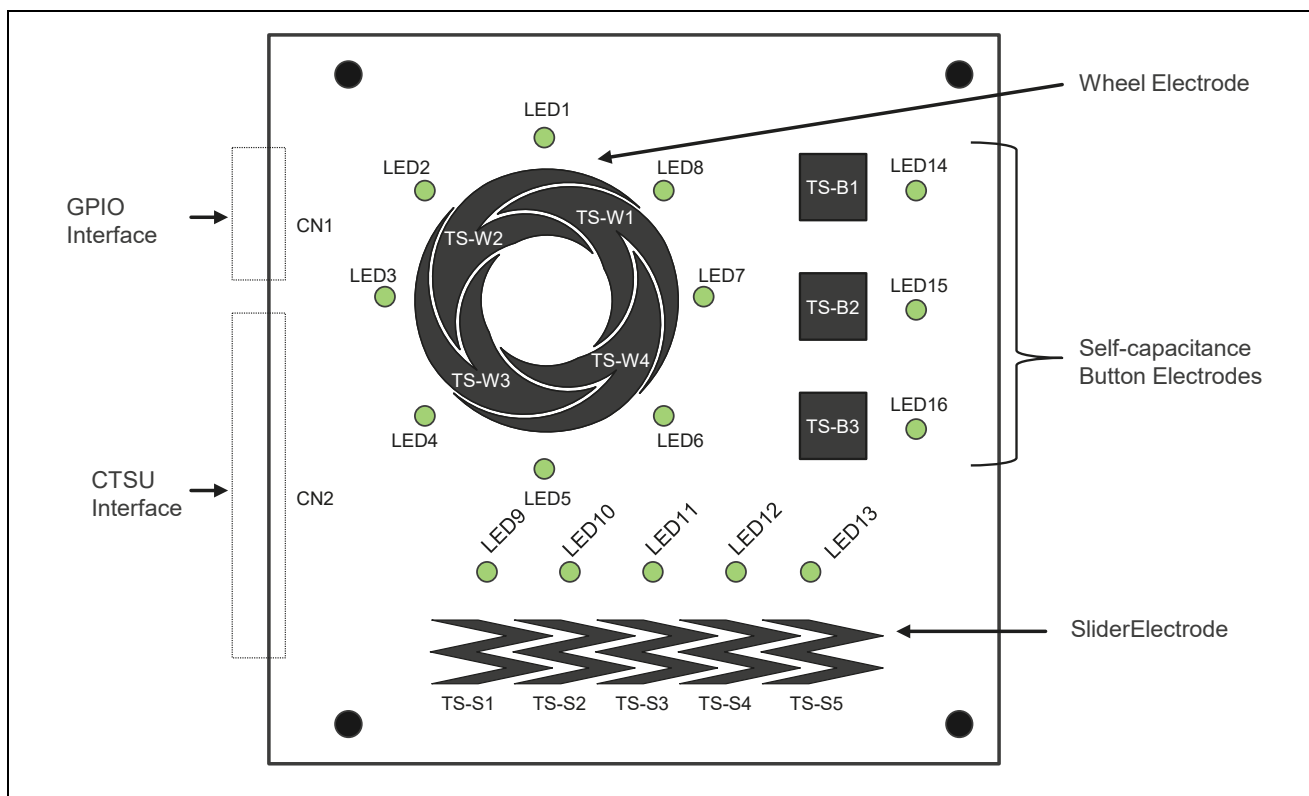


Figure 3-1. Board Layout and TS Pin Assignments

#### 3.2 Application Headers

Application headers CN1 and CN2 are the interface for connection to the Renesas Capacitive Touch Evaluation System CPU board. Table 3-1 lists the pin assignments for CN1. Table 3-2 lists the pin assignments for CN2.

Table 3-1. Application Header (CN1)

Pin	Function	MCU Connection	Pin	Function	MCU Connection
15	LED_VCC	VCC	16	LED_VSS (GND)	VSS (GND)
13	LED_ROW0	PJ7	14	LED_ROW1	PE5
11	LED_ROW2	P40	12	LED_ROW3	PJ6
9	—	P42	10	—	P41
7	LED_COL3	P44	8	—	P43
5	LED_COL1	P46	6	LED_COL2	P45
3	—	PD2	4	LED_COL0	P47
1	—	PE1	2	—	PE0

— : Not Applicable

**Table 3-2. Application Header (CN2)**

Pin	Touch Electrode	CTSU (RX140) (Note1)	Pin	Touch Electrode	CTSU (RX140) (Note1)
39	—	—	40	—	TSCAP
37	—	—	38	—	—
35	—	TS34	36	TS-W1	TS35
33	—	TS32	34	TS-W2	TS33
31	TS-W3	TS30	32	—	TS31
29	—	TS28	30	—	TS29
27	—	TS26	28	TS-W4	TS27
25	—	TS24	26	—	TS25
23	—	TS22	24	—	TS23
21	—	TS20	22	SHIELD-W1	TS21 (Note2)
19	—	TS18	20	—	TS19
17	—	TS16	18	—	TS17
15	—	TS14	16	—	TS15
13	—	TS12	14	—	TS13
11	—	TS10	12	—	TS11
9	TS-B1	TS8	10	TS-B2	TS9
7	SHIELD-B1	TS6 (Note2)	8	TS-B3	TS7
5	TS-S1	TS3	6	SHIELD-S1	TS5 (Note2)
3	TS-S3	TS0	4	TS-S2	TS1
1	TS-S5	TS2	2	TS-S4	TS4

— : Not Applicable

Note 1: Set output of all unassigned pins to low by software.

Note 2: SHIELD-S1, SHIELD-W1 and SHIELD-B1 are shield electrodes. To enable the function, set the pin to shield signal output mode by software.



## 4. Reference Materials

- [1]. Renesas RX140 Group User's Manual: Hardware (R01UH0905)
- [2]. E1/E20/E2 Emulator, E2 Emulator Lite Additional Document for User's Manual (Notes on Connection of RX Devices) (R20UT0399)

## 5. Additional Information

### Support

Refer to the Integrated Development Environment help menu for more information on how to use the IDE.

Refer to the RX140 Group User's Manual Hardware Version for more information on RX140 Group MCUs.

For general information on Renesas microcontrollers, visit : <https://www.renesas.com/>

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## Appendix: Self-Capacitance Touch Electrode Board Connection

The following describes how to use the Renesas Self-Capacitance Touch Electrode Board (RTK0EG0019B01002BJ).

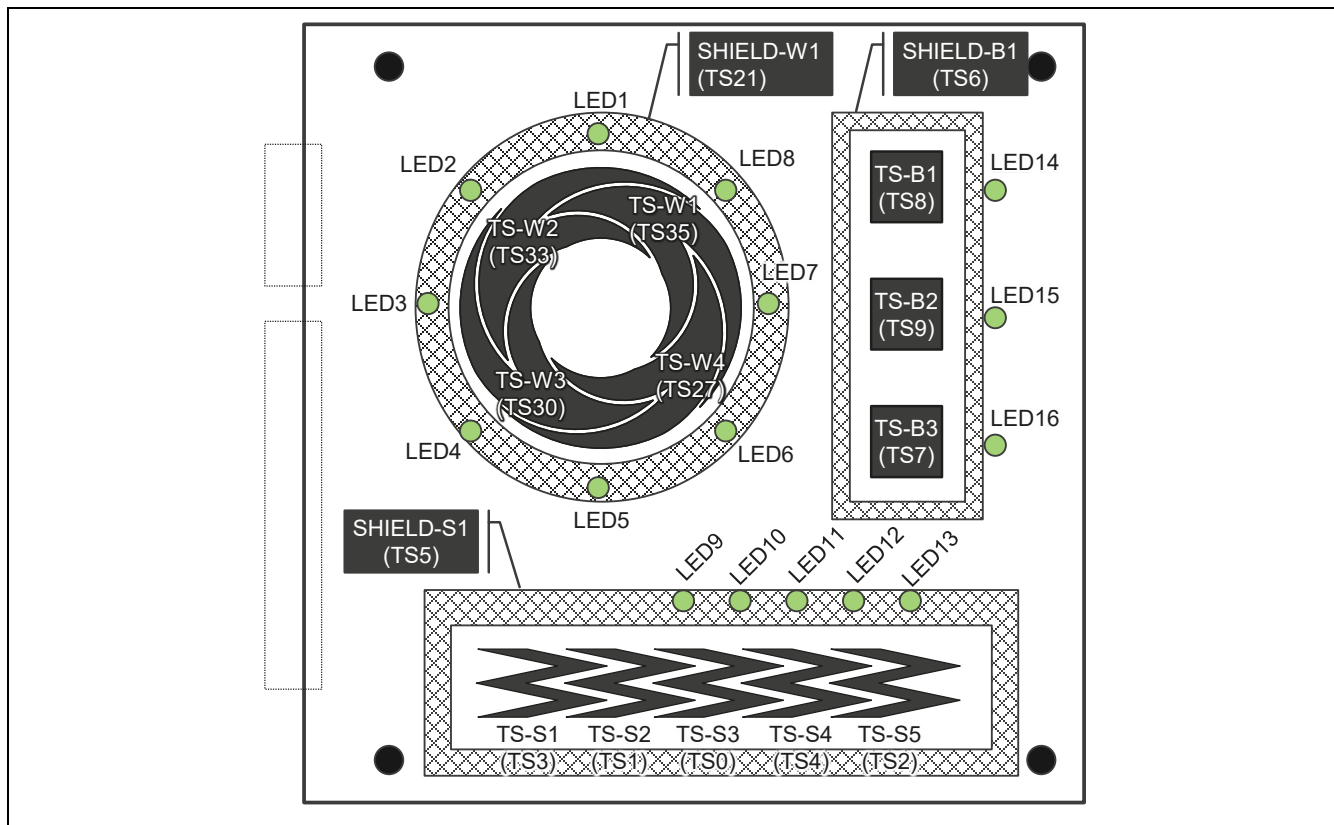


Figure 1. Board Layout and TS Pin Assignments

Table 1. TS Pin (CN2) Assignments

Touch Electrode	CTSU (RX140)	Description
TS-W1	TS35	Wheel Electrode
TS-W2	TS33	
TS-W3	TS30	
TS-W4	TS27	
SHIELD-W1	TS21 (Note)	Wheel Shield Electrode
TS-B1	TS8	Button Electrode
TS-B2	TS9	
TS-B3	TS7	
SHIELD-B1	TS6 (Note)	Button Shield Electrode
TS-S1	TS3	Slider Electrode
TS-S2	TS1	
TS-S3	TS0	
TS-S4	TS4	
TS-S5	TS2	
SHIELD-S1	TS5 (Note)	Slider Shield Electrode

Note: Set the pin to shield signal output mode by software.

**Table 2. TS Pin (CN2) Assignments**

Signal	RX140 Port	Description
LED_ROW0	PJ7	LED Matrix, High Side Transistor Drive
LED_ROW1	PE5	
LED_ROW2	P40	
LED_ROW3	PJ6	
LED_COL0	P47	LED Matrix, Low Side Transistor Drive
LED_COL1	P46	
LED_COL2	P45	
LED_COL3	P44	
LED_VCC	VCC	VCC
LED_GND	GND	GND

**Table 3. LED Matrix Table**

	LED_COL0	LED_COL1	LED_COL2	LED_COL3
LED_ROW0	LED1	LED5	LED13	LED9
LED_ROW1	LED2	LED6	LED14	LED10
LED_ROW2	LED3	LED7	LED15	LED11
LED_ROW3	LED4	LED8	LED16	LED12

**Table 4. LED Status and Pin Output Settings**

LED	LED_ROWn Connection Pin	LED_COLn Connection Pin
On	Low	High
Off	High	Low

Note: n=0~3

Revision History	RX140 Group Capacitive Touch Evaluation System User's Manual
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Rev.	Date	Description	
		Page	Summary
1.00	Feb.25.22	—	First Edition issued

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