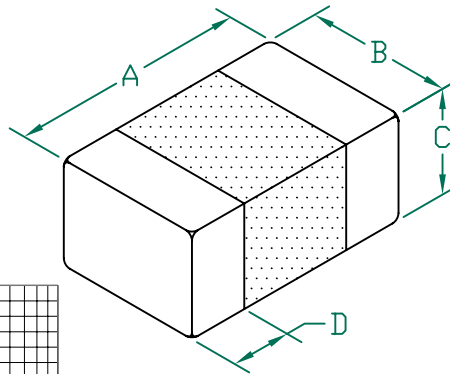


HZ0805B222R-10

UNCONTROLLED DOCUMENT

PHYSICAL DIMENSIONS:

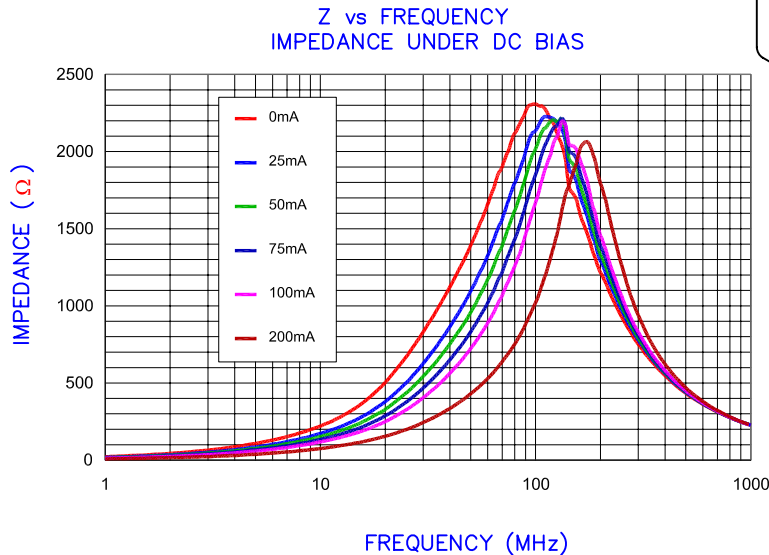
A	2.00	[.079]	±	0.20	[.008]
B	1.25	[.049]	±	0.20	[.008]
C	0.89	[.035]	±	0.20	[.008]
D	0.51	[.020]	±	0.25	[.010]



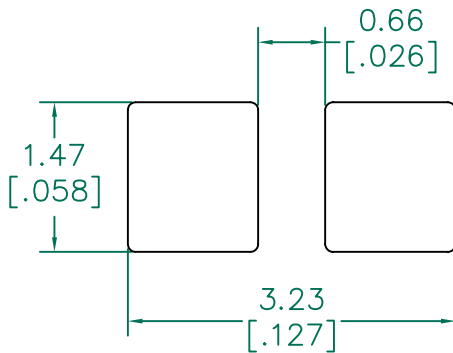
ELECTRICAL CHARACTERISTICS:

Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	2200	
Minimum	1650	
Maximum	2750	0.8
		200 mA

- NOTES: UNLESS OTHERWISE SPECIFIED
- TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REEL, 4000 PCS/REEL, PAPER TAPE.
 - COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
 - TERMINATION FINISH 100% TIN.

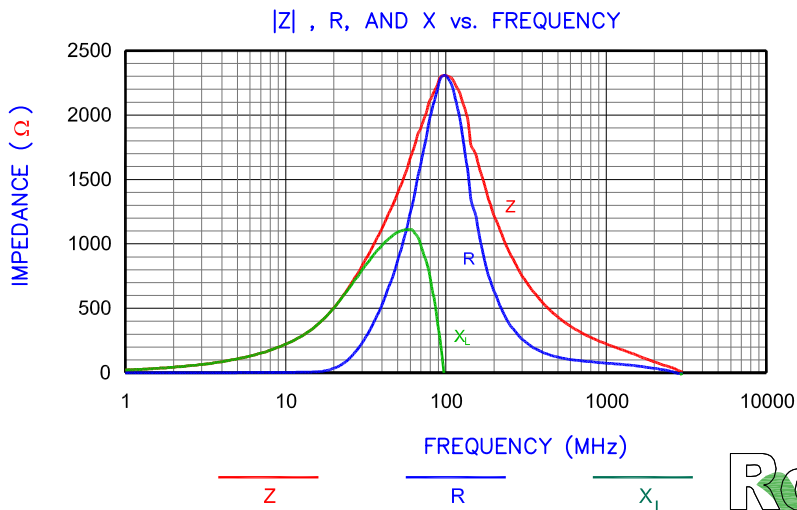
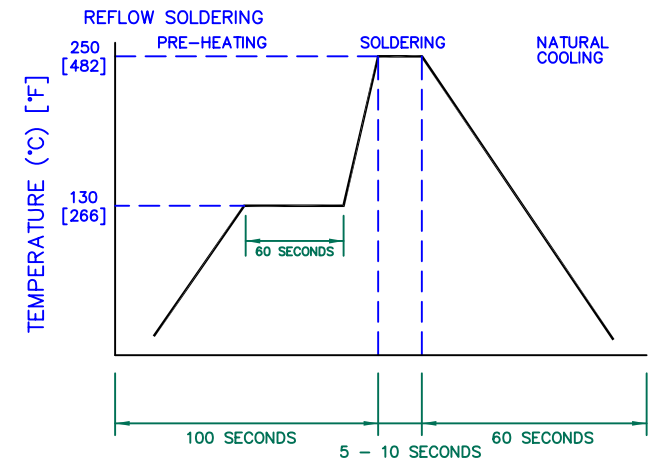


LAND PATTERNS FOR REFLOW SOLDERING

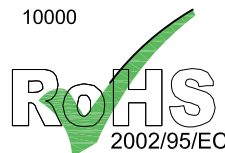


(For wave soldering, add 0.763 [.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS



AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 5569



DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.		Laird TECHNOLOGIES		
E	CHANGE TO PAPER TAPE	03/03/10	JUN	PROJECT/PART NUMBER:		REV	PART TYPE:	DRAWN BY:
D	CHG C DIM FROM [.049] Chg REEL QTY.	07/09/08	JRK	HZ0805B222R-10		E	CO-FIRE	JRK
C	UPDATE COMPANY LOGO	03/18/08	JRK	DATE:		SCALE:		SHEET:
B	ADD BIAS CURVE UPDATE LOGO	07/31/07	JRK	04/07/06		NTS		
A	ORIGINAL DRAFT	04/07/06	JRK	CAD #		TOOL #		
REV	DESCRIPTION	DATE	INT	HZ0805B222R-10-E		-		2 of 2