

36V 2ch Stepping Motor Driver

BD68888AEKV

General Description

BD68888AEKV is a bipolar low-consumption driver that driven by PWM constant current. Rated power supply voltage of the device is 36 V, and rated output current is 1.5 A. PARA-IN drive mode is adopted for input interface. This motor driver can drive 2ch bipolar stepping motors. In addition, the power supply can be driven by one single system, which simplifies the design.

Features

- Two bipolar stepping motors can be driven
- Rated Output Current 1.5 A
- Low ON Resistance DMOS Output
- PARA-IN Drive Mode correspondence
- PWM Constant Current control (the other excitation method)
- Built-in Spike Noise Blanking Function (external noise filter is unnecessary)
- Full-, Half (two kinds)-, Quarter-step correspondence
- Power Save Function
- Built-in Logic Input Pull-down Resistor
- Thermal Shutdown Circuit (TSD)
- Over-current Protection Circuit (OCP)
- Under Voltage Lockout Circuit (UVLO)
- Over Voltage Lockout Circuit (OVLO)
- Ghost Supply Prevention (protects against malfunction when power supply is disconnected)

Application

- Monitoring Camera, WEB Camera, PPC, Multi-function Printer, Laser Beam Printer, Ink-jet Printer, Sewing Machine, Photo Printer, FAX, Scanner, Mini Printer, Toy and Robot

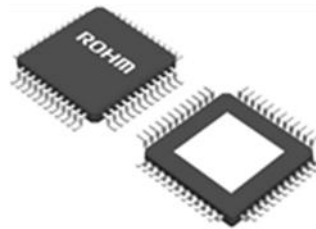
Key Specifications

- Range of Power Supply Voltage 8 V to 28 V
- Rated Output Current 1.5 A
- Range of Operating Temperature -25 °C to +85 °C
- Output ON Resistance 1.0 Ω (Typ)
(total of upper and lower resistors)

Package

HTQFP48V

W(Typ) x D(Typ)x H(Max)
9.00 mm x 9.00 mm x 1.00 mm



Typical Application Circuit

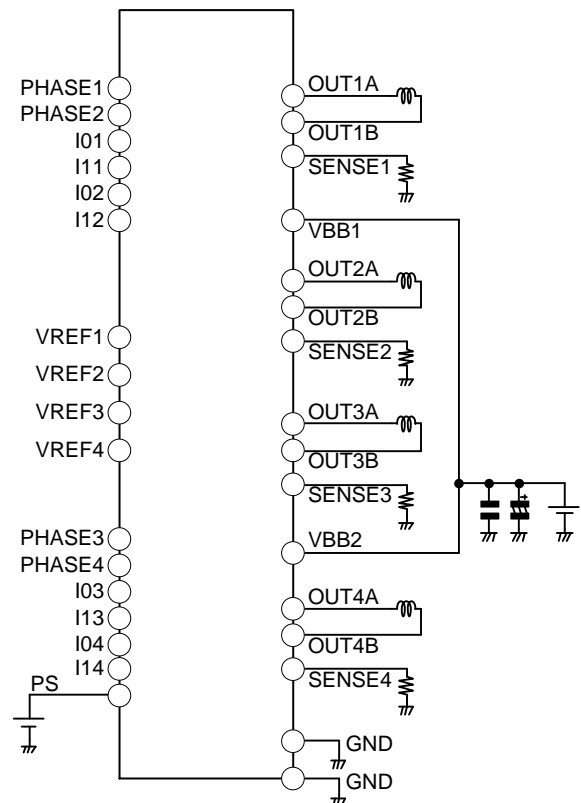


Figure 1. BD68888AEKV Application Circuit Diagram

Pin Configuration

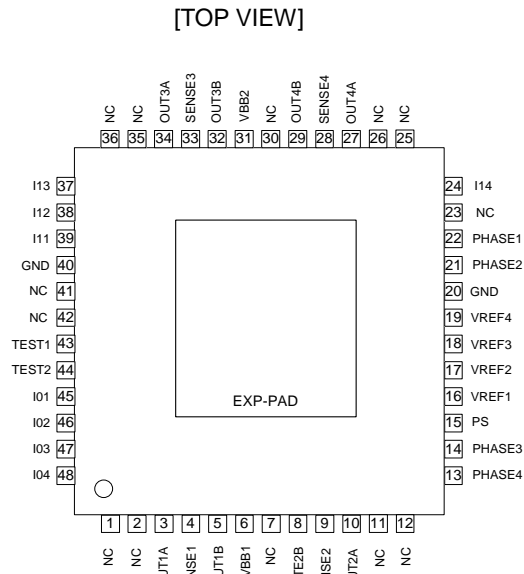


Figure 2. Pins Configuration Diagram

Block Diagram

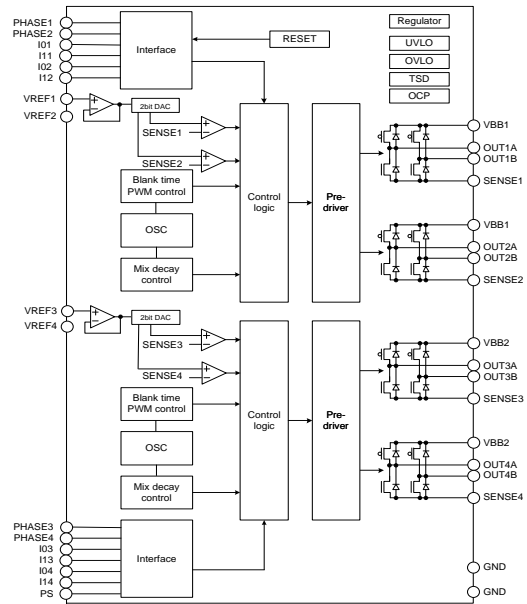


Figure 3. BD68888AEKV Block Diagram

Pin Descriptions

Pin No.	Pin Name	Function	Pin No.	Pin name	Function
1	NC	Non connection	25	NC	Non connection
2	NC	Non connection	26	NC	Non connection
3	OUT1A	H bridge output pin	27	OUT4A	H bridge output pin
4	SENSE1	Connection pin of resistor for output current detection	28	SENSE4	Connection pin of resistor for output current detection
5	OUT1B	H bridge output pin	29	OUT4B	H bridge output pin
6	VBB1	Power supply pin	30	NC	Non connection
7	NC	Non connection	31	VBB2	Power supply pin
8	OUT2B	H bridge output pin	32	OUT3B	H bridge output pin
9	SENSE2	Connection pin of resistor for output current detection	33	SENSE3	Connection pin of resistor for output current detection
10	OUT2A	H bridge output pin	34	OUT3A	H bridge output pin
11	NC	Non connection	35	NC	Non connection
12	NC	Non connection	36	NC	Non connection
13	PHASE4	Phase selection pin	37	I13	VREF division ratio setting pin
14	PHASE3	Phase selection pin	38	I12	VREF division ratio setting pin
15	PS	Power save pin	39	I11	VREF division ratio setting pin
16	VREF1	Output current value setting pin	40	GND	Ground pin
17	VREF2	Output current value setting pin	41	NC	Non connection
18	VREF3	Output current value setting pin	42	NC	Non connection
19	VREF4	Output current value setting pin	43	TEST1	Pin for testing
20	GND	Ground pin	44	TEST2	Pin for testing
21	PHASE2	Phase selection pin	45	I01	VREF division ratio setting pin
22	PHASE1	Phase selection pin	46	I02	VREF division ratio setting pin
23	NC	Non connection	47	I03	VREF division ratio setting pin
24	I14	VREF division ratio setting pin	48	I04	VREF division ratio setting pin
-	EXP-PAD	The EXP-PAD of the center of product connect to GND.	-	-	-

Absolute Maximum Ratings (Ta=25 °C)

Item	Symbol	Rated Value	Unit
Supply Voltage	VBB1,VBB2	-0.2 to +36.0	V
Input Voltage for Control Pin	V _{IN}	-0.2 to +5.5	V
SENSE Maximum Input Voltage	V _{SENSE}	0.7	V
Output Current ^(Note 2)	I _{OUT}	1.5 ^(Note 1)	A/Phase
Storage Temperature Range	T _{stg}	-55 to +150	°C
Maximum Junction Temperature	T _{jmax}	+150	°C

Caution 1: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

Caution 2: Should by any chance the maximum junction temperature rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. In case of exceeding this absolute maximum rating, design a PCB boards with thermal resistance taken into consideration by increasing board size and copper area so as not to exceed the maximum junction temperature rating.

(Note 1) Do not exceed T_{jmax}=150 °C.

Recommended Operating Conditions

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	Topr	-25	+25	+85	°C
Supply Voltage	VBB1,VBB2	+8	+24	+28	V
Maximum Output Current (DC)	I _{OUT}	-	-	+1.2 ^(Note 2)	A/ Phase

(Note 2) Do not exceed T_{jmax}=150 °C.

Thermal Resistance^(Note 3)

Parameter	Symbol	Thermal Resistance (Typ)		Unit
		1s ^(Note 5)	2s2p ^(Note 6)	
HTQFP48V				
Junction to Ambient	θ _{JA}	82.6	21.4	°C/W
Junction to Top Characterization Parameter ^(Note 4)	Ψ _{JT}	3	2	°C/W

(Note 3) Based on JESD51-2A (Still-Air).

(Note 4) The thermal characterization parameter to report the difference between junction temperature and the temperature at the top center of the outside surface of the component package.

(Note 5) Using a PCB board based on JESD51-3.

(Note 6) Using a PCB board based on JESD51-5, 7.

Layer Number of Measurement Board	Material	Board Size
Single	FR-4	114.3 mm x 76.2 mm x 1.57 mmt

Top	
Copper Pattern	Thickness
Footprints and Traces	70 μm

Layer Number of Measurement Board	Material	Board Size	Thermal Via ^(Note 7)	
			Pitch	Diameter
4 Layers	FR-4	114.3 mm x 76.2 mm x 1.6 mmt	1.20 mm	Φ0.30 mm

Top		2 Internal Layers		Bottom	
Copper Pattern	Thickness	Copper Pattern	Thickness	Copper Pattern	Thickness
Footprints and Traces	70 μm	74.2 mm x 74.2 mm	35 μm	74.2 mm x 74.2 mm	70 μm

(Note 7) This thermal via connects with the copper pattern of all layers.

Electrical Characteristics (Unless otherwise specified Ta=25 °C, VBB1, VBB2=24 V)

Item	Symbol	Limit			Unit	Condition
		Min	Typ	Max		
[Whole]						
Circuit Current at Standby	I _{CCST}	-	0	10	μA	PS=L
Circuit Current	I _{CC}	-	5.0	8.0	mA	PS=H, VREF _x (<i>Note 8</i>)=1.5 V
[Control Input]						
H-level Input Voltage	V _{INH}	2.0	-	-	V	
L-level Input Voltage	V _{INL}	-	-	0.8	V	
H-level Input Current	I _{INH}	35	50	100	μA	V _{IN} =5 V
L-level Input Current	I _{INL}	-10	0	-	μA	V _{IN} =0 V
[Output]						
Output ON Resistance	R _{ON}	-	1.0	1.4	Ω	I _{OUT} =±1.0 A (total of upper and lower)
Output Leak Current	I _{LEAK}	-	-	10	μA	
[Current Control]						
SENSE _x (<i>Note 9</i>) Input Current	I _{SENSE}	-80	-40	-	μA	SENSE _x =0 V
VREF _x Input Current	I _{VREF}	-2.0	-0.1	-	μA	VREF _x =0 V
VREF _x Input Voltage Range	V _{VREF}	0	-	1.5	V	
Minimum ON Time (Blank Time)	t _{ONMIN}	0.3	1.0	1.5	μs	
Comparator Threshold	V _{CTH}	0.48	0.50	0.52	V	VREF _x =1.5 V

(*Note 8*) x=1,2,3 or 4(*Note 9*) x=1,2,3 or 4

Function Explanation

PS/Power Save Pin

The PS pin can make the circuit standby state and make the motor output OPEN. When PS=L→H, be careful because there is a delay of 40 μs (Max) before it is returned from standby state to normal state and the motor output becomes ACTIVE.

PS	Status
L	Standby state
H	ACTIVE

PHASE_x (Note 10)/Phase Selection Pin

These pins determine output state.

PHASE _x	OUT _x A (Note 11)	OUT _x B (Note 12)
L	L	H
H	H	L

(Note 10) x=1, 2, 3 or 4

(Note 11) x=1, 2, 3 or 4

(Note 12) x=1, 2, 3 or 4

I0_x (Note 13), I1_x (Note 14)/VREF Division Ratio Setting Pin

Although VREF pins voltage is input to 2bit-DAC, these pins set the split ratio of 2bit-DAC output voltage.

I0 _x	I1 _x	Output Current Level (%)
L	L	100
H	L	67
L	H	33
H	H	0

(I0_x, I1_x)=(H, H): motor outputs are open.

(Note 13) x=1, 2, 3 or 4

(Note 14) x=1, 2, 3 or 4

VBB1, VBB2/Power Supply Pin

The wire should be thick, short and has low impedance, because Motor's drive current is flowing in it. The VBB1 pin and the VBB2 pin voltage may have great fluctuation, so arrange the bypass capacitor of about 100 μF to 470 μF as close to the pin as possible and adjust the VBB1 pin and the VBB2 pin voltage is stable. Increase the capacity as needed especially, when a large current is used or those motors that have great back electromotive force are used.

In addition, for the purpose of reducing of power supply's impedance in wideband, it is recommended to set parallel connection of multi-layered ceramic capacitor of 0.01 μF to 0.1 μF etc. Extreme care must be used to make sure that the VBB1 pin and the VBB2 pin voltage do not exceed the rating even for a moment. The VBB1 pin and the VBB2 pin are shorted inside the IC, but be sure to short externally the VBB1 pin and the VBB2 pin when using. If used without shorting, malfunction or destruction may occur because of concentration of current routes etc. Still more, in the power supply pin, there is built-in clamp component for preventing of electrostatic destruction. When a steep pulse signal or voltage such as a surge exceeding the absolute maximum rating is applied, this clamp component operates, and the IC might be destroyed as a result. Be sure that the maximum absolute rating must not be exceeded. It is effective to mount a Zener diode of about the maximum absolute rating. Moreover, the diode for preventing electrostatic destruction is inserted between the VBB1 pin, the VBB2 pin and the GND pin. Be careful about the reverse voltage because the IC might be destroyed as a result if reverse voltage is applied to the VBB1 pin, the VBB2 pin and the GND pin.

GND/Ground Pin

In order to reduce the electric noise by switching current and to stabilize the internal reference voltage of the IC, make the wiring impedance from this pin as low as possible to achieve the lowest electrical potential no matter what operating state it can be. Moreover, design the patterns not to have any common impedance with other GND patterns.

OUT_xA, OUT_xB/H Bridge Output Pin

Motor's drive current is flowing in it, so the wire should be thick, short and has low impedance. It is also effective to add a schottky diode if output has positive or negative great fluctuation when large current is used. For example, counter electromotive voltage etc. Moreover, in the output pin, there is built-in clamp component for preventing of electrostatic destruction. When a steep pulse signal or voltage such as a surge exceeding the absolute maximum rating is applied, this clamp component operates, and the IC might be destroyed in the end. Be sure that the maximum absolute rating must not exceeded.

Function Explanation – continued

SENSE_x(*Note 15*)/Connection Pin of Resistor for Detecting of Output Current

Connect the resistor of 0.1 Ω to 0.3 Ω for current detection between this pin and GND. Determine the resistor so that power consumption $W=I_{OUT}^2 \cdot R$ [W] of the current-detecting resistor does not exceed the maximum absolute rating of the resistor. In addition, it has a low impedance and does not have a common impedance with other GND patterns because motor's drive current flows in the pattern through the SENSE_x pin to current-detecting resistor to GND. Do not exceed the rating because there is the possibility of circuits' malfunction etc., if the SENSE pin voltage exceeds the maximum rating (0.7V). Moreover, be careful because if the SENSE_x pin is shorted to GND, large current flows without normal PWM constant current control, and OCP or TSD might operate. If there is a possibility of malfunction, such as output does not flow even when the SENSE_x pin is open, please do not put to such a state.

(*Note 15*) x=1, 2, 3 or 4

VREF_x(*Note 16*)/Output Current Value Setting Pin

This is the pin to set the output current value. It can be set by the VREF pin voltage and current-detecting resistor (SENSE resistor).

$$I_{OUT} = \frac{V_{REF}}{3} / SENSE \quad [A]$$

Where

I_{OUT} is the output current.

V_{REF} is the voltage of output current value-setting pin.

$SENSE$ is the current-detecting resistor.

Avoid using the IC with the VREF_x pin is open because if it is open, it may have malfunctions such as flowing a large current by unstable input, the increased the VREF_x pin voltage and increased setting current. The input voltage range must be kept because a large current might flow to output and OCP or TSD might operate if the voltage of over 1.5 V is applied on the VREF_x pin. Besides, select the resistance value in consideration of the outflow current (Max 2 μA) if it is inputted by resistance division. The minimum current, which can be controlled by the VREF_x pin voltage, is determined by motor coil's L, R values and minimum ON time because there is a minimum ON time in PWM drive.

(*Note 16*) x=1, 2, 3 or 4

TEST_x(*Note 17*)/Pin for Inspection

This pin is used for delivery inspection of the IC, and shall be grounded before use.

In addition, malfunctions can be caused by application without grounding.

(*Note 17*) x=1 or 2

NC Pin

This pin is unconnected electrically with the IC internal circuit.

EXP-PAD

For HTQFP48V package, the heat-radiating metal is mounted on the IC's backside. It is the precondition that making the heat-radiating treatment when in use. Therefore, it must be connected by solder with the GND plane on the board and ensure the sufficient heat-radiation area by taking the GND pattern as wide as possible. Moreover, the back side metal is shorted with IC chip's back side and becomes the GND potential, so there is the danger of malfunction and destruction if shorted with potentials other than GND. Never design any wiring patterns other than GND through the IC's backside.

Protection Circuits

Thermal Shutdown (TSD)

This IC has a built-in thermal shutdown circuit for thermal protection. When the IC's chip temperature rises to 175 °C (Typ) or more, the motor output becomes OPEN. Also, when the temperature returns to 150 °C (Typ) or less, it automatically returns to normal operation. However, if heat is continued to be added externally even while TSD is in operation, heat overdrive can lead to destruction.

Over Current Protection (OCP)

This IC has a built in over current protection circuit as a provision against destruction when the motor outputs are shorted each other or VBB1, VBB2-motor output or motor output-GND is shorted. This circuit latches the motor output to OPEN condition when the regulated current flows for 4 μs (Typ). It returns with power reactivation or a reset of the PS pin. The over current protection circuit's only aim is to prevent the destruction of the IC from irregular situations such as motor output shorts, and is not meant to be used as protection or security for the set. Therefore, sets should not be designed to take into account this circuit's functions. After OCP operating, if irregular situations continue and the return by power reactivation or a reset of the PS pin is carried out repeatedly, then OCP operates repeatedly and the IC may generate heat or otherwise deteriorate. When the L value of the wiring is great due to the wiring being long of faults, ground faults and shorting, there is a possibility of destruction after the over current has flowed and the output pin voltage jumps up and the absolute maximum values can be exceeded. Also, when current which is the output current rating or more and the OCP detection current or less flows, the IC can heat up to over $T_{jmax}=150$ °C and can deteriorate, so current which exceeds the output rating should not be applied.

Under Voltage Lock Out (UVLO)

This IC has a built-in under voltage lock out function to prevent false operation such as IC output during power supply under voltage. When the applied voltage to the VBB1 pin and the VBB2 pin goes 5 V (Typ) or less, the motor output is set to OPEN. This switching voltage has a 1 V (Typ) hysteresis to prevent malfunction due to noise etc. Be aware that this circuit does not operate during power save mode. Also, the electrical angle is reset when the UVLO circuit operates.

Over Voltage Lock Out (OVLO)

This IC has a built-in over voltage lock out function to protect the IC output and the motor during power supply over voltage. When the applied voltage to the VBB1 pin and the VBB2 pin goes 32 V (Typ) or more, the motor output is set to OPEN. This switching voltage has a 1 V (Typ) hysteresis and a 4 μs (Typ) mask time to prevent malfunction due to noise etc. Although this over voltage locked out circuit is built-in, there is a possibility of destruction if the absolute maximum value for power supply voltage is exceeded, therefore the absolute maximum value should not be exceeded. Be aware that this circuit does not operate during power save mode.

Ghost Supply Prevention (protects against malfunction when power supply is disconnected)

If a control signal (logic input, VREFx) is input when there is no power supplied to this IC, there is a function which prevents a malfunction where voltage is supplied to power supply of this IC or other IC in the set via the electrostatic destruction prevention diode from these input pins to the VBB1 pin and the VBB2 pin. Therefore, there is no malfunction of the circuit even when voltage is supplied to these input pins while there is no power supply.

Operation Under Strong Electromagnetic Field

The IC is not designed for using in the presence of strong electromagnetic field. Be sure to confirm that no malfunction is found when using the IC in a strong electromagnetic field.

PWM Constant Current Control

1) Current control operation

The output current increases due to the output transistor turned on. When the voltage on the SENSE_x^(Note 19) pin, the output current is converted voltage due to connect the external resistance to the SENSE_x pin, reaches the voltage value set by the internal 2-bit DAC and the VREF_x^(Note 20) input voltage, the current limit comparator engages and enters current decay mode. Thereafter the output turned on again after a period of time determined the internal timer. The process repeats itself constantly.

(Note 19) x=1, 2, 3 or 4

(Note 20) x=1, 2, 3 or 4

2) Noise-masking function

In order to avoid misdetection of current detection comparator due to SENSE_x spike noise that may occur when the output turns on, the IC employs the minimum ON-time (t_{ONMIN}). It invalids the current detection for the minimum ON-time of 1 μs (Typ) from the output transistor turned on. This allows constant-current drive without the need for an external filter.

3) Internal Timer

IC internal voltage repeat charging and discharging between VL to VH.

The detection of the internal comparator is masked while charging from VL to VH. This period defines the minimum ON-time (t_{ONMIN}). The internal voltage begins discharging once the voltage reaches VH. When the output current reaches the current limit during this period, then the IC enters decay mode. It reaches VL, at which point the IC internal voltage is switched back ON. The current output and internal terminal begin charging simultaneously.

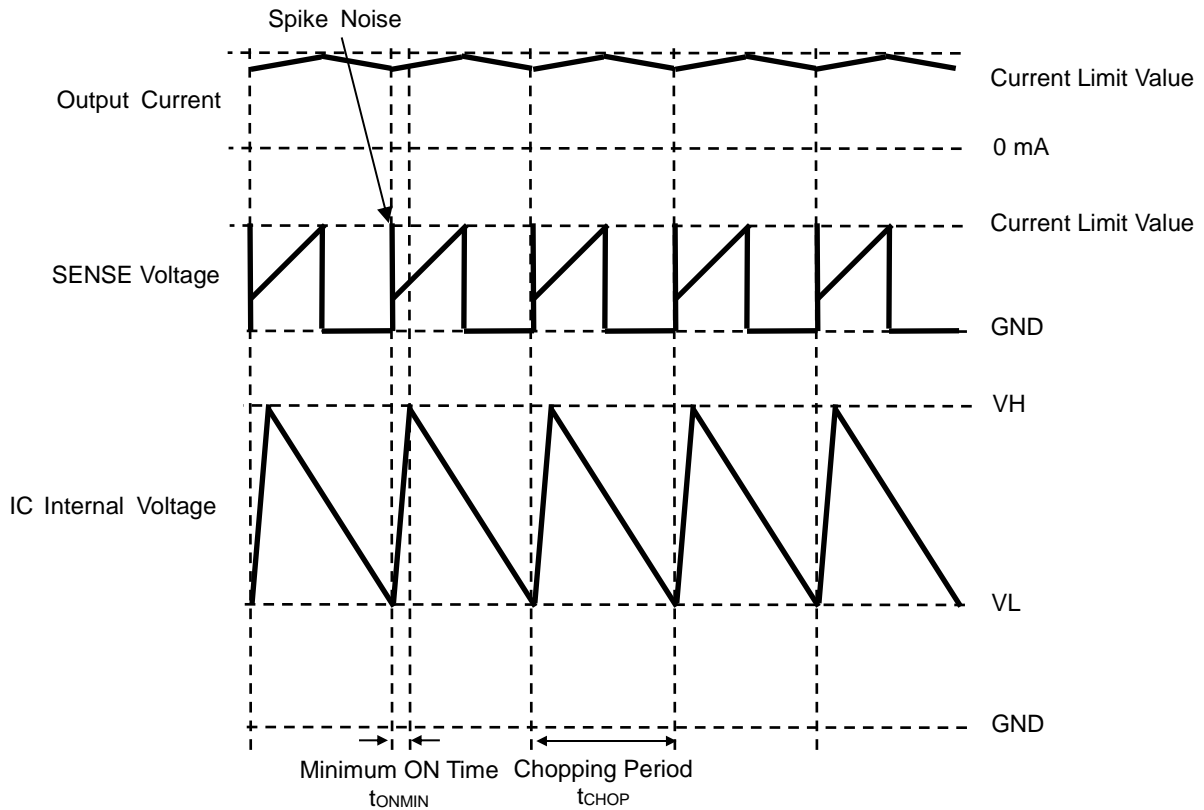


Figure 4. Timing Chart of IC Internal Voltage, the SENSE_x pin voltage and Output Current

PWM Constant Current Control – continued

Current Decay Mode

PWM Constant Current Control can be optionally set the current decay mode in which the ratio of MIX DECAY and SLOW DECAY.

The following diagrams show the state of each transistor and the regenerative current path during the current decay for each decay mode:

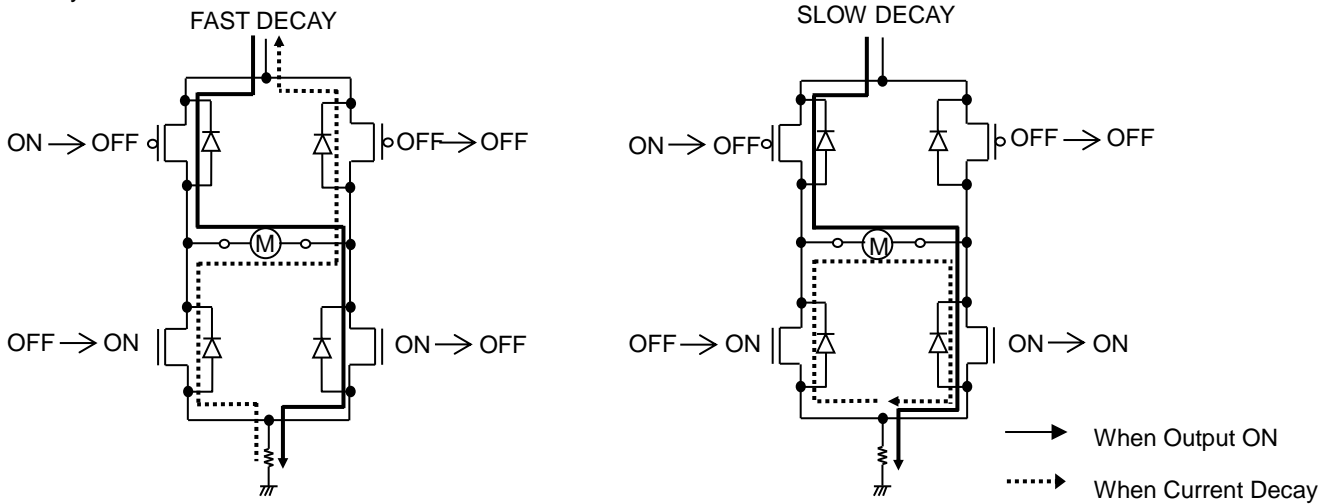


Figure 5. Route of Regenerated Current during Current Decay

The merits of each decay mode are as follows:

SLOW DECAY

The voltage of motor coils is small and the regenerative current decreases slowly. So the output current ripple is small and this is favorable for motor torque. However, it cannot follow the change of current limit value, the current waveform distorts and the motor vibration increases in output current due to deterioration of current controllability in the low-current region and it is easily influenced by EMF when high-pulse-rate in HALF STEP or QUARTER STEP modes. Thus, this decay mode is most suited to FULL STEP modes or low-pulse-rate as HALF STEP or QUARTER STEP modes.

FAST DECAY

FAST DECAY decreases the regeneration current much more quickly than slow decay, greatly reducing distortion of the output current waveform. However, FAST DECAY yields a much larger output current ripple, which decreases the overall average current running through the motor. This causes two problems: first, the motor torque decreases (increasing the current limit value can help eliminate this problem, but the rated output current must be taken into consideration); and second, the power loss within the motor increases and thereby radiates more heat. If neither of these problems is of concern, then FAST DECAY can be used for high-pulse-rate HALF STEP or QUARTER STEP drive

Additionally, this IC has MIX DECAY as a method to remedy the problems caused by the above SLOW DECAY and FAST DECAY. In this IC, SLOW DECAY / MIX DECAY (60 % Typ SLOW DECAY) can be selected. Switching between SLOW DECAY and FAST DECAY during current decay can improve current control without increasing current ripple.

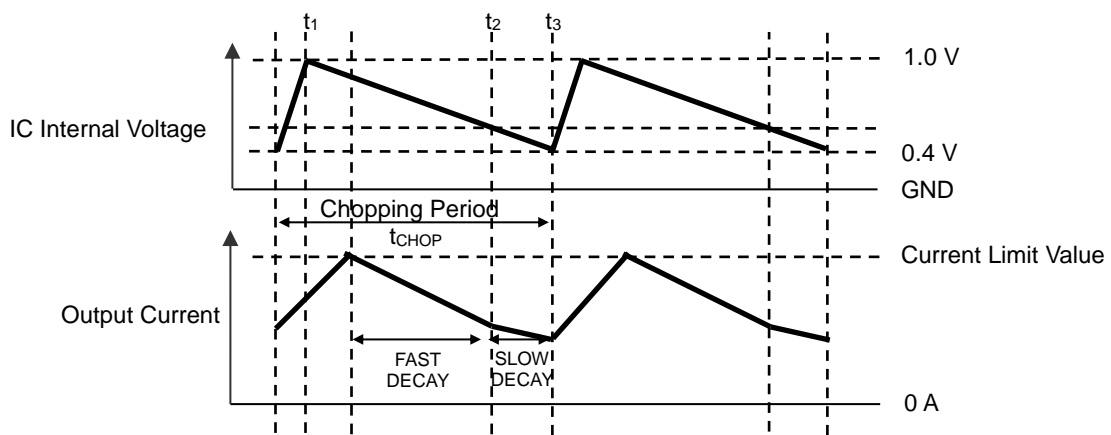


Figure 6. Internal Voltage and Output Current during MIX DECAY

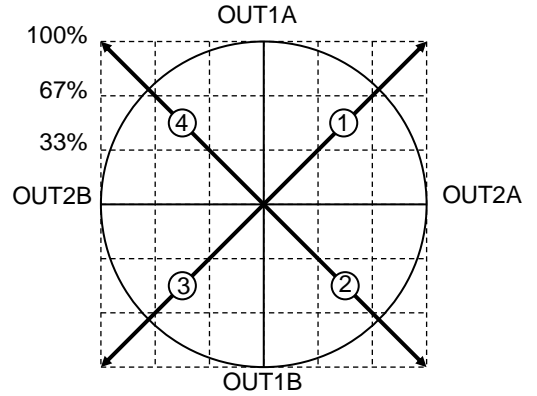
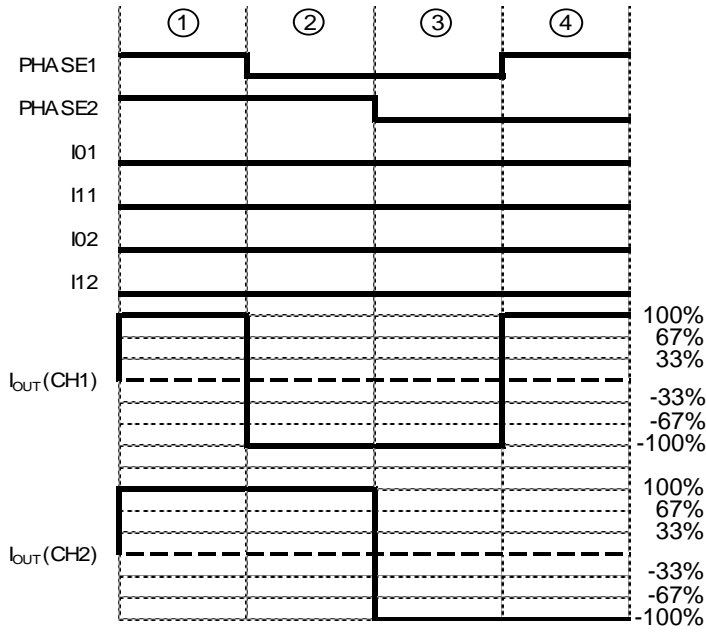
PARALLEL-IN Drive Mode - Description for CH1 (CH2: same as CH1)-

It is possible to drive stepping motor with FULL STEP, HALF STEP, and QUARTER STEP by inputting the following motor control signals using PARALLEL-IN drive mode.

Examples of control sequence and torque vector

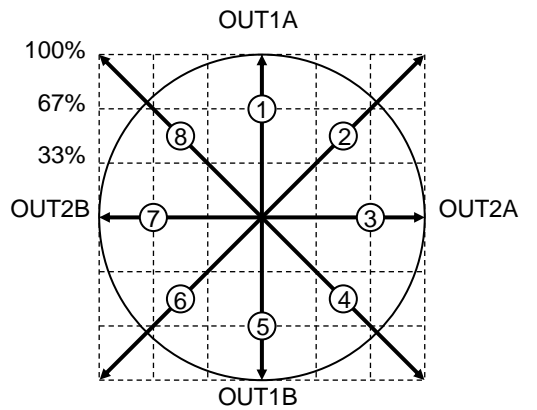
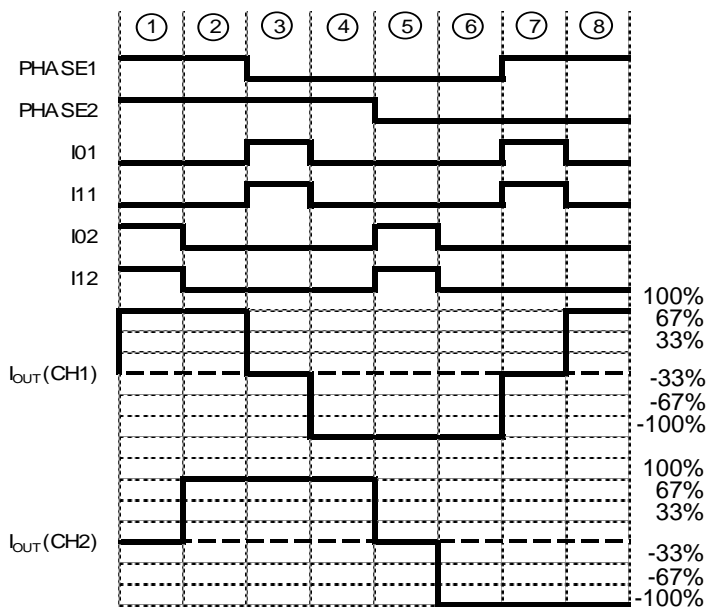
FULL STEP

Controlled by 2 logic signals of PHASE1 and PHASE2



HALF STEP A

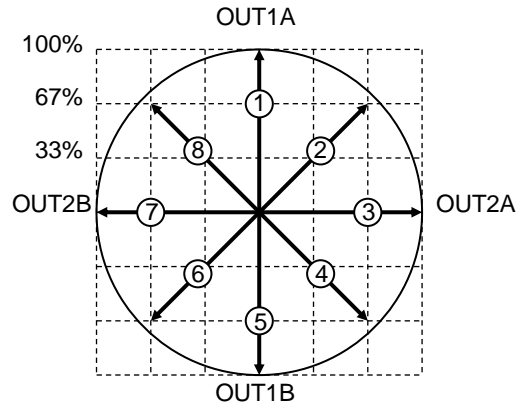
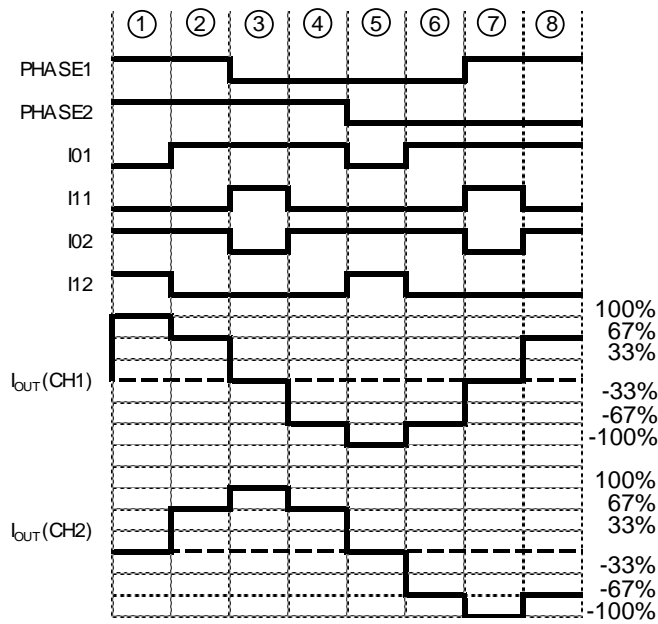
Controlled by 4 logic signals of PHASE1, PHASE2, I01 (I11), and I02 (I12)



PARALLEL-IN Drive Mode – continued

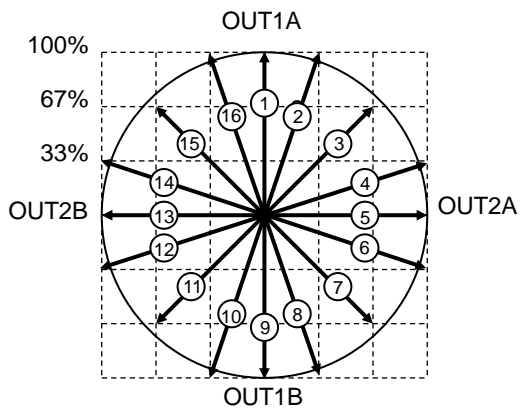
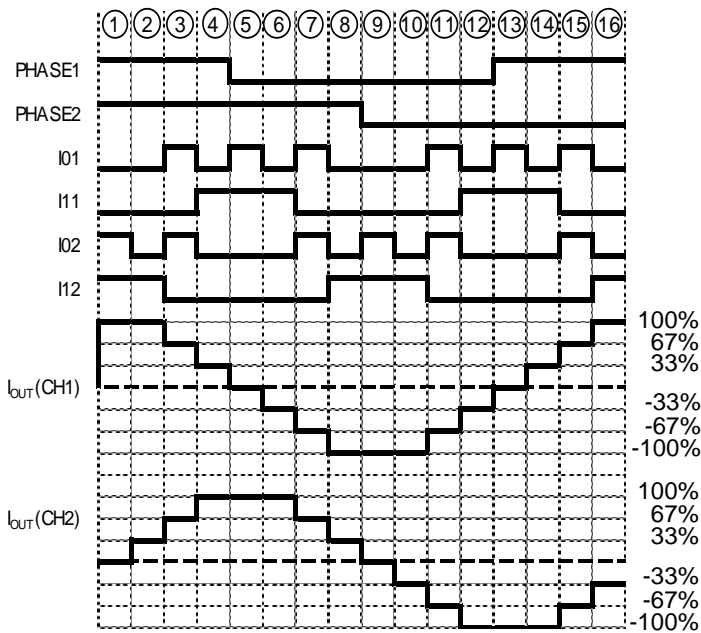
HALF STEP B

Controlled by 6 logic signals of PHASE1, PHASE2, I01, I11, I02 and I12



QUARTER STEP

Controlled by 6 logic signals of PHASE1, PHASE2, I01, I11, I02 and I12



Power Dissipation

Confirm that the IC's chip temperature T_j is not over 150 °C in consideration of the IC's power consumption (W), thermal resistance (°C/W) and ambient temperature (T_a). When $T_j=150$ °C is exceeded, the functions as a semiconductor do not operate and problems such as parasitism and leaks occur. Constant use under these circumstances leads to deterioration and eventually destruction of the IC. $T_{jmax}=150$ °C must be strictly obeyed under all circumstances.

Thermal Calculation

The IC's consumed power can be estimated roughly with the power supply voltage (V_{BB1} and V_{BB2}), circuit current (I_{CC}), output ON resistance (R_{ONH} , R_{ONL}) and motor output current value (I_{OUT}).

The calculation method during FULL STEP drive, SLOW DECAY mode is shown here:

$$W_{VBB} = VBB \times I_{CC} \quad [W]$$

where:

W_{VBB} is the consumed power of the VBB.
 VBB is the power supply voltage.
 I_{CC} is the circuit current.

$$W_{DMOS} = W_{ON} + W_{DECAY} \quad [W]$$

$$W_{ON} = (R_{ONH} + R_{ONL}) \times I_{OUT}^2 \times 2 \times on_duty \quad [W]$$

$$W_{DECAY} = (2 \times R_{ONL}) \times I_{OUT}^2 \times 2 \times (1 - on_duty) \quad [W]$$

where:

W_{DMOS} is the consumed power of the output DMOS.
 W_{ON} is the consumed power during output ON.
 W_{DECAY} is the consumed power during current decay.
 R_{ONH} is the upper P-channel DMOS ON-resistance.
 R_{ONL} is the lower N-channel DMOS ON-resistance.
 I_{OUT} is the motor output current value.
 on_duty PWM on duty= t_{ON} / t_{CHOP}
 "2" is the H bridge A and B.

t_{ON} varies depending on the L and R values of the motor coil and the current set value. Confirm by actual measurement, or make an approximate calculation.

t_{CHOP} is the chopping period, which is determined by the internal timer. Refer to [P.8](#), [9](#) for details.

IC number	Upper Pch DMOS ON Resistance $R_{ONH}[\Omega]$ (Typ)	Lower Nch DMOS ON Resistance $R_{ONL}[\Omega]$ (Typ)
BD68888AEKV	0.70	0.30

$$W_{total} = W_{VBB} + W_{DMOS} \quad [W]$$

$$T_j = T_a + \theta_{ja} \times W_{total} \quad [^\circ C]$$

where:

W_{total} is the consumed total power of IC.
 T_j is the junction temperature.
 T_a is the air temperature.
 θ_{ja} is the thermal resistance value.

However, the thermal resistance value θ_{ja} [°C/W] differs greatly depending on circuit board conditions. The calculated values above are only theoretical. For actual thermal design, perform sufficient thermal evaluation for the application board used, and create the thermal design with enough margin not to exceed $T_{jmax}=150$ °C. Although unnecessary with normal use, if the IC is to be used under especially strict heat conditions, consider externally attaching a Schottky diode between the motor output pin and GND to abate heat from the IC.

Power Dissipation – continued

Temperature Monitoring

In respect of BD68888AEKV, there is a way to directly measure the approximate chip temperature by using the LOGIC pin (10x^(Note 21) or 11x^(Note 22)) with a protection diode for prevention from electrostatic discharge. However, temperature monitor using this LOGIC pin is only for evaluation and experimenting, and must not be used in actual usage conditions.

(Note 21) x=1, 2, 3 or 4
 (Note 22) x=1, 2, 3 or 4

- (1) Measure the pin voltage when a current of $I_{DIODE} = 50 \mu A$ flows from the LOGIC pin to the GND, without supplying VBB1 and VBB2 to the IC. This measurement is of the V_F voltage inside the diode.
- (2) Measure the temperature characteristics of this pin voltage. (V_F has a linear negative temperature factor against the temperature.) With the results of these temperature characteristics, chip temperature may be calibrated from the LOGIC pin voltage.
- (3) Supply VBB1 and VBB2, confirm the LOGIC pin voltage while running the motor, and calculate approximately the chip temperature from the results of (2).

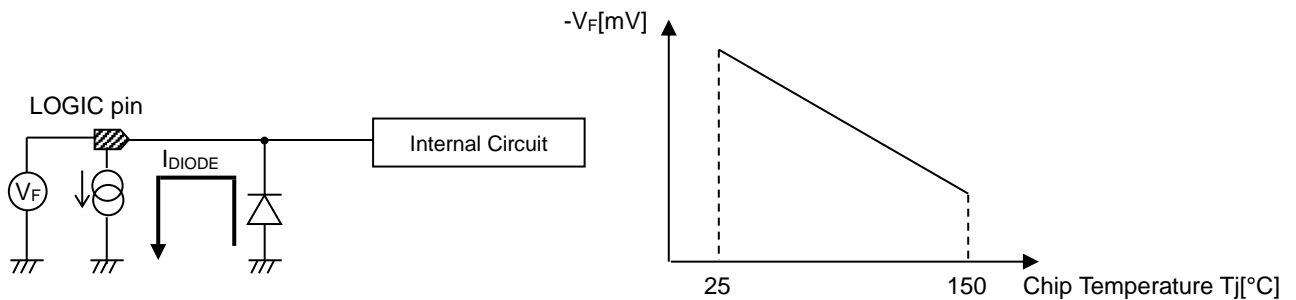


Figure 7. Model Diagram for Measuring Chip Temperature

Application Example

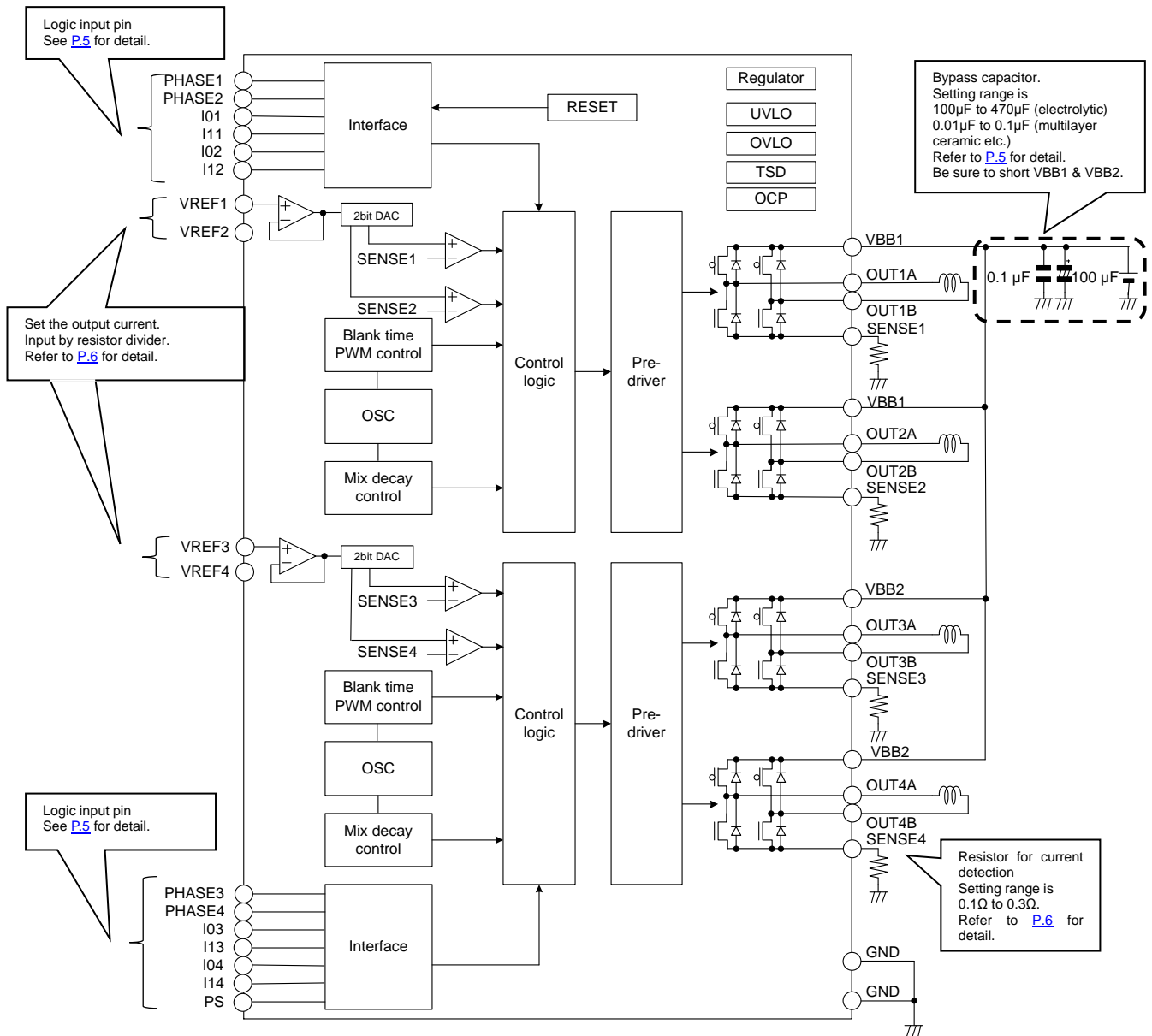
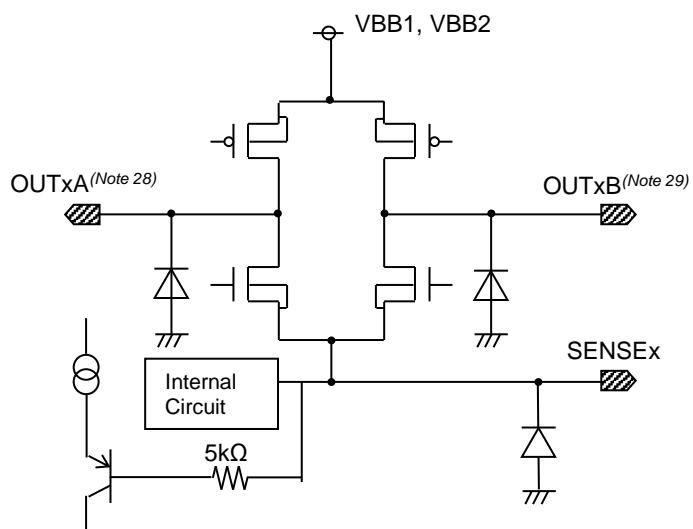
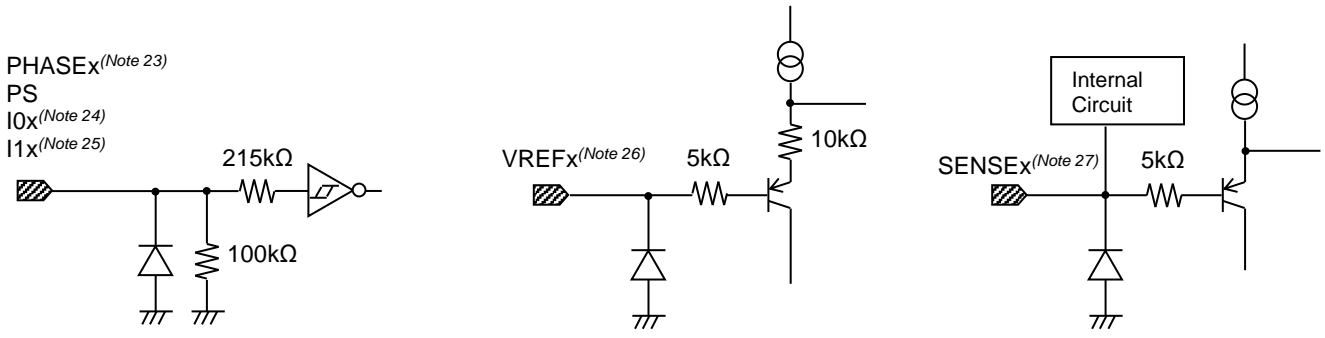


Figure 8. BD68888AEKV Block Diagram and Applied Circuit Diagram

I/O Equivalence Circuit



- (Note 23) x=1, 2, 3 or 4
- (Note 24) x=1, 2, 3 or 4
- (Note 25) x=1, 2, 3 or 4
- (Note 26) x=1, 2, 3 or 4
- (Note 27) x=1, 2, 3 or 4
- (Note 28) x=1, 2, 3 or 4
- (Note 29) x=1, 2, 3 or 4

Operational Notes

1. Reverse Connection of Power Supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

2. Power Supply Lines

Design the PCB layout pattern to provide low impedance supply lines. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. Ground Voltage

Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. Ground Wiring Pattern

When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. Recommended Operating Conditions

The function and operation of the IC are guaranteed within the range specified by the recommended operating conditions. The characteristic values are guaranteed only under the conditions of each item specified by the electrical characteristics.

6. Inrush Current

When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

7. Operation Under Strong Electromagnetic Field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

8. Testing on Application Boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

9. Inter-pin Short and Mounting Errors

Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

10. Unused Input Pins

Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

Operational Notes – continued

11. Regarding the Input Pin of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When $GND > Pin A$ and $GND > Pin B$, the P-N junction operates as a parasitic diode.
 When $GND > Pin B$, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

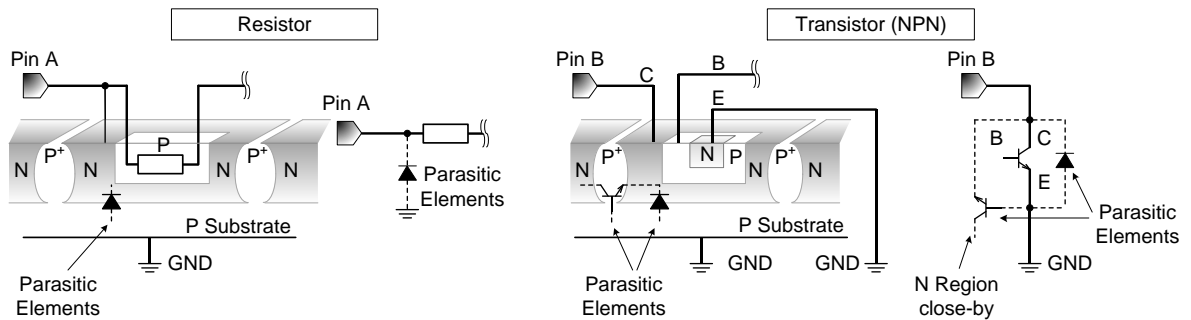


Figure 9. Example of monolithic IC structure

12. Ceramic Capacitor

When using a ceramic capacitor, determine a capacitance value considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

13. Thermal Shutdown Circuit (TSD)

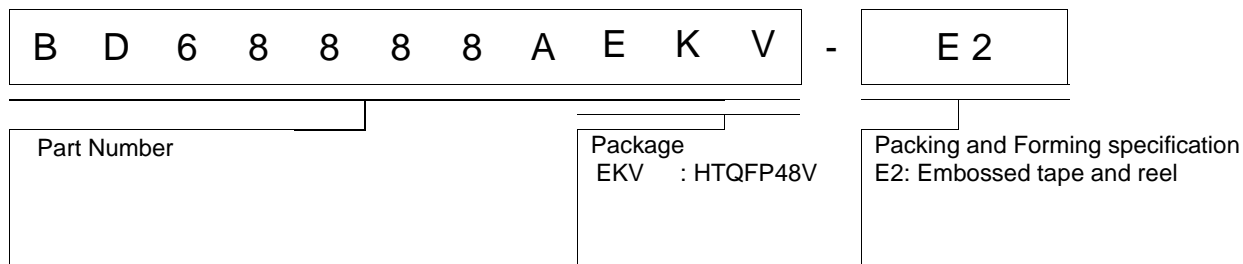
This IC has a built-in thermal shutdown circuit that prevents heat damage to the IC. Normal operation should always be within the IC's maximum junction temperature rating. If however the rating is exceeded for a continued period, the junction temperature (T_j) will rise which will activate the TSD circuit that will turn OFF power output pins. When the T_j falls below the TSD threshold, the circuits are automatically restored to normal operation.

Note that the TSD circuit operates in a situation that exceeds the absolute maximum ratings and therefore, under no circumstances, should the TSD circuit be used in a set design or for any purpose other than protecting the IC from heat damage.

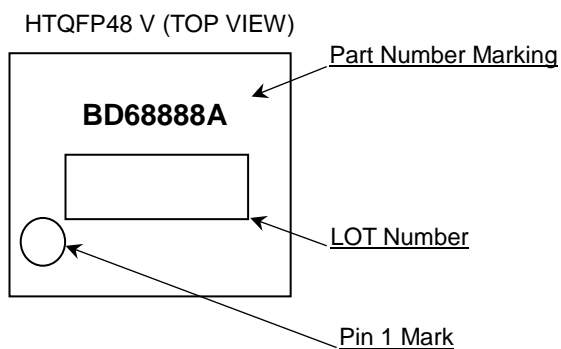
14. Over Current Protection Circuit (OCP)

This IC incorporates an integrated overcurrent protection circuit that is activated when the load is shorted. This protection circuit is effective in preventing damage due to sudden and unexpected incidents. However, the IC should not be used in applications characterized by continuous operation or transitioning of the protection circuit.

Ordering Information



Marking Diagram



Revision History

Date	Revision	Changes
17.May.2018	001	New Release

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JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

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 - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
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 - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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