



# 201027903 CP2110 Errata Version 0.2 Release

**Bulletin Issue Date:** 10/27/2020

**Effective Date:** 10/27/2020

## Description of Change

Silicon Labs is pleased to announce errata version 0.2 for the CP2110 devices.

## Reason for Change

This errata update adds errata item E102 describing the behavior of RS485 output pin at high baud rates and the associated workaround.

## Product Identification

Existing Part #  
CP2110-F01-GM  
CP2110-F01-GMR  
CP2110-F02-GM1  
CP2110-F02-GM1R  
CP2110P\*FGM  
CP2110P\*FGMR

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

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## Customer Actions Needed:

Review errata.

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