

## Extruded heatsinks for PCB mounting

for semiconductor clip-mounting

	<p>... STC</p> <p>... STIC</p> <p>... STCB</p>		
<b>art. no.</b>		↔ [mm]	$R_{th}$ [K/W]
<b>SK 129 25,4 ...</b>	TO 220	25.4	7.8
<b>SK 129 38,1 ...</b>	TO 220	38.1	6.5
<b>SK 129 50,8 ...</b>	TO 220	50.8	5.3
<b>SK 129 63,5 ...</b>	TO 220	63.5	4.5
<b>please indicate:</b>	<p>... mounting method</p> <p><b>STC</b> =with solder pins</p> <p><b>STIC</b> =with solder pins and insulating washer</p> <p><b>STCB</b>=with threaded bolts M 3, brass</p>		

**P** = raised retaining stud, **E** = mounting method

special lengths and drillings on request

**surface treatment:** black anodised

for semiconductor screw-mounting

	<p>... STS</p> <p>... STIS</p> <p>... STSB</p>		
<b>art. no.</b>		↔ [mm]	$R_{th}$ [K/W]
<b>SK 129 25,4 ...</b>	TO 220/ SOT 32/ TO 3 P	25.4	7.8
<b>SK 129 38,1 ...</b>	TO 220/ SOT 32/ TO 3 P	38.1	6.5
<b>SK 129 50,8 ...</b>	TO 220/ SOT 32/ TO 3 P	50.8	5.3
<b>SK 129 63,5 ...</b>	TO 220/ SOT 32/ TO 3 P	63.5	4.5
<b>please indicate:</b>	<p>... mounting method</p> <p><b>STS</b> =with solder pins</p> <p><b>STIS</b> =with solder pins and insulating washer</p> <p><b>STSB</b>=with threaded bolts M 3, brass</p>		

**P** = raised retaining stud, **E** = mounting method

special lengths and drillings on request

**surface treatment:** black anodised