Vishay General Semiconductor

Dual High-Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low $V_F = 0.57$ V at $I_F = 5.0$ A



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DESIGN SUPPORT TOOLS AVAILABLE



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 10 A				
V _{RRM}	150 V				
I _{FSM}	120 A				
V_F at I_F = 10 A (T_J = 125 °C)	0.65 V				
T _J max.	175 °C				
Package	SMPD (TO-263AC)				
Circuit configuration	Common cathode				

FEATURES

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available:
 Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

MECHANICAL DATA

Case: SMPD (TO-263AC) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 gualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test **Polarity:** as marked

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V20DM153C	UNIT	
Device marking code			V20DM153C		
Maximum repetitive peak reverse voltage		V _{RRM}	150	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)} ⁽¹⁾	20	٨	
	per diode		10	— A	
Peak forward surge current 8.3 ms single half superimposed on rated load	sine-wave	I _{FSM}	120	А	
Operating junction temperature range		T _J ⁽²⁾	-40 to +175	°C	
Storage temperature range		T _{STG}	-55 to +175	7	

Notes

⁽¹⁾ Mounted on infinite heatsink

⁽²⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{eJA}$

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1

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V20DM153C





FREE

V20DM153C



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ELECTRICAL CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T ₁ = 25 °C	V _F ⁽¹⁾	0.72	-	V	
	I _F = 10 A			0.85	0.98		
	I _F = 5 A	- T _J = 125 °C		0.57	-		
	I _F = 10 A			0.65	0.70		
Reverse current at rated V_R per diode	V _R = 100 V	T _J = 25 °C	I _R ⁽²⁾	0.0005	-	mA	
		T _J = 125 °C		0.8	-		
	150.1	T _J = 25 °C		-	0.1		
	V _R = 150 V	T _J = 125 °C		2	7		
Typical junction capacitance	4.0 V, 1 MHz	•	CJ	480	-	pF	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V20DM153C	UNIT		
Typical thermal resistance per device	R _{0JC} ⁽¹⁾	2.0	°C/W		
	R _{0JA} ⁽²⁾⁽³⁾	58	0/10		

Notes

⁽¹⁾ Mounted on infinite heatsink

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$ - junction-to-ambient

⁽³⁾ Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V20DM153C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V20DM153CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel		

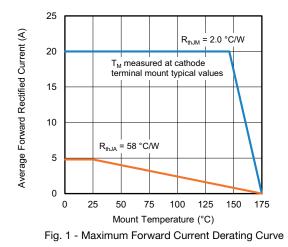
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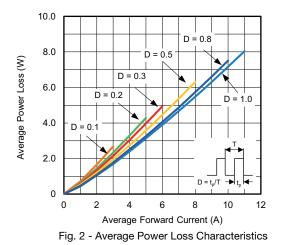
(1) AEC-Q101 qualified

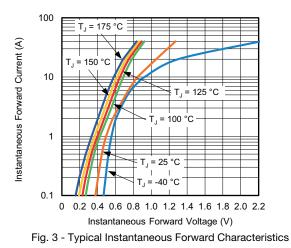


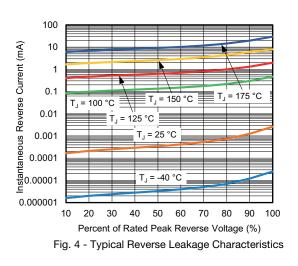
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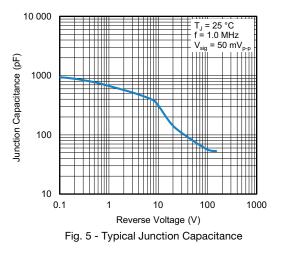
RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)











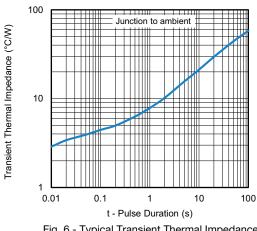


Fig. 6 - Typical Transient Thermal Impedance

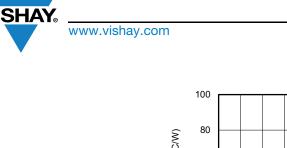
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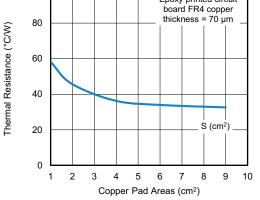
3

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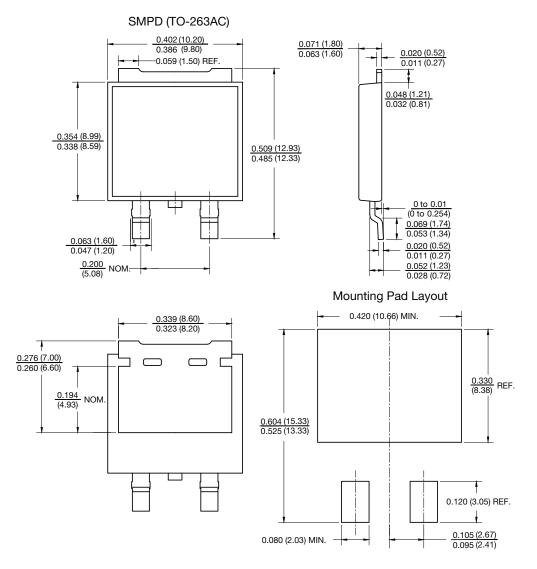




Epoxy printed circuit

Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



 Revision: 21-Oct-2020
 4
 Document Number: 87187

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