RGW60TS65CHR

650V 30A Hybrid IGBT with Built-In SiC-SBD

Datasheet

V _{CES}	650V
I _{C (100°C)}	30A
V _{CE(sat) (Typ.)}	1.5V
P_D	178W

Outline TO-247N (1) (2)(3)

Features

- 1) AEC-Q101 Qualified
- 2) Low Collector Emitter Saturation Voltage
- 3) Low Switching Loss & Soft Switching
- 4) Built in No Recovery Silicon Carbide SBD
- 5) Pb free Lead Plating; RoHS Compliant

Application

Automotive

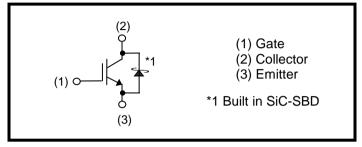
On & Off Board Chargers

DC-DC Converters

PFC

Industrial Inverter

●Inner Circuit



Packaging Specifications

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	Packaging	Tube			
	Reel Size (mm)	-			
Tuno	Tape Width (mm)	-			
Type Basic Ordering Ur Packing Code Marking	Basic Ordering Unit (pcs)	450			
	Packing Code	C11			
	Marking	RGW60TS65C			

● Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V_{GES}	±30	V
Collector Current	T _C = 25°C	I _C	64	А
Collector Current	T _C = 100°C	I _C	39	А
Pulsed Collector Current		I _{CP} *1	120	А
Diodo Forward Current	T _C = 25°C	I _F	39	А
Diode Forward Current	T _C = 100°C	I _F	25	А
Diode Pulsed Forward Current		I _{FP} *1	100	А
Down Discipation	T _C = 25°C	P _D	178	W
Power Dissipation	T _C = 100°C	P _D	89	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

^{*1} Pulse width limited by T_{jmax.}

●Thermal Resistance

Parameter	Symbol	Values			Unit
Falametei	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.84	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	ı	1.34	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

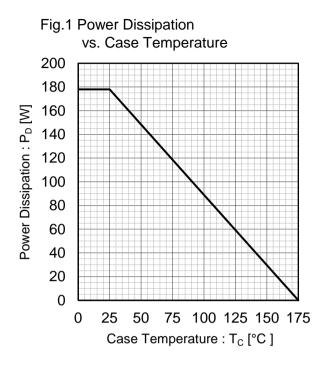
Parameter	Symbol	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_C = 5$ mA, $V_{GE} = 0$ V	650	ı	ı	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	ı	ı	5	mA
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, V_{CE} = 0V$	1	1	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 20.0 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_{C} = 30A, V_{GE} = 15V,$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.5 1.85	1.9 -	V

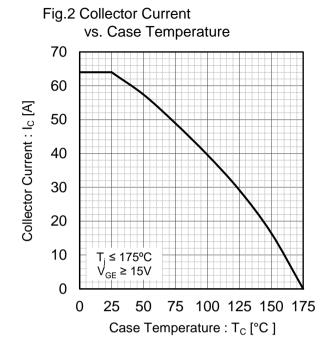
ullet IGBT Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Dorometer	Symbol	Conditions	Values			Unit
Parameter			Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	$V_{CE} = 30V$,	-	2530	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$,	-	65	-	pF
Reverse transfer Capacitance	C _{res}	f = 1MHz	-	46	-	
Total Gate Charge	Q_g	V _{CE} = 400V,	-	84	-	
Gate - Emitter Charge	Q_ge	I _C = 30A,	-	17	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	31	-	
Turn - on Delay Time	t _{d(on)}		-	37	-	
Rise Time	t _r	$I_C = 15A, V_{CC} = 400V,$ $V_{GE} = 15V, R_G = 10\Omega,$	-	8	-	ns
Turn - off Delay Time	t _{d(off)}	$T_i = 25^{\circ}C$	-	91	-	
Fall Time	t _f	Inductive Load *E _{on} include diode reverse recovery	-	70	-	
Turn - on Switching Loss	E _{on}		-	0.07	1	mJ
Turn - off Switching Loss	E _{off}		-	0.22	1	1113
Turn - on Delay Time	t _{d(on)}		-	34	ı	
Rise Time	t _r	$I_C = 15A, V_{CC} = 400V,$ $V_{GF} = 15V, R_G = 10\Omega,$	-	9	1	nc
Turn - off Delay Time	t _{d(off)}	T _j = 175°C Inductive Load *E _{on} include diode reverse recovery	-	134	1	ns
Fall Time	t _f		-	86	1	
Turn - on Switching Loss	E _{on}		-	0.07	1	mJ
Turn - off Switching Loss	E _{off}		-	0.33	1	1113
Reverse Bias Safe Operating Area	RBSOA	$I_C = 120A$, $V_{CC} = 520V$, $V_P = 650V$, $V_{GE} = 15V$, $R_G = 100\Omega$, $T_j = 175^{\circ}C$	FU	LL SQUA	RE	-

●SiC-SBD Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
		I _F = 20A,				
Diode Forward Voltage	V _F	$T_j = 25^{\circ}C$ $T_i = 175^{\circ}C$	-	1.35 1.63	1.55 -	V
Diode Reverse Recovery Time	t _{rr}		-	34	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	I _F = 15A, V _{CC} = 400V,	ı	2.5	-	А
Diode Reverse Recovery Charge	Q _{rr}	di _F /dt = 200A/µs, T _j = 25°C	ı	49	ı	nC
Diode Reverse Recovery Energy	E _{rr}		ı	1.2	-	μJ
Diode Reverse Recovery Time	t _{rr}	$I_F = 15A$, $V_{CC} = 400V$, $di_F/dt = 200A/\mu s$, $T_j = 175^{\circ}C$	-	37	-	ns
Diode Peak Reverse Recovery Current	I _{rr}		-	2.5	-	А
Diode Reverse Recovery Charge	Q _{rr}		-	55	-	nC
Diode Reverse Recovery Energy	E _{rr}		-	1.6	-	μJ
Total Capacitance	С	$V_R = 1V, f=1MHz$	-	730	-	pF
- Otal Sapaolanoo		$V_R = 600V, f=1MHz$	-	74	-	Pi





1000 1µs 100 Collector Current : I_C [A] 10µs 10 100µs 1 0.1 $T_{\rm C} = 25^{\circ}{\rm C}$ Single Pulse 0.01 10 100 1000 Collector To Emitter Voltage: V_{CE} [V]

Fig.3 Forward Bias Safe Operating Area

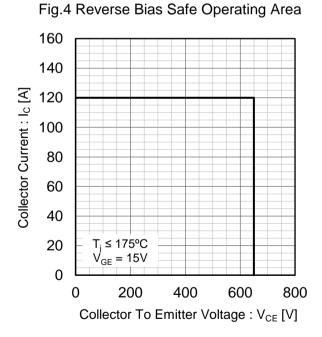


Fig.5 Typical Output Characteristics

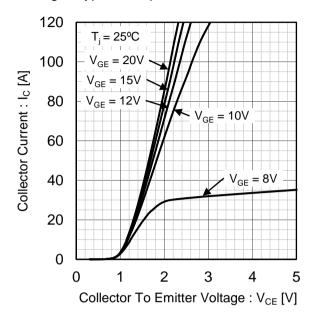


Fig.6 Typical Output Characteristics

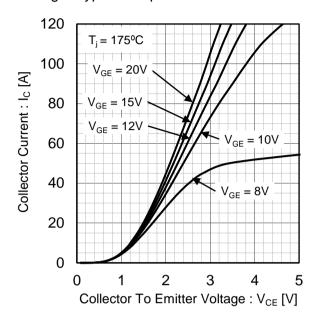


Fig.7 Typical Transfer Characteristics

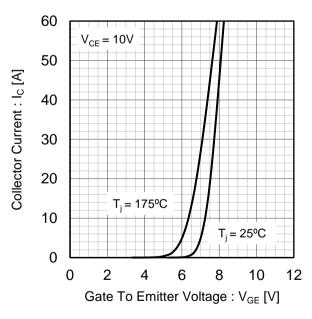
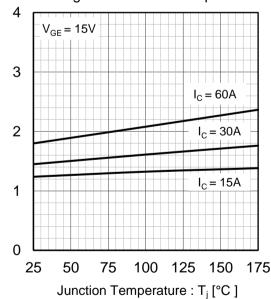


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



Collector To Emitter Saturation

Voltage: V_{CE(sat)} [V]

Fig.9 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage 20 $T_i = 25^{\circ}C$ Collector To Emitter Saturation $I_C = 60A$ 15 Voltage: V_{CE(sat)} [V] $I_{\rm C} = 30A$ $I_C = 15A$ 10 5 0 5 10 15 20

Gate To Emitter Voltage: VGE [V]

Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

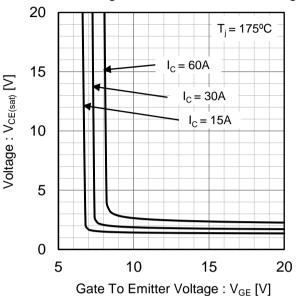
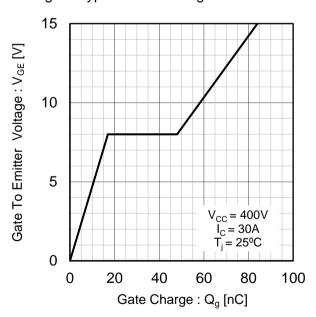


Fig.11 Typical Capacitance vs. Collector to Emitter Voltage 10000 C_{ies} 1000 Capacitance [pF] Coes 100 C_{res} 10 f = 1MHz $V_{GE} = 0V$ = 25°C 0.01 0.1 1 10 100 Collector To Emitter Voltage: V_{CE} [V]

Fig.12 Typical Gate Charge



Collector To Emitter Saturation

Fig.13 Typical Switching Time vs. Collector Current 1000 Switching Time [ns] $t_{d(off)}$ 100 t_f $t_{d(on)}$ 10 $V_{CC} = 400V, V_{GE} = 15V,$ $R_{G} = 10\Omega, T_{j} = 25^{\circ}C$ Inductive load 1 0 10 20 30 40 50 60 Collecter Current : I_C [A]

vs. Gate Resistance 1000 $t_{d(off)}$ Switching Time [ns] 100 t_f $t_{d(on)}$ 10 $V_{CC} = 400V, V_{GE} = 15V,$ $I_{C} = 15A, T_{j} = 25^{\circ}C$ Inductive load 1 0 10 20 30 50 Gate Resistance : $R_G[\Omega]$

Fig.14 Typical Switching Time

Fig.15 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1 E_{off} 0.1 V_{CC} = 400V, V_{GE} = 15V, R_G = 10 Ω , T_j = 25°C Inductive load 0.01 0 10 20 30 40 50 60 Collecter Current : I_C [A]

vs. Gate Resistance 10 E_{off} 0.1 E_{off} $V_{cc} = 400V, V_{GE} = 15V, I_{c} = 15A, T_{j} = 25^{\circ}C$ Inductive load 0 10 20 30 40 50 Gate Resistance : R_{G} [Ω]

Fig.16 Typical Switching Energy Losses

Fig.17 Typical Switching Time vs. Collector Current 1000 $t_{d(off)}$ Switching Time [ns] 100 $t_{d(on)}$ 10 $V_{CC} = 400V, V_{GE} = 15V,$ $R_G = 10\Omega, T_j = 175^{\circ}C$ Inductive load 1 0 10 20 30 40 50 60 Collecter Current : I_C [A]

Fig.18 Typical Switching Time vs. Gate Resistance 1000 t_{d(off)} Switching Time [ns] 100 t_{d(on)} 10 $V_{CC} = 400 \text{V}, V_{GE} = 15 \text{V}, \\ I_{C} = 15 \text{A}, T_{j} = 175 ^{\circ} \text{C} \\ \text{Inductive load}$ 1 0 10 20 30 50 Gate Resistance : $R_G[\Omega]$

vs. Collector Current 10 Switching Energy Losses [mJ] 1 $\mathsf{E}_{\mathsf{off}}$ 0.1 $V_{CC} = 400V, V_{GE} = 15V,$ $R_G = 10\Omega, T_j = 175^{\circ}C$ E_{on} Inductive load 0.01 10 20 30 40 50 60

Collecter Current : I_C [A]

Fig.19 Typical Switching Energy Losses

vs. Gate Resistance

10

See Story

1

Eoff E_{on} $V_{cc} = 400V, V_{GE} = 15V, I_{C} = 15A, T_{J} = 175^{\circ}C$ Inductive load

0 10 20 30 40 50

Gate Resistance : R_{G} [Ω]

Fig.20 Typical Switching Energy Losses

Fig.21 Typical Diode Forward Current vs. Forward Voltage

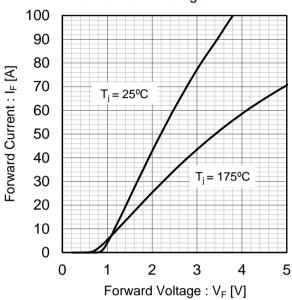


Fig.22 Typical Diode Revese Recovery Time vs. Forward Current

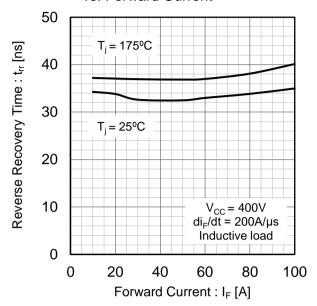


Fig.23 Typical Diode Reverse Recovery Current vs. Forward Current

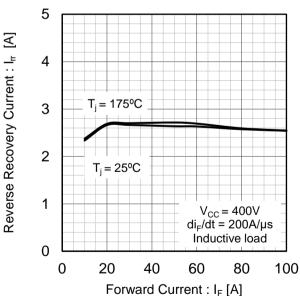
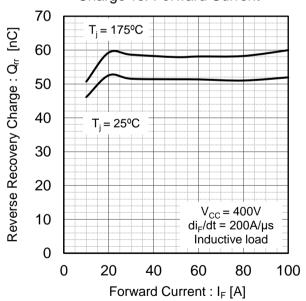
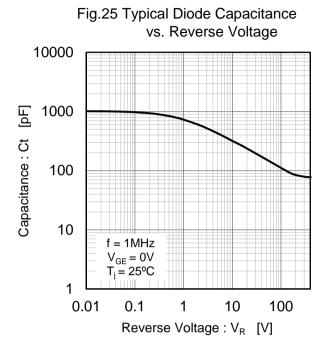
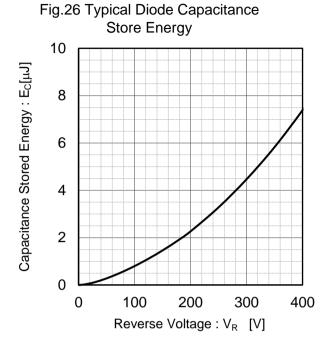


Fig.24 Typical Diode Rrverse Recovery Charge vs. Forward Current







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Fig.27 Typical IGBT Transient Thermal Impedance

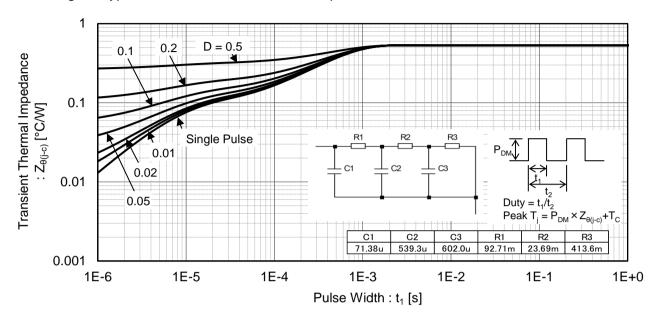
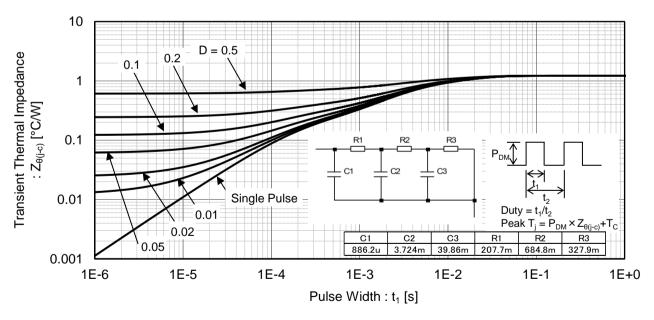


Fig.28 Typical Diode Transient Thermal Impedance



●Inductive Load Switching Circuit and Waveform

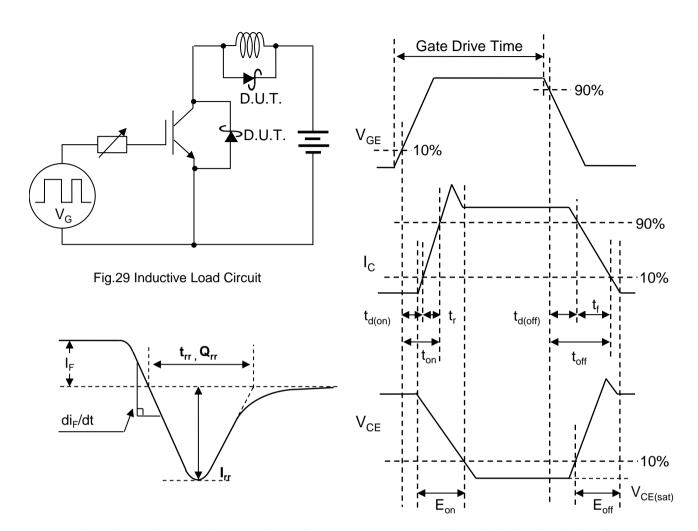


Fig.31 Diode Reverse Recovery Waveform

Fig.30 Inductive Load Waveform

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