



## Customer Information Notification

201906010I

**Issue Date:** 21-Jun-2019

**Effective Date:** 22-Jun-2019

Dear *Gordon Love*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)

**This notice is NXP Company Proprietary.**



### Management Summary

The LPC802x errata sheet has been updated with the VDD.1 errata

#### Change Category

- |  |   |  |   |   |
|--|---|--|---|---|
| <input type="checkbox"/> Wafer Fab Process   | <input type="checkbox"/> Assembly Process   | <input type="checkbox"/> Product Marking           | <input type="checkbox"/> Test Location  | <input type="checkbox"/> Design                         |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification  | <input type="checkbox"/> Test Process   | <input checked="" type="checkbox"/> Errata              |
| <input type="checkbox"/> Wafer Fab Location  | <input type="checkbox"/> Assembly Location  | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware            | <input type="checkbox"/> Other              |  |   |   |

LPC802x Errata Sheet Update with VDD.1 Errata

### Description

The LPC802x errata sheet has been updated to include the VDD.1 errata:

VDD.1: The rise time of the power supply ramp must be 1 ms or below.

The updated errata sheet, ES\_LPC802\_v1.3, is available on the NXP website:

[https://www.nxp.com/docs/en/errata/ES\\_LPC802.pdf](https://www.nxp.com/docs/en/errata/ES_LPC802.pdf)

#### Reason

For customer information

#### Identification of Affected Products

Product identification does not change

### Anticipated Impact on Form, Fit, Function, Reliability or Quality

The VDD rise time requirement is affected as described in the errata.

#### Data Sheet Revision

No impact to existing datasheet

#### Additional information

Affected products and sales history information: see attached file

Additional documents: [view online](#)



#### Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

**Name** Tim Camenzind  
**Position** Senior Quality Engineer  
**e-mail address** [tim.camenzind@nxp.com](mailto:tim.camenzind@nxp.com)

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

NXP Quality Management Team.

#### About NXP Semiconductors

NXP Semiconductors N.V. (NASDAQ: NXPI) provides High Performance Mixed Signal and Standard Product solutions that leverage its leading RF, Analog, Power Management, Interface, Security and Digital Processing expertise. These innovations are used in a wide range of automotive, identification, wireless infrastructure, lighting, industrial, mobile, consumer and computing applications.

You have received this email because you are a designated contact or subscribed to NXP Quality Notifications. NXP shall not be held liable if this Notification is not correctly distributed within your organization.

This message has been automatically distributed. Please do not reply.

[View Notification](#)

[Subscription](#)

[Support](#)

[NXP](#) | [Privacy Policy](#) | [Terms of Use](#)

NXP Semiconductors  
High Tech Campus, 5656 AG Eindhoven, The Netherlands

© 2006-2010 NXP Semiconductors. All rights reserved.