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DS52100A

dsPIC33EP256GP506 64-Pin TQFP to 100-Pin PIM

Overview

The dsPIC33EP256GP506 64-Pin TQFP to 100-Pin PIM (MA330030) comes with a 64-pin dsPIC33EP256GP506 TQFP device and enables users to explore the innovative features of the 70 MIPS dsPIC33E using the Explorer 16 Development Board.

This Plug-in Module (PIM) can be used to evaluate the dsPIC33EPXXXGP50X and PIC24EPXXXGP20X family of devices.

Table 1 shows the mapping between the 100-pin PIM socket and the 64-pin dsPIC® Digital Signal Controller (DSC). Not all PIM pins are connected. Every dsPIC DSC pin is connected to the PIM.

Figure 1 shows the dsPIC33EP256GP506 64-Pin TQFP to 100-Pin PIM schematic.

Table 1: dsPIC33EP256GP506 64-Pin TQFP to 100-Pin PIM

PIM Pin #	Device Pin #	dsPIC33EP256GP506 Functional Description
1	—	Not Connected
2	**Note 1**	VDD
3	63	RPI45/CTPLS/RB13
4	2	RPI46/T3CK/RB14
5	3	RPI47/T5CK/RB15
6	—	Not Connected
7	—	Not Connected
8	—	Not Connected
9	—	Not Connected
10	4	RP118/RG6
11	5	RPI119/RG7
12	6	RP120/RG8
13	7	$\overline{\text{MCLR}}$
14	—	Not Connected
15	**Note 2**	VSS
16	**Note 1**	VDD
17	21	AN6/OA3OUT/C4IN1+/OCFB/RC0
18	46	RP39/INT0/RB7
19	—	Not Connected
20	13	AN0/OA2OUT/RA0
21	14	AN1/C2IN1+/RA1
22	12	AN9/RPI27/RA11
23	59	RP97/RF1
24	15	PGED3/VREF-/AN2/C2IN1-/$\overline{\text{SS1}}$/RPI32/CTED2/RB0
25	11	AN10/RPI28/RA12
26	17	PGEC1/AN4/C1IN1+/RPI34/RB2
27	18	PGED1/AN5/C1IN1-/RP35/RB3
28	—	Not Connected
29	—	Not Connected
30	**Note 1**	VDD
31	**Note 2**	VSS
32	16	PGEC3/VREF+/AN3/OA1OUT/RPI33/CTED1/RB1
33	—	Not Connected
34	—	Not Connected
35	—	Not Connected
36	**Note 2**	VSS
37	**Note 1**	VDD
38	22	AN7/C3IN1-/C4IN1-/RC1
39	23	AN8/C3IN1+/$\overline{\text{U1RTS}}$/BCLK/RC2
40	24	AN11/C1IN2-/$\overline{\text{U1CTS}}$/RC11
41	30	AN15/RPI95/RE15
42	—	Not Connected
43	1	TDI/RA7
44	64	TDO/RA10
45	**Note 2**	VSS
46	**Note 1**	VDD
47	42	RD8
48	45	RPI58/RC10
49	44	PGEC2/ASCL2/RP38/RB6
50	43	PGED2/ASDA2/RP37/RB5

Note 1: Device VDD pins are 10, 19, 28, 38 and 57.
2: Device VSS pins are 9, 20, 25 and 41.

Table 1: dsPIC33EP256GP506 64-Pin TQFP to 100-Pin PIM (Continued)

PIM Pin #	Device Pin #	dsPIC33EP256GP506 Functional Description
51	—	Not Connected
52	—	Not Connected
53	33	CVREF2O/SDO1/RP20/T1CK/RA4
54	34	SDI1/RPI25/RA9
55	35	SCK1/RPI51/RC3
56	36	SDA1/RPI52/RC4
57	37	SCL1/RPI53/RC5
58	32	SCL2/RP36/RB4
59	31	SDA2/RPI24/RA8
60	27	AN12/C2IN2-/$\overline{\text{U2RTS}}$/BCLK2/RE12
61	28	AN13/C3IN2-/$\overline{\text{U2CTS}}$/RE13
62	**Note 1**	VDD
63	39	OSC1/CLKI/RC12
64	40	OSC2/CLKO/RC15
65	**Note 2**	VSS
66	—	Not Connected
67	—	Not Connected
68	—	Not Connected
69	—	Not Connected
70	—	Not Connected
71	—	Not Connected
72	—	Not Connected
73	—	Not Connected
74	—	Not Connected
75	**Note 2**	VSS
76	—	Not Connected
77	—	Not Connected
78	—	Not Connected
79	8	RPI121/RG9
80	53	RD5
81	50	RP54/RC6
82	47	RC13
83	54	RD6
84	51	RP55/RC7
85	—	Not Connected
86	—	Not Connected
87	58	RPI96/RF0
88	55	RP57/RC9
89	—	Not Connected
90	—	Not Connected
91	29	AN14/RPI94/RE14
92	52	RP56/RC8
93	48	TCK/CVREF1O/ASCL1/RP40/T4CK/RB8
94	49	TMS/ASDA1/RP41/RB9
95	—	Not Connected
96	—	Not Connected
97	—	Not Connected
98	60	RP42/RB10
99	61	RP43/RB11
100	62	RPI44/RB12

Note 1: Device VDD pins are 10, 19, 28, 38 and 57.
2: Device VSS pins are 9, 20, 25 and 41.

Figure 1: Schematics