

## Product Advisor (PA)

**Subject:** Datasheet Correction for the Listed ISL95338 Product Family

**Publication Date:** 10/29/2018

**Effective Date:** 10/29/2018

**Revision Description:**

Initial Release

**Description of Change:**

This notice is to inform you that the datasheet associated with the ISL95338\* family of products has been updated to correct errors in the absolute maximum rating and the package outline sections of the document. Please refer to appendix A for a complete comparison of the changes.

The updated data sheet is available on the Renesas web site at: [ISL95338 Datasheet Link](#)

**Affected Products:**

ISL95338HRTZ	ISL95338HRTZ-TK	ISL95338IRTZ-T
ISL95338HRTZ-T	ISL95338HRTZ-TS2778	ISL95338IRTZ-TK
ISL95338HRTZ-T7A	ISL95338IRTZ	

**Reason for Change:**

Update of the datasheet is required to correct information that was in error in the initial release of the datasheet. There have been not changes to the product, these are corrections only.

**Impact on fit, form, function, quality & reliability:**

The change will have no other impact on the form, fit, function, quality, reliability and environmental compliance of the devices.

**Product Identification:**

Product affected by this change is identifiable via the internal traceability system.

**Qualification status:** Not applicable, correction only

**Sample availability:** 10/29/2018

**Device material declaration:** Available upon request

*Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.*

For additional information regarding this notice, please contact your regional change coordinator (below)			
Americas: <a href="mailto:PCN-US@Renesas.com">PCN-US@Renesas.com</a>	Europe: <a href="mailto:PCN-EU@Renesas.com">PCN-EU@Renesas.com</a>	Japan: <a href="mailto:PCN-JP@Renesas.com">PCN-JP@Renesas.com</a>	Asia Pac: <a href="mailto:PCN-APAC@Renesas.com">PCN-APAC@Renesas.com</a>

## Appendix A

### 2.1 Absolute Maximum Ratings Table

From:

Parameter	Minimum	Maximum	Unit
CSIP, CSIN, DCIN, ADPS, ADP	-0.3	+30	V
CSIP	0.3	ADP + 2	V
PHASE1	GND - 0.3	+30	V
PHASE1	GND - 2 (<20ns)	+30	V
UGATE1	PHASE1 - 0.3	BOOT1 + 0.3	V
PHASE2	GND - 0.3	+30	V
PHASE2	GND - 2 (<20ns)	+30	V
UGATE2	PHASE2 - 0.3	BOOT2 + 0.3	V
LGATE1, LGATE2	GND - 0.3	VDDP + 0.3	V
LGATE1, LGATE2	GND - 2 (<20ns)	VDDP + 0.3	V
VOUT, VOUTS, CSOP, CSON	-0.3	+27	V
VDD, VDDP	-0.3V	+6.5	V
BOOT1, BOOT2	- 0.3	VDDP + 27	V
BOOT1	(PHASE1 - 0.3)	PHASE1 + 6.5	V
BOOT2	(PHASE2 - 0.3)	PHASE2 + 6.5	V
COMPR, COMPF, REF, PROG	-0.3	+6.5	V
RVSEN, FRWEN, ADDR0, ADDR1	-0.3	+6.5	V
FRWPG, PROCHOT#, RVSPG	-0.3	+6.5	V
SCL, SDA	-0.3	+6.5	V
BOOT1-PHASE1, BOOT2-PHASE2	-0.3	+0.3	V
CSIP-CSIN, CSOP-CSON		2	mA

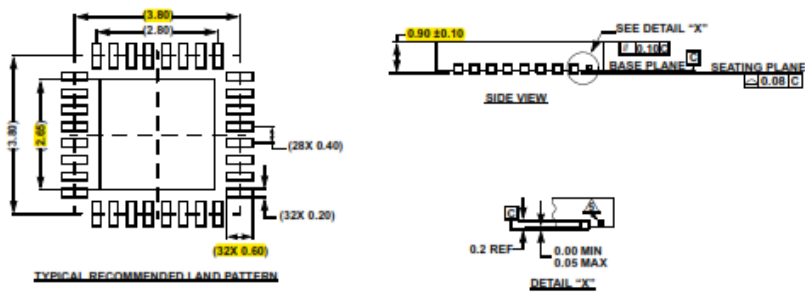
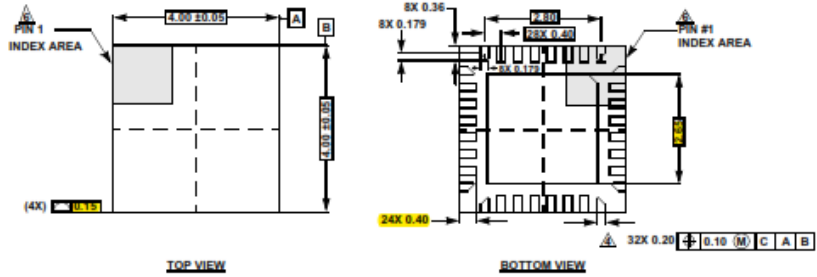
To:

Parameter	Minimum	Maximum	Unit
CSIP, CSIN, DCIN, ADPS, ADP	-0.3	+30	V
CSIP	0.3	ADP + 2	V
PHASE1	GND - 0.3	+30	V
PHASE1	GND - 2 (<20ns)	+30	V
UGATE1	PHASE1 - 0.3	BOOT1 + 0.3	V
PHASE2	GND - 0.3	+30	V
PHASE2	GND - 2 (<20ns)	+30	V
UGATE2	PHASE2 - 0.3	BOOT2 + 0.3	V
LGATE1, LGATE2	GND - 0.3	VDDP + 0.3	V
LGATE1, LGATE2	GND - 2 (<20ns)	VDDP + 0.3	V
VOUT, VOUTS, CSOP, CSON	-0.3	+24	V
VDD, VDDP	-0.3V	+6.5	V
BOOT1, BOOT2	- 0.3	VDDP + 25	V
BOOT1	(PHASE1 - 0.3)	PHASE1 + 6.5	V
BOOT2	(PHASE2 - 0.3)	PHASE2 + 6.5	V
BOOT1-PHASE1, BOOT2-PHASE2	-0.3	+6.5	V
COMPR, COMPF, REF, PROG	-0.3	+6.5	V
RVSEN, FRWEN, ADDR0, ADDR1	-0.3	+6.5	V
FRWPG, PROCHOT#, RVSPG	-0.3	+6.5	V
SCL, SDA	-0.3	+6.5	V
CSIP-CSIN, CSOP-CSON	-0.3	+0.3	V
RVSEN, FRWEN, SDA, SCL, FRWPG, RVSPG, PROCHOT#		2	mA

## 9. Package Outline Drawing

From:

L32.4x4A  
32 LEAD QUAD FLAT NO-LEAD PLASTIC PACKAGE  
Rev 5, 2/16



To:

L32.4x4D  
32 LEAD THIN QUAD FLAT NO-LEAD PLASTIC PACKAGE  
Rev 2, 10/16

