



dsPIC33EPXXXMC504 and PIC24EPXXXMC204

dsPIC33EPXXXMC504 and PIC24EPXXXMC204 Plug-In Module (PIM) Information Sheet for Motor Control (MC) Devices Using External Op amps

The dsPIC33EPXXXMC504 and PIC24EPXXXMC204 PIM is designed to demonstrate the capabilities of the dsPIC33EPXXXMC504 and PIC24EPXXXMC204 Motor Control devices using external Op amps with development boards such as the dsPICDEM™ MCLV, MCHV, and MCSM (DM330021, DM330023, and DM330022), which support 100-pin PIM interfaces.

The dsPIC33EPXXXMC504 is a high-performance, 16-bit Digital Signal Controller (DSC) within a small 44-pin QFN package. The PIC24EPXXXMC204 microcontroller (MCU) is similar in that it shares many of the same peripheral features; however, it is not equipped with the Digital Signal Processing (DSP) engine or other DSP features.

The dsPIC33EPXXXMC504 and PIC24EPXXXMC204 devices are equipped with Peripheral Pin Select (PPS), which allows many of the digital peripherals to be remapped to use any number of pins on the device.

The PIM takes advantage of PPS by using zero ohm resistors to connect the I/O to one of the 100-pin development boards previously listed. Simply removing the zero ohm resistors and using the test points associated with each I/O pin (i.e., pin 1, pin 2, etc.), allows a designer to re-route the pin to any of the available pins on the development boards. This capability enables easier and faster design times.

Table 1 shows the mapping between the development board functions and the device pins.

TABLE 1: 44-PIN TO 100-PIN PINOUT

Device Pin Number	dsPIC33EPXXXMC504 and PIC24EPXXXMC204	PIM Pin Number	Functional Description
1	TMS/ASDA1/RP41/RB9	19	Fault Reset
2	RP54/RC6	77	Data 2
3	RP55/RC7	78	Data 3
4	RP56/RC8	68, 20	Hall Sensor A/QEB_POT
5	RP57/RC9	69, 21	Hall Sensor B/QEA
6	Vss	65	N/A
7	VCAP	85	VCAP
8	RP42/PWM3H/RB10	3	PWMH3
9	RP43/PWM3L/RB11	100	PWML3
10	RPI44/PWM2H/RB12	99	PWMH2
11	RPI45/PWM2L/CTPLS/RB13	98	PWML2
12	TDO/RA10	—	N/A
13	TDI/RA7	71	PWM Enable
14	RPI46/PWM1H/T3CK/RB14	94	PWMH1
15	RPI47/PWM1L/T5CK/RB15	93	PWML1
16	AVss	31	N/A
17	AVdd	30	N/A
18	MCLR	13	Master Clear
19	AN0/OA2OUT/RA0	25	Phase 1 Current Feedback
20	AN1/C2IN1+/RA1	24	Phase 2 Current Feedback
21	PGED3/VREF-/AN2/C2IN1-/SS1/RP132/CTED2/RB0	35	DC Bus
22	PGEC3/VREF+/AN3/OA1OUT/RP133/CTED1/RB1	41	Phase 1 Voltage Feedback

Device Pin Number	dsPIC33EPXXXMC504 and PIC24EPXXXMC204	PIM Pin Number	Functional Description
23	PGECL/AN4/C1IN1+/RPI34/RB2	42	Phase 2 Voltage Feedback
24	PGED1/AN5/C1IN1-/RP35/RB3	43	Phase 3 Voltage Feedback
25	AN6/OA3OUT/C4IN1+/OCFB/RC0	—	N/A
26	AN7/C3IN1-/C4IN1-/RC1	20	QEB_POT
27	AN8/C3IN1+/U1RTS/BCLK1/FLT3/RC2	32	Bus Shunt
28	VDD	16	N/A
29	Vss	15	N/A
30	OSC1/CLK1/RA2	63	OSC1
31	OSC2/CLKO/RA3	64	OSC2
32	SDA2/RPI24/RA8	83	Switch 1
33	FLT32/SCL2/RP36/RB4	84	Switch 2
34	CVREF20/SDO1/RP20/T1CK/RA4	81	Enable
35	SDI1/RPI25/RA9	82	RD/WR
36	SCK1/RPI51/RC3	44	RS
37	SDA1/RPI52/RC4	72	Data 0
38	SCL1/RPI53/RC5	76	Data 1
39	Vss	36	N/A
40	VDD	37	N/A
41	PGED2/ASDA2/RP37/RB5	27	Debug
42	PGECL/ASCL2/RP38/RB6	26	Debug
43	RP39/INT0/RB7	22, 70	Index/Hall Sensor C
44	TCK/CVREF10/ASCL1/RP40/T4CK/RB8	18	Fault A

FIGURE 1: 100-PIN HEADER SCHEMATIC

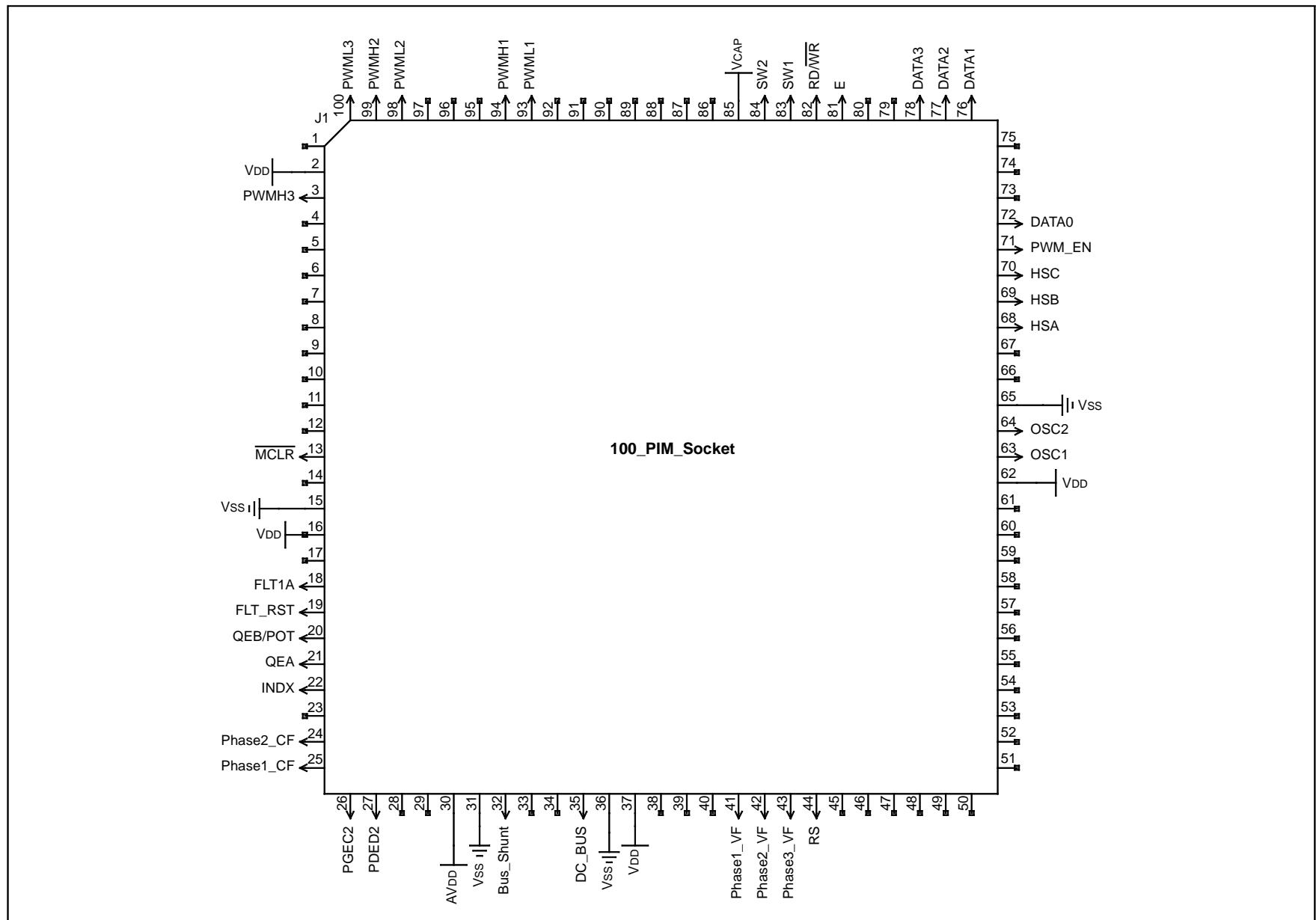
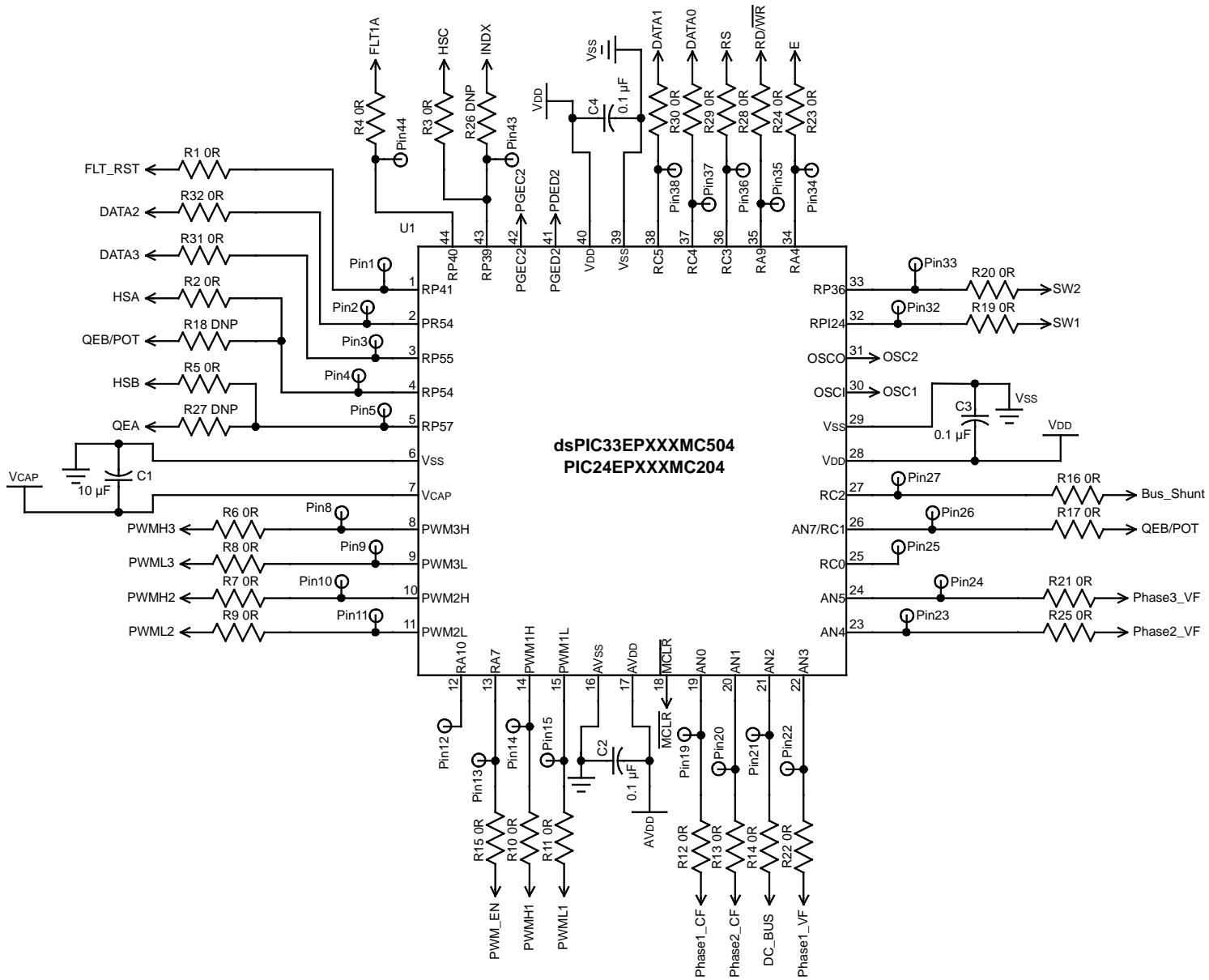


FIGURE 2: 44-PIN DEVICE SCHEMATIC



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NOTES:

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