



Customer Information Notification

202102029I : i.MX RT1170 Errata Update to Rev 1.1

Note: This notice is NXP Company Proprietary.

Issue Date: Mar 28, 2021 **Effective date:** Mar 29, 2021

Dear Emma Tempest,

Here is your personalized notification about a NXP general announcement.

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Change Category

☐ Wafer Fab Process	☐ Assembly Process	☐ Product Marking	☐ Test Process	☐ Design
☐ Wafer Fab Materials	☐ Assembly Materials	☐ Mechanical Specification	☐ Test Equipment	☒ Errata
☐ Wafer Fab Location	☐ Assembly Location	☐ Packing/Shipping/Labeling	☐ Test Location	☐ Electrical spec./Test coverage
☐ Firmware ☐ Other				

PCN Overview

Description

NXP Semiconductors announces an errata update for i.MX RT1170 to revision 1.1. The revision history included in the updated document provides a detailed description of the changes.

Changes are summarized below:

- Added the following errata:
 - ERR003777: GPT: Possibility of additional pulse on src_clk when switching between clock sources
 - ERR050708: Debug: CoreSight components are not linked to CoreSight ROM table
 - ERR006941: Core: Asynchronous sampling of SWDIOTMS might cause incorrect operation of SerialWire/JTAG Debug Port
- Removed the following errata:
 - ERR011575: Core: Single stepping Cortex-M7 enters pending exception handler
- Revised the following errata:
 - ERR050607: LPSPi: TCR[FRAMSZ] can be ignored when TCR[TXMSK]=1b1

The i.MX RT1170 errata is attached to this notice, and can be found at:

<https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/i-mx-rt->

[crossover-mcus/i-mx-rt1170-crossover-mcu-family-first-ghz-mcu-with-arm-cortex-m7-and-cortex-m4-cores:i.MX-RT1170?tab=Documentation_Tab&linkline=Errata](https://www.nxp.com/crossover-mcus/i-mx-rt1170-crossover-mcu-family-first-ghz-mcu-with-arm-cortex-m7-and-cortex-m4-cores:i.MX-RT1170?tab=Documentation_Tab&linkline=Errata)

Reason

The errata was added or removed for additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: [view online](#)

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name technical support

e-mail address tech.support@nxp.com

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NXP Quality Management Team.

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