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Vishay Siliconix

Automotive P-Channel 40 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	-40			
$R_{DS(on)} (\Omega)$ at $V_{GS} =$ - 10 V	0.014			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.023			
I _D (A)	-17.3			
Configuration	Single			

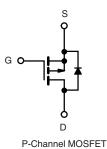
FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN FREE



ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	SQ4401CEY (for detailed order number please see www.vishay.com/doc?79771)

PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	-40	V	
Gate-source voltage		V_{GS}	± 20		
Continuous drain current	T _C = 25 °C		-17.3		
	T _C = 125 °C	- I _D	-10		
Continuous source current (diode conduction)		I _S	-6.5	А	
Pulsed drain current ^a		I _{DM}	-69		
Single pulse avalanche current	. 0.411	I _{AS}	-30		
Single pulse avalanche energy	L = 0.1 mH	E _{AS}	45	mJ	
	T _C = 25 °C	D	7.14	W	
Maximum power dissipation	T _C = 125 °C	P_{D}	2.4	VV	
Operating junction and storage temperature range		T _J , T _{sta}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount ^b	R _{thJA}	85	°C/W	
Junction-to-foot (drain)		R _{thJF}	21	C/VV	

Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. When mounted on 1" square PCB (FR-4 material)



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static						ı	<u> </u>
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0$, $I_D = -250 \mu A$		-40	-	-	V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA		-1.5	-2.0	-2.5	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	±100	nA
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = -40 V	-	-	-1.0	μΑ
		V _{GS} = 0 V	V _{DS} = -40 V, T _J = 125 °C	-	-	-50	
		V _{GS} = 0 V	V _{DS} = -40 V, T _J = 175 °C	-	-	-150	
On-state drain current ^a	I _{D(on)}	V _{GS} = -10 V	V _{DS} ≥ -5 V	-30	-	-	Α
Drain-source on-state resistance ^a		V _{GS} = -10 V	I _D = -10.5 A	-	0.011	0.014	Ω
		V _{GS} = -10 V	I _D = -10.5 A, T _J = 125 °C	-	-	0.021	
	R _{DS(on)}	V _{GS} = -10 V	I _D = -10.5 A, T _J = 175 °C	-	-	0.025	
		V _{GS} = -4.5 V	I _D = -8.7 A	-	0.018	0.023	
Forward transconductance a	9 _{fs}		-15 V, I _D = -10.5 A	_	30	-	S
	315	Dynamic b					
Input capacitance	C _{iss}	V _{GS} = 0 V	V _{DS} = -20 V, f = 1 MHz	-	3471	4250	pF
Output capacitance	C _{oss}			-	526	658	
Reverse transfer capacitance	C _{rss}	1		-	349	436	
Total gate charge ^c	Qg			-	71	115	
Gate-source charge ^c	Q _{gs}	$V_{GS} = -10 \text{ V}$	$V_{DS} = -20 \text{ V}, I_{D} = -10.5 \text{ A}$	-	11.4	-	nC
Gate-drain charge ^c	Q _{gd}			-	18.3	-	
Gate resistance	R _g	f = 1 MHz		0.95	1.90	3.00	Ω
Turn-on delay time ^c	t _{d(on)}	V_{DD} = -15 V, R_L = 15 Ω $I_D \cong$ -1 A, V_{GEN} = -4.5 V, R_g = 6 Ω		-	35	55	ns
Rise time ^c	t _r			_	52	80	
Turn-off delay time c	t _{d(off)}			-	62	95	
Fall time ^c	t _f			_	50	80	
Source-Drain Diode Ratings and Characteri							
Pulsed current a	I _{SM}			_	l -	-69	Α
Forward voltage	V _{SD}	I _F = -2.7 A, V _{GS} = 0 V		-	-0.75	-1.1	V
Body diode reverse recovery time	t _{rr}		<u></u>	-	30	60	ns
Body diode reverse recovery charge	Q _{rr}	I _F = -2.1 A, di/dt = 100 A/μs		-	36	72	nC
Reverse recovery fall time	t _a			-	19	-	ns
Reverse recovery rise time	t _b			-	11	-	
Body diode peak reverse recovery current	I _{RM(REC)}			-	-2.5	-	Α

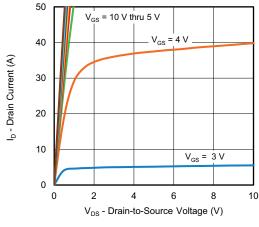
Notes

- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

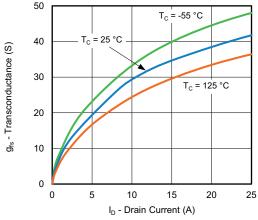
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



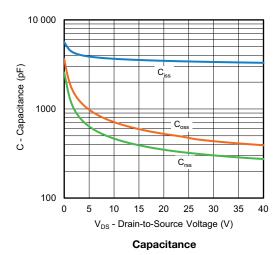
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

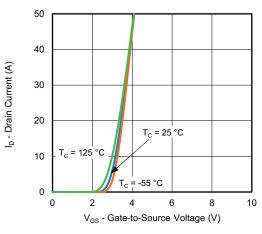


Output Characteristics

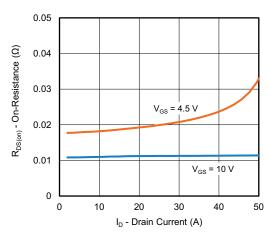


Transconductance

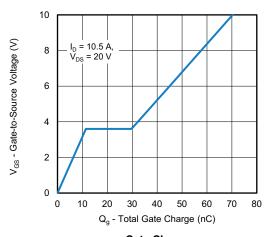




Transfer Characteristics

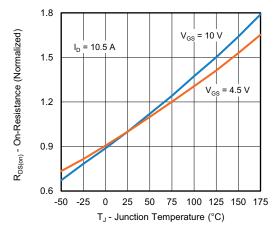


On-Resistance vs. Drain Current

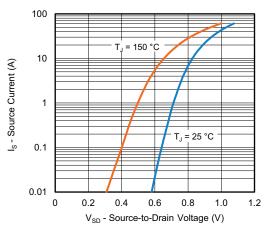




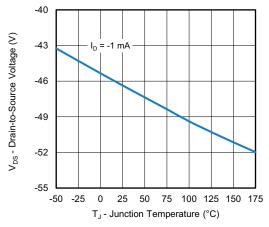
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



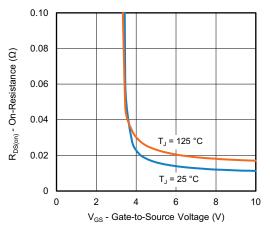
On-Resistance vs. Junction Temperature



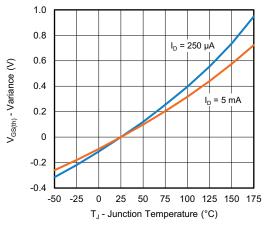
Source Drain Diode Forward Voltage



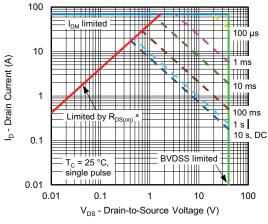
Breakdown Voltage vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Safe Operating Area

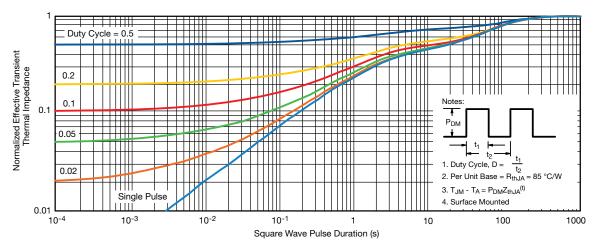
Note

a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

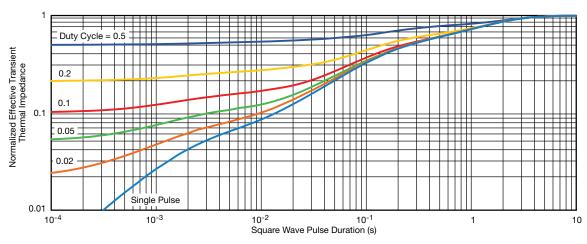
For technical questions, contact: automostechsu



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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