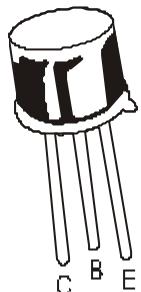


## NPN SILICON PLANAR TRANSISTOR

2N3553

TO-39

Metal Can Package



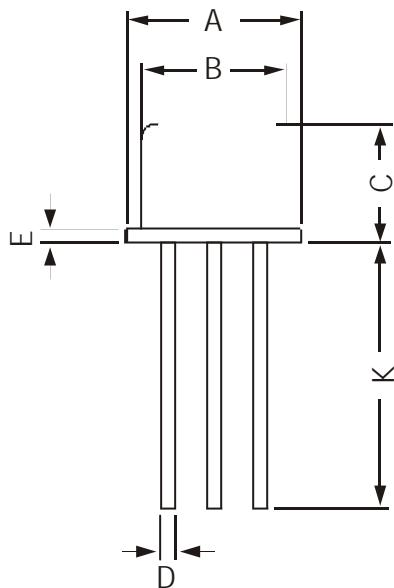
### ABSOLUTE MAXIMUM RATINGS (Ta = 25° C unless noted otherwise)

DESCRIPTION	SYMBOL	VALUE	UNIT
Collector Base Voltage	V <sub>CBO</sub>	65	V
Collector Emitter Voltage	V <sub>CEO</sub>	40	
Emitter Base Voltage	V <sub>EBO</sub>	4	
Collector Current Continuous	I <sub>C</sub>	1	A
Total Power Dissipation @ T <sub>C</sub> =25° C	P <sub>D</sub>	7	
Derate above 25°C		40	mW/°C
Operating and Storage Junction Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-65 to +200	°C

### ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25° C unless specified otherwise)

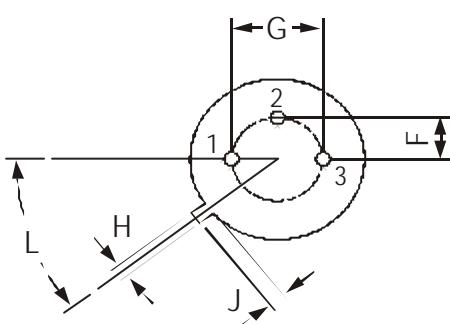
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP.	MAX.	UNIT
Collector Emitter Sustaining Voltage	V <sub>CEO(SUS)</sub>	I <sub>C</sub> =200mA, I <sub>B</sub> =0	40			V
Emitter Base Breakdown Voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> =0.1mA, I <sub>C</sub> =0	4			V
Collector Cut off Current	I <sub>CEO</sub>	V <sub>CE</sub> = 30V, I <sub>B</sub> =0			0.1	mA
Collector Cut Off Current	I <sub>CEX</sub>	V <sub>CE</sub> =65V, V <sub>BE(OFF)</sub> =1.5V			1	
Emitter Cut Off Current	I <sub>EBO</sub>	V <sub>BE</sub> =4V, I <sub>C</sub> =0			0.1	
DC Current Gain	h <sub>FE</sub>	V <sub>CE</sub> =1V, I <sub>C</sub> = 250mA	10			
Collector Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =250mA, I <sub>B</sub> =50mA			1.0	V
Collector Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> = 30V, I <sub>E</sub> = 0, f = 100KHz		8	10	pF

### TO-39 PACKAGE OUTLINE AND DIMENSION



DIM	MIN	MAX
A	8.50	9.39
B	7.74	8.50
C	6.09	6.60
D	0.40	0.53
E	—	0.88
F	2.41	2.66
G	4.82	5.33
H	0.71	0.86
J	0.73	1.02
K	12.70	—
L	42 DEG	48 DEG

All dimensions are in mm



#### PIN CONFIGURATION

1. Emitter
2. Base
3. Collector

### Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-39	500 pcs/polybag	540 gm/500 pcs	3" x 7.5" x 7.5"	20K	17" x 15" x 13.5"	32K	40 kgs



Continental Device India Limited

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## Notes

### Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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