



Product Change Notification / SYST-24GQSM275

Date:

28-Mar-2022

Product Category:

8-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

ERRATA - AVR64DB28/32/48/64 Silicon Errata and Data Sheet Clarifications Errata Document Revision

Affected CPNs:

[SYST-24GQSM275_Affected_CPN_03282022.pdf](#)

[SYST-24GQSM275_Affected_CPN_03282022.csv](#)

Notification Text:

SYST-24GQSM275

Microchip has released a new Product Documents for the AVR64DB28/32/48/64 Silicon Errata and Data Sheet Clarifications of devices. If you are using one of these devices please read the document located at [AVR64DB28/32/48/64 Silicon Errata and Data Sheet Clarifications](#).

Notification Status: Final

Description of Change:

Document: General editorial updates.

Added errata:

- DAC: 2.4.1. DAC Output Buffer Lifetime Drift
- NVMCTRL: 2.5.1. Flash Multi-Page Erase Can Erase Write Protected Section
- TCD: 2.8.3. Halting TCD and Waiting for SW Restart Does Not Work if Compare

Value A is 0 or Dual Slope Mode is Used

- TWI: 2.9.1. Flush Non-Functional

Added data sheet clarifications:

- Device:

3.1.1. Features

3.1.2. FUSE - Configuration and User Fuses - SYSCFG0

- AC: 3.2.1. Analog Comparator Interrupt Control

- DAC: 3.3.1. DAC Output

- Electrical Characteristics: 3.4.1. Electrical Characteristics - Memory Programming Specifications

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 25 March 2022

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Revision History:

March 28, 2022: Re-issued Errata and Datasheet Clarification Documents to correct the affected CPNs.

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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