

PRODUCT / PROCESS CHANGE NOTIFICATION

1. PCN basic data

1.1 Company		STMicroelectronics International N.V
1.2 PCN No.	MDG/18/11255	
1.3 Title of PCN	STM32L4R/S products improvements	
1.4 Product Category	STM32L4R/S products	
1.5 Issue date	2018-12-19	

2. PCN Team

2.1 Contact supplier	
2.1.1 Name	MARSHALL DAVE
2.1.2 Phone	
2.1.3 Email	dave.marshall@st.com
2.2 Change responsibility	
2.2.1 Product Manager	Ricardo Antonio DE SA EARP
2.1.2 Marketing Manager	Veronique BARLATIER
2.1.3 Quality Manager	Pascal NARCHE

3. Change

3.1 Category	3.2 Type of change	3.3 Manufacturing Location
General Product & Design	Die redesign : Mask or mask set change with new die design like metallization (specifically chip frontside) or bug fix	TSMC T14F Taiwan

4. Description of change

	Old	New
4.1 Description	Limitation described in errata-sheet(ES0393) : - 2.2.9 LTDC and DSI not functional with Stop 2 - 2.2.10 Regulator startup failure at low VDD - 2.8.4 Spurious temperature measurement due to spike noise	To fix the issues on cut1.4 of the STM32L4R/S #1 we have modified the design to ensure that the FIFOs are properly reset by the power controller when waking-up from STOP2 mode. #2 the regulator control block has been modified to avoid any start-up issue at power on. #3 Internal reference has been isolated from the source of noise.
4.2 Anticipated Impact on form,fit, function, quality, reliability or processability?	Function: improvements are indicated in errata sheet.	

5. Reason / motivation for change

5.1 Motivation	The strategy of ST MCD Division is to increase the robustness and improve performances, quality and functionality of our products. This is achieved by introducing new die revision for STM32L4R/S products.
5.2 Customer Benefit	QUALITY IMPROVEMENT

6. Marking of parts / traceability of change

6.1 Description	The die revision changes from "Y" to "W". It is marked on packages of the part.
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7. Timing / schedule

7.1 Date of qualification results	2019-02-28
7.2 Intended start of delivery	2019-03-28
7.3 Qualification sample available?	Upon Request

8. Qualification / Validation

8.1 Description	11255 MDG-MCD-RERMCD1603 - PCN11255 - STM32L4R-S product improvements - die 470XXXW.pdf		
8.2 Qualification report and qualification results	Available (see attachment)	Issue Date	2018-12-19

9. Attachments (additional documentations)			
11255 Public product.pdf 11255 MDG-MCD-RERMCD1603 - PCN11255 - STM32L4R-S product improvements - die 470XXXW.pdf 11255 PCN11255 Additional information.pdf			

10. Affected parts		
10. 1 Current		10.2 New (if applicable)
10.1.1 Customer Part No	10.1.2 Supplier Part No	10.1.2 Supplier Part No
	STM32L4R5AGI6	
	STM32L4R5AII6	
	STM32L4R5QGI6	
	STM32L4R5QII6	
	STM32L4R5VGT6	
	STM32L4R5VIT6	
	STM32L4R5ZGT6	
	STM32L4R5ZIT6	
	STM32L4R5ZIT6P	
	STM32L4R7AII6	
	STM32L4R7VIT6	
	STM32L4R7ZIT6	
	STM32L4R9AGI6	
	STM32L4R9AII6	
	STM32L4R9VGT6	
	STM32L4R9VIT6	
	STM32L4R9ZGJ6	
	STM32L4R9ZGT6	
	STM32L4R9ZIJ6	
	STM32L4R9ZIT6	
	STM32L4R9ZIY6TR	
	STM32L4S5AII6	
	STM32L4S5QII6	
	STM32L4S5VIT6	
	STM32L4S5ZIT6	
	STM32L4S7AII6	
	STM32L4S7VIT6	
	STM32L4S7ZIT6	
	STM32L4S9AII6	
	STM32L4S9VIT6	
	STM32L4S9ZIJ6	
	STM32L4S9ZIT6	
	STM32L4S9ZIY6TR	

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