Product Change Notification - SYST-20LKZW430   (Printer Friendly)

Date:	21 Sep 2016
Product Category:	32-bit PIC Microcontrollers; 8-bit Microcontrollers
Affected CPNs:	📄 📊
Notification subject:	Data Sheet - PIC32MX5XX/6XX/7XX Family Data Sheet Data Sheet Document Revision
Notification text:	SYST-20LKZW430

Microchip has released a new DeviceDoc for the PIC32MX5XX/6XX/7XX Family Data Sheet of devices. If you are using one of these devices please read the document located at PIC32MX5XX/6XX/7XX Family Data Sheet.

Notification Status: Final

Description of Change:

1) Updated Communication Interfaces for LIN support to 2.1. and updated Qualification and Class B Support to AEC-Q100 REVH. 2.0 in the Guidelines for Getting Started with 32-bit MCUs" at "32-bit Microcontrollers (up to 512 KB Flash and 128 KB SRAM) with Graphics Interface, USB, CAN, and Ethernet"
2) Updated the Recommended Minimum Connection diagram and example of MCLR Pin Connections diagram and added "EMI/EMC/EFT (IEC 61000-4-4 and IEC 61000-4-2) Suppression Considerations" in section 2.0 -Guidelines for Getting Started with 32-bit MCUs
3) Added the SFR Memory Map in in section 4.0 Memory Organization
4) Updated the UART interrupt sourcesin the Interrupt IRQ, Vector, and Bit location table in in section 7.0 - Interrupt Controller
5) Updated the bit value definitions for the TUN<5:0> bits in the OCSTUN register in section 8.0 -Oscillator Configuration
6) Relocated the content in this chapter from the Special Features chapter to its own chapter in section 15.0 - Watchdog Timer (WDT)
7) Combined register map tables in section 18.0 -Serial Peripheral Interface (SPI)
8) Combined register map tables and updated PMADDR register in section 19.0 - Inter-Integrated Circuit (I2C).
9) Updated the bit value definitions for the ADRMUX<1:0> and CSF<1:0> bits in the PMCON register in section 21.0 -Parallel Master Port (PMP)
10) Removed the duplicate bit value definition for '010' in the DEVCFG2 register ,note 1 was added to the Programming, Debugging, and Trace Ports block diagram, DDPCON register was relocated and updated the Device ID, Revision, and Configuration Summary in section 29.0 -Special Features
11) Updated the Note 4 in the Operating Current specification, Note 3 in the Idle Current specification, Note 6 references in the Power-Down Current specification, The Program Memory parameters, D135, D136, and D137, and Note 4, Voltage Reference Specifications, EJTAG Timing Characteristics, The maximum value for parameters ET13 and ET14 in the Ethernet Module Specifications and Parameter PM7 (TDHOLD) . Also added parameter DO50 (Cosco) to the Capacitive Loading, requirements on Output Pins in section 32.0

-Electrical Characteristics.
12) Packaging diagrams were updated in 34.0 - Packaging Information
13) The Speed and Program Memory Size were updated and Note 1 was added in Product Identification System

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 21 Sep 2016

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):   PIC32MX5XX/6XX/7XX Family Data Sheet
Please contact your local Microchip sales office with questions or concerns regarding this notification.

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